



FPGA Multiplexer Resource Usage

Summary

This quick reference provides detailed information about resource usage of all pre-synthesized Multiplexer cores.

Core Reference
CR0137 (v1.11) December 19, 2008

Multiplexer

The available Multiplexer cores are listed as follows:

- [1-Bit Multiplexers](#)
- [2-Bits Multiplexers](#)
- [3-Bits Multiplexers](#)
- [4-Bits Multiplexers](#)
- [5-Bits Multiplexers](#)
- [6-Bits Multiplexers](#)
- [7-Bits Multiplexers](#)
- [8-Bits Multiplexers](#)
- [9-Bits Multiplexers](#)
- [10-Bits Multiplexers](#)
- [12-Bits Multiplexers](#)
- [16-Bits Multiplexers](#)
- [32-Bits Multiplexers](#)

1-Bit Multiplexers

The available 1-Bit Multiplexers cores are listed as follows:

M1_B2S1	M1_B2S1E	M1_B4S1	M1_B4S1_SB
M1_B4S1E	M1_B4S1E_SB	M1_B8S1	M1_B8S1_SB
M1_B8S1E	M1_B8S1E_SB	M1_B16S1	M1_B16S1_SB
M1_B16S1E	M1_B16S1E_SB	M1_S1B2	M1_S1B2E
M1_S1B4	M1_S1B4_SB	M1_S1B4E	M1_S1B4E_SB
M1_S1B8	M1_S1B8_SB	M1_S1B8E	M1_S1B8E_SB
M1_S1B16	M1_S1B16_SB	M1_S1B16E	M1_S1B16E_SB
M1_S1S2	M1_S1S2E	M1_S1S4	M1_S1S4_SB
M1_S1S4E	M1_S1S4E_SB	M1_S1S8	M1_S1S8_SB
M1_S1S8E	M1_S1S8E_SB	M1_S1S16	M1_S1S16_SB
M1_S1S16E	M1_S1S16E_SB	M1_S2S1	M1_S2S1E

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M1_S4S1	M1_S4S1_SB	M1_S4S1E	M1_S4S1E_SB
M1_S8S1	M1_S8S1_SB	M1_S8S1E	M1_S8S1E_SB
M1_S16S1	M1_S16S1_SB	M1_S16S1E	M1_S16S1E_SB

2-Bits Multiplexers

The available 2-Bits Multiplexers cores are listed as follows:

M2_B1B2	M2_B1B2E	M2_B1B4	M2_B1B4_SB
M2_B1B4E	M2_B1B4E_SB	M2_B1B8	M2_B1B8_SB
M2_B1B8E	M2_B1B8E_SB	M2_B1B16	M2_B1B16_SB
M2_B1B16E	M2_B1B16E_SB	M2_B2B1	M2_B2B1E
M2_B4B1	M2_B4B1_SB	M2_B4B1E	M2_B4B1E_SB
M2_B8B1	M2_B8B1_SB	M2_B8B1E	M2_B8B1E_SB
M2_B16B1	M2_B16B1_SB	M2_B16B1E	M2_B16B1E_SB
M2_S1S2	M2_S1S2E	M2_S1S4	M2_S1S4_SB
M2_S1S4E	M2_S1S4E_SB	M2_S1S8	M2_S1S8_SB
M2_S1S8E	M2_S1S8E_SB	M2_S2S1	M2_S2S1E
M2_S4S1	M2_S4S1_SB	M2_S4S1E	M2_S4S1E_SB
M2_S8S1	M2_S8S1_SB	M2_S8S1E	M2_S8S1E_SB

3-Bits Multiplexers

The available 3-Bits Multiplexers cores are listed as follows:

M3_B1B2	M3_B1B2E	M3_B1B4	M3_B1B4_SB
M3_B1B4E	M3_B1B4E_SB	M3_B1B8	M3_B1B8_SB
M3_B1B8E	M3_B1B8E_SB	M3_B1B16	M3_B1B16_SB
M3_B1B16E	M3_B1B16E_SB	M3_B2B1	M3_B2B1E
M3_B4B1	M3_B4B1_SB	M3_B4B1E	M3_B4B1E_SB
M3_B8B1	M3_B8B1_SB	M3_B8B1E	M3_B8B1E_SB
M3_B16B1	M3_B16B1_SB	M3_B16B1E	M3_B16B1E_SB
M3_S1S2	M3_S1S2E	M3_S1S4	M3_S1S4_SB
M3_S1S4E	M3_S1S4E_SB	M3_S2S1	M3_S2S1E
M3_S4S1	M3_S4S1_SB	M3_S4S1E	M3_S4S1E_SB

4-Bits Multiplexers

The available 4-Bits Multiplexers cores are listed as follows:

M4_B1B2	M4_B1B2E	M4_B1B4	M4_B1B4_SB
M4_B1B4E	M4_B1B4E_SB	M4_B1B8	M4_B1B8_SB
M4_B1B8E	M4_B1B8E_SB	M4_B1B16	M4_B1B16_SB
M4_B1B16E	M4_B1B16E_SB	M4_B2B1	M4_B2B1E
M4_B4B1	M4_B4B1_SB	M4_B4B1E	M4_B4B1E_SB
M4_B8B1	M4_B8B1_SB	M4_B8B1E	M4_B8B1E_SB
M4_B16B1	M4_B16B1_SB	M4_B16B1E	M4_B16B1E_SB
M4_S1S2	M4_S1S2E	M4_S1S4	M4_S1S4_SB
M4_S1S4E	M4_S1S4E_SB	M4_S2S1	M4_S2S1E
M4_S4S1	M4_S4S1_SB	M4_S4S1E	M4_S4S1E_SB

5-Bits Multiplexers

The available 5-Bits Multiplexers cores are listed as follows:

M5_B1B2	M5_B1B2E	M5_B1B4	M5_B1B4_SB
M5_B1B4E	M5_B1B4E_SB	M5_B1B8	M5_B1B8_SB
M5_B1B8E	M5_B1B8E_SB	M5_B1B16	M5_B1B16_SB
M5_B1B16E	M5_B1B16E_SB	M5_B2B1	M5_B2B1E
M5_B4B1	M5_B4B1_SB	M5_B4B1E	M5_B4B1E_SB
M5_B8B1	M5_B8B1_SB	M5_B8B1E	M5_B8B1E_SB
M5_B16B1	M5_B16B1_SB	M5_B16B1E	M5_B16B1E_SB
M5_S1S2	M5_S1S2E	M5_S2S1	M5_S2S1E

6-Bits Multiplexers

The available 6-Bits Multiplexers cores are listed as follows:

M6_B1B2	M6_B1B2E	M6_B1B4	M6_B1B4_SB
M6_B1B4E	M6_B1B4E_SB	M6_B1B8	M6_B1B8_SB
M6_B1B8E	M6_B1B8E_SB	M6_B1B16	M6_B1B16_SB
M6_B1B16E	M6_B1B16E_SB	M6_B2B1	M6_B2B1E
M6_B4B1	M6_B4B1_SB	M6_B4B1E	M6_B4B1E_SB
M6_B8B1	M6_B8B1_SB	M6_B8B1E	M6_B8B1E_SB
M6_B16B1	M6_B16B1_SB	M6_B16B1E	M6_B16B1E_SB
M6_S1S2	M6_S1S2E	M6_S2S1	M6_S2S1E

7-Bits Multiplexers

The available 7-Bits Multiplexers cores are listed as follows:

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M7_B1B2	M7_B1B2E	M7_B1B4	M7_B1B4_SB
M7_B1B4E	M7_B1B4E_SB	M7_B1B8	M7_B1B8_SB
M7_B1B8E	M7_B1B8E_SB	M7_B1B16	M7_B1B16_SB
M7_B1B16E	M7_B1B16E_SB	M7_B2B1	M7_B2B1E
M7_B4B1	M7_B4B1_SB	M7_B4B1E	M7_B4B1E_SB
M7_B8B1	M7_B8B1_SB	M7_B8B1E	M7_B8B1E_SB
M7_B16B1	M7_B16B1_SB	M7_B16B1E	M7_B16B1E_SB
M7_S1S2	M7_S1S2E	M7_S2S1	M7_S2S1E

8-Bits Multiplexers

The available 8-Bits Multiplexers cores are listed as follows:

M8_B1B2	M8_B1B2E	M8_B1B4	M8_B1B4_SB
M8_B1B4E	M8_B1B4E_SB	M8_B1B8	M8_B1B8_SB
M8_B1B8E	M8_B1B8E_SB	M8_B1B16	M8_B1B16_SB
M8_B1B16E	M8_B1B16E_SB	M8_B2B1	M8_B2B1E
M8_B4B1	M8_B4B1_SB	M8_B4B1E	M8_B4B1E_SB
M8_B8B1	M8_B8B1_SB	M8_B8B1E	M8_B8B1E_SB
M8_B16B1	M8_B16B1_SB	M8_B16B1E	M8_B16B1E_SB
M8_S1S2	M8_S1S2E	M8_S2S1	M8_S2S1E

9-Bits Multiplexers

The available 9-Bits Multiplexers cores are listed as follows:

M9_B1B2	M9_B1B2E	M9_B1B4	M9_B1B4_SB
M9_B1B4E	M9_B1B4E_SB	M9_B1B8	M9_B1B8_SB
M9_B1B8E	M9_B1B8E_SB	M9_B1B16	M9_B1B16_SB
M9_B1B16E	M9_B1B16E_SB	M9_B2B1	M9_B2B1E
M9_B4B1	M9_B4B1_SB	M9_B4B1E	M9_B4B1E_SB
M9_B8B1	M9_B8B1_SB	M9_B8B1E	M9_B8B1E_SB
M9_B16B1	M9_B16B1_SB	M9_B16B1E	M9_B16B1E_SB

10-Bits Multiplexers

The available 10-Bits Multiplexers cores are listed as follows:

M10_B1B2	M10_B1B2E	M10_B1B4	M10_B1B4_SB
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M10_B1B4E	M10_B1B4E_SB	M10_B1B8	M10_B1B8_SB
M10_B1B8E	M10_B1B8E_SB	M10_B1B16	M10_B1B16_SB
M10_B1B16E	M10_B1B16E_SB	M10_B2B1	M10_B2B1E
M10_B4B1	M10_B4B1_SB	M10_B4B1E	M10_B4B1E_SB
M10_B8B1	M10_B8B1_SB	M10_B8B1E	M10_B8B1E_SB
M10_B16B1	M10_B16B1_SB	M10_B16B1E	M10_B16B1E_SB

12-Bits Multiplexers

The available 12-Bits Multiplexers cores are listed as follows:

M12_B1B2	M12_B1B2E	M12_B1B4	M12_B1B4_SB
M12_B1B4E	M12_B1B4E_SB	M12_B1B8	M12_B1B8_SB
M12_B1B8E	M12_B1B8E_SB	M12_B1B16	M12_B1B16_SB
M12_B1B16E	M12_B1B16E_SB	M12_B2B1	M12_B2B1E
M12_B4B1	M12_B4B1_SB	M12_B4B1E	M12_B4B1E_SB
M12_B8B1	M12_B8B1_SB	M12_B8B1E	M12_B8B1E_SB
M12_B16B1	M12_B16B1_SB	M12_B16B1E	M12_B16B1E_SB

16-Bits Multiplexers

The available 16-Bits Multiplexers cores are listed as follows:

M16_B1B2	M16_B1B2E	M16_B1B4	M16_B1B4_SB
M16_B1B4E	M16_B1B4E_SB	M16_B1B8	M16_B1B8_SB
M16_B1B8E	M16_B1B8E_SB	M16_B1B16	M16_B1B16_SB
M16_B1B16E	M16_B1B16E_SB	M16_B2B1	M16_B2B1E
M16_B4B1	M16_B4B1_SB	M16_B4B1E	M16_B4B1E_SB
M16_B8B1	M16_B8B1_SB	M16_B8B1E	M16_B8B1E_SB
M16_B16B1	M16_B16B1_SB	M16_B16B1E	M16_B16B1E_SB

32-Bits Multiplexers

The available 32-Bits Multiplexers cores are listed as follows:

M32_B1B2	M32_B1B2E	M32_B1B4	M32_B1B4_SB
M32_B1B4E	M32_B1B4E_SB	M32_B1B8	M32_B1B8_SB
M32_B1B8E	M32_B1B8E_SB	M32_B1B16	M32_B1B16_SB
M32_B1B16E	M32_B1B16E_SB	M32_B2B1	M32_B2B1E
M32_B4B1	M32_B4B1_SB	M32_B4B1E	M32_B4B1E_SB

Legacy documentation
refer to the Altium Wiki for current information

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[M32_B8B1](#)

[M32_B8B1_SB](#)

[M32_B8B1E](#)

[M32_B8B1E_SB](#)

[M32_B16B1](#)

[M32_B16B1_SB](#)

[M32_B16B1E](#)

[M32_B16B1E_SB](#)

M1_B2S1

1x2-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		1	1										
Cyclone2		1	1										
Cyclone3		1	1										
Stratix		1	1										
Stratix2		1	1										
Stratix3			1										
StratixGX		1	1										
Stratix2GX			1										
Max2		1	1										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	1										
Spartan2E		2	1										
Spartan3		2	1										
Spartan3A		2	1										
Spartan3ADSP		2	1										
Spartan3AN		2	1										
Spartan3E		2	1										
Spartan3L		2	1										
Virtex		2	1										
Virtex2		2	1										
Virtex2p		2	1										
VirtexE		2	1										
Virtex4		2	1										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		1											
ProAsic3		1											
ProAsic3E		1											
Fusion		1											
EC		2	1										
ECP		2	1										
ECP2		2	1										
ECP2M		2	1										
SC		2	1										
MACHXO		2	1										
XP		2	1										
XP2		2	1										

FPGA Multiplexer Resource Usage

M1_B2S1E

1x2-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		1	1										
Cyclone2		1	1										
Cyclone3		1	1										
Stratix		1	1										
Stratix2		1	1										
Stratix3			1										
StratixGX		1	1										
Stratix2GX			1										
Max2		1	1										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	1										
Spartan2E		2	1										
Spartan3		2	1										
Spartan3A		2	1										
Spartan3ADSP		2	1										
Spartan3AN		2	1										
Spartan3E		2	1										
Spartan3L		2	1										
Virtex		2	1										
Virtex2		2	1										
Virtex2p		2	1										
VirtexE		2	1										
Virtex4		2	1										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		2	1										
ECP		2	1										
ECP2		2	1										
ECP2M		2	1										
SC		2	1										
MACHXO		2	1										
XP		2	1										
XP2		2	1										

M1_B4S1

1x4-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		1	1										
Stratix3			1										
StratixGX		2	2										
Stratix2GX			1										
Max2		2	2										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

FPGA Multiplexer Resource Usage

M1_B4S1_SB

1x4-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		1	1										
Stratix3			1										
StratixGX		2	2										
Stratix2GX			1										
Max2		2	2										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

M1_B4S1E

1x4-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		1	1										
Stratix3			1										
StratixGX		3	3										
Stratix2GX			1										
Max2		3	3										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	3										
ECP		4	3										
ECP2		4	3										
ECP2M		4	3										
SC		4	3										
MACHXO		4	3										
XP		4	3										
XP2		4	3										

FPGA Multiplexer Resource Usage

M1_B4S1E_SB

1x4-Bit Bus to 1x1-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		1	1										
Stratix3			1										
StratixGX		3	3										
Stratix2GX			1										
Max2		3	3										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	3										
ECP		4	3										
ECP2		4	3										
ECP2M		4	3										
SC		4	3										
MACHXO		4	3										
XP		4	3										
XP2		4	3										

M1_B8S1

1x8-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		3	3										
Stratix3			2										
StratixGX		5	5										
Stratix2GX			2										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			2										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_B8S1_SB

1x8-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		3	3										
Stratix3			2										
StratixGX		5	5										
Stratix2GX			2										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			2										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_B8S1E

1x8-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		4	4										
Stratix3			3										
StratixGX		5	5										
Stratix2GX			3										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5			4										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		6	5										
ECP		6	5										
ECP2		6	5										
ECP2M		6	5										
SC		6	5										
MACHXO		6	5										
XP		6	5										
XP2		6	5										

FPGA Multiplexer Resource Usage

M1_B8S1E_SB

1x8-Bit Bus to 1x1-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		4	4										
Stratix3			3										
StratixGX		5	5										
Stratix2GX			3										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5			4										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		6	5										
ECP		6	5										
ECP2		6	5										
ECP2M		6	5										
SC		6	5										
MACHXO		6	5										
XP		6	5										
XP2		6	5										

M1_B16S1

1x16-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		10	9										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		10	9										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		15											
ProAsic3E		15											
Fusion		15											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M1_B16S1_SB

1x16-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		10	9										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		10	9										
Virtex4		8	8										
Virtex5		6											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		15											
ProAsic3E		15											
Fusion		15											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M1_B16S1E

1x16-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		11	11										
Cyclone2		11	11										
Cyclone3		11	11										
Stratix		11	11										
Stratix2		5	5										
Stratix3			5										
StratixGX		11	11										
Stratix2GX			6										
Max2		11	11										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		14	13										
Spartan2E		14	13										
Spartan3		14	13										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		14	13										
Spartan3L		10	9										
Virtex		14	13										
Virtex2		14	13										
Virtex2p		14	13										
VirtexE		14	13										
Virtex4		14	13										
Virtex5			6										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

FPGA Multiplexer Resource Usage

M1_B16S1E_SB

1x16-Bit Bus to 1x1-Single Wire Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		11	11										
Cyclone2		11	11										
Cyclone3		11	11										
Stratix		11	11										
Stratix2		5	5										
Stratix3			5										
StratixGX		11	11										
Stratix2GX			6										
Max2		11	11										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		14	13										
Spartan2E		14	13										
Spartan3		14	13										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		14	13										
Spartan3L		10	9										
Virtex		14	13										
Virtex2		14	13										
Virtex2p		14	13										
VirtexE		14	13										
Virtex4		14	13										
Virtex5			6										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

M1_S1B2

1x1-Single Wire to 1x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

FPGA Multiplexer Resource Usage

M1_S1B2E

1x1-Single Wire to 1x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		3											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

M1_S1B4

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_S1B4_SB

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_S1B4E

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		7											
ProAsic3		6											
ProAsic3E		5											
Fusion		5											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_S1B4E_SB

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		7											
ProAsic3		6											
ProAsic3E		5											
Fusion		5											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_S1B8

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5			6										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M1_S1B8_SB

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M1_S1B8E

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		8	8										
Stratix3			8										
StratixGX		9	9										
Stratix2GX			8										
Max2		9	9										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		6											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		15											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

FPGA Multiplexer Resource Usage

M1_S1B8E_SB

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		8	8										
Stratix3			8										
StratixGX		9	9										
Stratix2GX			8										
Max2		9	9										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		6											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		15											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

M1_S1B16

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		14											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		22											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M1_S1B16_SB

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		22											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M1_S1B16E

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		17	17										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		27											
ProAsic3		22											
ProAsic3E		23											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M1_S1B16E_SB

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		17	17										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		27											
ProAsic3		22											
ProAsic3E		23											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M1_S1S2

1x1-Single Wire to 2x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

FPGA Multiplexer Resource Usage

M1_S1S2E

1x1-Single Wire to 2x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		3											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

M1_S1S4

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_S1S4_SB

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_S1S4E

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		7											
ProAsic3		6											
ProAsic3E		5											
Fusion		5											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_S1S4E_SB

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		7											
ProAsic3		6											
ProAsic3E		5											
Fusion		5											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_S1S8

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M1_S1S8_SB

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		10											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M1_S1S8E

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		8	8										
Stratix3			8										
StratixGX		9	9										
Stratix2GX			8										
Max2		9	9										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		6											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		15											
ProAsic3		14											
ProAsic3E		10											
Fusion		13											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

FPGA Multiplexer Resource Usage

M1_S1S8E_SB

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		8	8										
Stratix3			8										
StratixGX		9	9										
Stratix2GX			8										
Max2		9	9										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		6											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		15											
ProAsic3		14											
ProAsic3E		10											
Fusion		13											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

M1_S1S16

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		22											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M1_S1S16_SB

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		22											
Fusion		22											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M1_S1S16E

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		17	17										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		22											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		27											
ProAsic3		22											
ProAsic3E		23											
Fusion		26											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M1_S1S16E_SB

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		16										
Cyclone		18	18									
Cyclone2		18	18									
Cyclone3		18	18									
Stratix		18	18									
Stratix2		17	17									
Stratix3		16										
StratixGX		18	18									
Stratix2GX		16										
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Spartan3A		18	18									
Spartan3ADSP		18	18									
Spartan3AN		18	18									
Spartan3E		18	18									
Spartan3L		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
Virtex5		22										
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		27										
ProAsic3		22										
ProAsic3E		23										
Fusion		26										
EC		18	18									
ECP		18	18									
ECP2		18	18									
ECP2M		18	18									
SC		18	18									
MACHXO		18	18									
XP		18	18									
XP2		18	18									

M1_S2S1

2x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		1	1										
Cyclone2		1	1										
Cyclone3		1	1										
Stratix		1	1										
Stratix2		1	1										
Stratix3			1										
StratixGX		1	1										
Stratix2GX			1										
Max2		1	1										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	1										
Spartan2E		2	1										
Spartan3		2	1										
Spartan3A		2	1										
Spartan3ADSP		2	1										
Spartan3AN		2	1										
Spartan3E		2	1										
Spartan3L		2	1										
Virtex		2	1										
Virtex2		2	1										
Virtex2p		2	1										
VirtexE		2	1										
Virtex4		2	1										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		1											
ProAsic3		1											
ProAsic3E		1											
Fusion		1											
EC		2	1										
ECP		2	1										
ECP2		2	1										
ECP2M		2	1										
SC		2	1										
MACHXO		2	1										
XP		2	1										
XP2		2	1										

FPGA Multiplexer Resource Usage

M1_S2S1E

2x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		1	1										
Cyclone2		1	1										
Cyclone3		1	1										
Stratix		1	1										
Stratix2		1	1										
Stratix3			1										
StratixGX		1	1										
Stratix2GX			1										
Max2		1	1										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	1										
Spartan2E		2	1										
Spartan3		2	1										
Spartan3A		2	1										
Spartan3ADSP		2	1										
Spartan3AN		2	1										
Spartan3E		2	1										
Spartan3L		2	1										
Virtex		2	1										
Virtex2		2	1										
Virtex2p		2	1										
VirtexE		2	1										
Virtex4		2	1										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		2	1										
ECP		2	1										
ECP2		2	1										
ECP2M		2	1										
SC		2	1										
MACHXO		2	1										
XP		2	1										
XP2		2	1										

M1_S4S1

4x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		1	1										
Stratix3			1										
StratixGX		2	2										
Stratix2GX			1										
Max2		2	2										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

FPGA Multiplexer Resource Usage

M1_S4S1_SB

4x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		1	1										
Stratix3			1										
StratixGX		2	2										
Stratix2GX			1										
Max2		2	2										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		2	2										
ECP		2	2										
ECP2		2	2										
ECP2M		2	2										
SC		2	2										
MACHXO		2	2										
XP		2	2										
XP2		2	2										

M1_S4S1E

4x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		1	1										
Stratix3			1										
StratixGX		3	3										
Stratix2GX			1										
Max2		3	3										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5			2										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	3										
ECP		4	3										
ECP2		4	3										
ECP2M		4	3										
SC		4	3										
MACHXO		4	3										
XP		4	3										
XP2		4	3										

FPGA Multiplexer Resource Usage

M1_S4S1E_SB

4x1-Single Wire to 1x1-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			1										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		1	1										
Stratix3			1										
StratixGX		3	3										
Stratix2GX			1										
Max2		3	3										
Max3000a	1	1											
Max7000b	1	1											
Max7000ae	1	1											
Max7000s	1	1											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		2											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	3										
ECP		4	3										
ECP2		4	3										
ECP2M		4	3										
SC		4	3										
MACHXO		4	3										
XP		4	3										
XP2		4	3										

M1_S8S1

8x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		3	3										
Stratix3			2										
StratixGX		5	5										
Stratix2GX			2										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			2										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M1_S8S1_SB

8x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		3	3										
Stratix3			2										
StratixGX		5	5										
Stratix2GX			2										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			2										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M1_S8S1E

8x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		4	4										
Stratix3			3										
StratixGX		5	5										
Stratix2GX			3										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5			4										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		6	5										
ECP		6	5										
ECP2		6	5										
ECP2M		6	5										
SC		6	5										
MACHXO		6	5										
XP		6	5										
XP2		6	5										

FPGA Multiplexer Resource Usage

M1_S8S1E_SB

8x1-Single Wire to 1x1-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		4	4										
Stratix3			3										
StratixGX		5	5										
Stratix2GX			3										
Max2		5	5										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5		4											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		11											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		6	5										
ECP		6	5										
ECP2		6	5										
ECP2M		6	5										
SC		6	5										
MACHXO		6	5										
XP		6	5										
XP2		6	5										

M1_S16S1

16x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		10	9										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		10	9										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		15											
ProAsic3E		15											
Fusion		15											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M1_S16S1_SB

16x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		10	9										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		10	9										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		15											
ProAsic3E		15											
Fusion		15											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M1_S16S1E

16x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		11	11										
Cyclone2		11	11										
Cyclone3		11	11										
Stratix		11	11										
Stratix2		5	5										
Stratix3			5										
StratixGX		11	11										
Stratix2GX			6										
Max2		11	11										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		14	13										
Spartan2E		14	13										
Spartan3		14	13										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		14	13										
Spartan3L		10	9										
Virtex		14	13										
Virtex2		14	13										
Virtex2p		14	13										
VirtexE		14	13										
Virtex4		14	13										
Virtex5			6										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

FPGA Multiplexer Resource Usage

M1_S16S1E_SB

16x1-Single Wire to 1x1-Single Wire Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		11	11										
Cyclone2		11	11										
Cyclone3		11	11										
Stratix		11	11										
Stratix2		5	5										
Stratix3			5										
StratixGX		11	11										
Stratix2GX			6										
Max2		11	11										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		14	13										
Spartan2E		14	13										
Spartan3		14	13										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		14	13										
Spartan3L		10	9										
Virtex		14	13										
Virtex2		14	13										
Virtex2p		14	13										
VirtexE		14	13										
Virtex4		14	13										
Virtex5			6										
CoolRunner2	1												
CoolRunnerXpla3	1												
Xc9500	1												
Xc9500XL	1												
Xc9500XV	1												
ProAsicPlus		19											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		10	9										
ECP		10	9										
ECP2		10	9										
ECP2M		10	9										
SC		10	9										
MACHXO		10	9										
XP		10	9										
XP2		10	9										

M2_B1B2

1x2-Bit Bus to 2x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

FPGA Multiplexer Resource Usage

M2_B1B2E

1x2-Bit Bus to 2x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M2_B1B4

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		12											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_B1B4_SB

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		12											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_B1B4E

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		11											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_B1B4E_SB

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		11											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_B1B8

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		12											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		24											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M2_B1B8_SB

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		10											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		24											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M2_B1B8E

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M2_B1B8E_SB

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		14											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M2_B1B16

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		32	32										
Stratix3			32										
StratixGX		36	36										
Stratix2GX			32										
Max2		36	36										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		26											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		48											
ProAsic3		44											
ProAsic3E		44											
Fusion		40											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

FPGA Multiplexer Resource Usage

M2_B1B16_SB

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		32	32										
Stratix3			32										
StratixGX		36	36										
Stratix2GX			32										
Max2		36	36										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		26											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		48											
ProAsic3		44											
ProAsic3E		44											
Fusion		40											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		38	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

M2_B1B16E

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			34										
Cyclone		38	38										
Cyclone2		38	38										
Cyclone3		38	38										
Stratix		38	38										
Stratix2		34	34										
Stratix3			34										
StratixGX		38	38										
Stratix2GX			34										
Max2		38	38										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		38	38										
Spartan2E		38	38										
Spartan3		38	38										
Spartan3A		38	38										
Spartan3ADSP		38	38										
Spartan3AN		38	38										
Spartan3E		38	38										
Spartan3L		38	38										
Virtex		38	38										
Virtex2		38	38										
Virtex2p		38	38										
VirtexE		38	38										
Virtex4		38	38										
Virtex5		48											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		50											
ProAsic3		42											
ProAsic3E		44											
Fusion		44											
EC		38	38										
ECP		38	38										
ECP2		38	38										
ECP2M		38	38										
SC		38	38										
MACHXO		38	38										
XP		38	38										
XP2		38	38										

FPGA Multiplexer Resource Usage

M2_B1B16E_SB

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			34										
Cyclone		38	38										
Cyclone2		38	38										
Cyclone3		38	38										
Stratix		38	38										
Stratix2		34	34										
Stratix3			34										
StratixGX		38	38										
Stratix2GX			34										
Max2		38	38										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		38	38										
Spartan2E		38	38										
Spartan3		38	38										
Spartan3A		38	38										
Spartan3ADSP		38	38										
Spartan3AN		38	38										
Spartan3E		38	38										
Spartan3L		38	38										
Virtex		38	38										
Virtex2		38	38										
Virtex2p		38	38										
VirtexE		38	38										
Virtex4		38	38										
Virtex5		42											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		50											
ProAsic3		42											
ProAsic3E		44											
Fusion		44											
EC		38	38										
ECP		38	38										
ECP2		38	38										
ECP2M		38	38										
SC		38	38										
MACHXO		38	38										
XP		38	38										
XP2		38	38										

M2_B2B1

2x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

FPGA Multiplexer Resource Usage

M2_B2B1E

2x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

M2_B4B1

4x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		2	2										
Stratix3			2										
StratixGX		4	4										
Stratix2GX			2										
Max2		4	4										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M2_B4B1_SB

4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		2	2										
Stratix3			2										
StratixGX		4	4										
Stratix2GX			2										
Max2		4	4										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M2_B4B1E

4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		2	2										
Stratix3			2										
StratixGX		6	6										
Stratix2GX			2										
Max2		6	6										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	6										
ECP		8	6										
ECP2		8	6										
ECP2M		8	6										
SC		8	6										
MACHXO		8	6										
XP		8	6										
XP2		8	6										

FPGA Multiplexer Resource Usage

M2_B4B1E_SB

4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		2	2										
Stratix3			2										
StratixGX		6	6										
Stratix2GX			2										
Max2		6	6										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	6										
ECP		8	6										
ECP2		8	6										
ECP2M		8	6										
SC		8	6										
MACHXO		8	6										
XP		8	6										
XP2		8	6										

M2_B8B1

8x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			4										
StratixGX		10	10										
Stratix2GX			4										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		28											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_B8B1_SB

8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			4										
StratixGX		10	10										
Stratix2GX			4										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		28											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_B8B1E

8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			6										
StratixGX		10	10										
Stratix2GX			6										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		30											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		12	10										
ECP		12	10										
ECP2		12	10										
ECP2M		12	10										
SC		12	10										
MACHXO		12	10										
XP		12	10										
XP2		12	10										

FPGA Multiplexer Resource Usage

M2_B8B1E_SB

8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			6										
StratixGX		10	10										
Stratix2GX			6										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		30											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		12	10										
ECP		12	10										
ECP2		12	10										
ECP2M		12	10										
SC		12	10										
MACHXO		12	10										
XP		12	10										
XP2		12	10										

M2_B16B1

16x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		10	10										
Stratix3			10										
StratixGX		20	20										
Stratix2GX			10										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		18	18										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		18	18										
Virtex4		16	16										
Virtex5		14											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		56											
ProAsic3		30											
ProAsic3E		31											
Fusion		30											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M2_B16B1_SB

16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		10	10										
Stratix3			10										
StratixGX		20	20										
Stratix2GX			10										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		18	18										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		18	18										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		56											
ProAsic3		30											
ProAsic3E		31											
Fusion		30											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M2_B16B1E

16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		22	22										
Cyclone2		22	22										
Cyclone3		22	22										
Stratix		22	22										
Stratix2		10	10										
Stratix3			10										
StratixGX		22	22										
Stratix2GX			12										
Max2		22	22										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		24	23										
Spartan2E		24	23										
Spartan3		24	23										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		24	23										
Spartan3L		18	18										
Virtex		24	23										
Virtex2		24	23										
Virtex2p		24	23										
VirtexE		24	23										
Virtex4		24	23										
Virtex5		12											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		58											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		20	18										
ECP		20	18										
ECP2		20	18										
ECP2M		20	18										
SC		20	18										
MACHXO		20	18										
XP		20	18										
XP2		20	18										

FPGA Multiplexer Resource Usage

M2_B16B1E_SB

16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		22	22										
Cyclone2		22	22										
Cyclone3		22	22										
Stratix		22	22										
Stratix2		10	10										
Stratix3			10										
StratixGX		22	22										
Stratix2GX			12										
Max2		22	22										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		24	23										
Spartan2E		24	23										
Spartan3		24	23										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		24	23										
Spartan3L		18	18										
Virtex		24	23										
Virtex2		24	23										
Virtex2p		24	23										
VirtexE		24	23										
Virtex4		24	23										
Virtex5		12											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		58											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		20	18										
ECP		20	18										
ECP2		20	18										
ECP2M		20	18										
SC		20	18										
MACHXO		20	18										
XP		20	18										
XP2		20	18										

M2_S1S2

1x2-Single Wire to 2x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

FPGA Multiplexer Resource Usage

M2_S1S2E

1x2-Single Wire to 2x2-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		6											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M2_S1S4

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		12											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_S1S4_SB

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		12											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_S1S4E

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		11											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_S1S4E_SB

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		10											
Fusion		11											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_S1S8

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		24											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M2_S1S8_SB

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		24											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M2_S1S8E

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M2_S1S8E_SB

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		16	16										
Stratix3			16										
StratixGX		18	18										
Stratix2GX			16										
Max2		18	18										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		21											
Fusion		20											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M2_S2S1

2x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		2											
ProAsic3		2											
ProAsic3E		2											
Fusion		2											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

FPGA Multiplexer Resource Usage

M2_S2S1E

2x2-Single Wire to 1x2-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		2	2										
Cyclone2		2	2										
Cyclone3		2	2										
Stratix		2	2										
Stratix2		2	2										
Stratix3			2										
StratixGX		2	2										
Stratix2GX			2										
Max2		2	2										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		2	2										
Spartan2E		2	2										
Spartan3		2	2										
Spartan3A		2	2										
Spartan3ADSP		2	2										
Spartan3AN		2	2										
Spartan3E		2	2										
Spartan3L		2	2										
Virtex		2	2										
Virtex2		2	2										
Virtex2p		2	2										
VirtexE		2	2										
Virtex4		2	2										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		4	2										
ECP		4	2										
ECP2		4	2										
ECP2M		4	2										
SC		4	2										
MACHXO		4	2										
XP		4	2										
XP2		4	2										

M2_S4S1

4x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		2	2										
Stratix3			2										
StratixGX		4	4										
Stratix2GX			2										
Max2		4	4										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

FPGA Multiplexer Resource Usage

M2_S4S1_SB

4x2-Single Wire to 1x2-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		2	2										
Stratix3			2										
StratixGX		4	4										
Stratix2GX			2										
Max2		4	4										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		4	4										
ECP		4	4										
ECP2		4	4										
ECP2M		4	4										
SC		4	4										
MACHXO		4	4										
XP		4	4										
XP2		4	4										

M2_S4S1E

4x2-Single Wire to 1x2-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		2	2										
Stratix3			2										
StratixGX		6	6										
Stratix2GX			2										
Max2		6	6										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5			4										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus			8										
ProAsic3			8										
ProAsic3E			8										
Fusion			8										
EC		8	6										
ECP		8	6										
ECP2		8	6										
ECP2M		8	6										
SC		8	6										
MACHXO		8	6										
XP		8	6										
XP2		8	6										

FPGA Multiplexer Resource Usage

M2_S4S1E_SB

4x2-Single Wire to 1x2-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			2										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		2	2										
Stratix3			2										
StratixGX		6	6										
Stratix2GX			2										
Max2		6	6										
Max3000a	2	2											
Max7000b	2	2											
Max7000ae	2	2											
Max7000s	2	2											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	6										
ECP		8	6										
ECP2		8	6										
ECP2M		8	6										
SC		8	6										
MACHXO		8	6										
XP		8	6										
XP2		8	6										

M2_S8S1

8x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			4										
StratixGX		10	10										
Stratix2GX			4										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		28											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M2_S8S1_SB

8x2-Single Wire to 1x2-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			4										
StratixGX		10	10										
Stratix2GX			4										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		4											
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		28											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M2_S8S1E

8x2-Single Wire to 1x2-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			6										
StratixGX		10	10										
Stratix2GX			6										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		30											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		12	10										
ECP		12	10										
ECP2		12	10										
ECP2M		12	10										
SC		12	10										
MACHXO		12	10										
XP		12	10										
XP2		12	10										

FPGA Multiplexer Resource Usage

M2_S8S1E_SB

8x2-Single Wire to 1x2-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		6	6										
Stratix3			6										
StratixGX		10	10										
Stratix2GX			6										
Max2		10	10										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	2												
CoolRunnerXpla3	2												
Xc9500	2												
Xc9500XL	2												
Xc9500XV	2												
ProAsicPlus		30											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		12	10										
ECP		12	10										
ECP2		12	10										
ECP2M		12	10										
SC		12	10										
MACHXO		12	10										
XP		12	10										
XP2		12	10										

M3_B1B2

1x3-Bit Bus to 2x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

FPGA Multiplexer Resource Usage

M3_B1B2E

1x3-Bit Bus to 2x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		8											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

M3_B1B4

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			8										
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus			16										
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

FPGA Multiplexer Resource Usage

M3_B1B4_SB

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		16											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M3_B1B4E

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		10											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		15											
Fusion		15											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

FPGA Multiplexer Resource Usage

M3_B1B4E_SB

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		15											
Fusion		15											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M3_B1B8

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		20											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		34											
ProAsic3		34											
ProAsic3E		32											
Fusion		28											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M3_B1B8_SB

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		22											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		34											
ProAsic3		34											
ProAsic3E		31											
Fusion		28											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M3_B1B8E

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		26	26										
Cyclone2		26	26										
Cyclone3		26	26										
Stratix		26	26										
Stratix2		24	24										
Stratix3			24										
StratixGX		26	26										
Stratix2GX			24										
Max2		26	26										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		26	26										
Spartan2E		26	26										
Spartan3		26	26										
Spartan3A		26	26										
Spartan3ADSP		26	26										
Spartan3AN		26	26										
Spartan3E		26	26										
Spartan3L		26	26										
Virtex		26	26										
Virtex2		26	26										
Virtex2p		26	26										
VirtexE		26	26										
Virtex4		26	26										
Virtex5		18											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		36											
ProAsic3		34											
ProAsic3E		34											
Fusion		30											
EC		26	26										
ECP		26	26										
ECP2		26	26										
ECP2M		26	26										
SC		28	26										
MACHXO		26	26										
XP		26	26										
XP2		26	26										

FPGA Multiplexer Resource Usage

M3_B1B8E_SB

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		26	26										
Cyclone2		26	26										
Cyclone3		26	26										
Stratix		26	26										
Stratix2		24	24										
Stratix3			24										
StratixGX		26	26										
Stratix2GX			24										
Max2		26	26										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		26	26										
Spartan2E		26	26										
Spartan3		26	26										
Spartan3A		26	26										
Spartan3ADSP		26	26										
Spartan3AN		26	26										
Spartan3E		26	26										
Spartan3L		26	26										
Virtex		26	26										
Virtex2		26	26										
Virtex2p		26	26										
VirtexE		26	26										
Virtex4		26	26										
Virtex5		20											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		36											
ProAsic3		34											
ProAsic3E		34											
Fusion		30											
EC		26	26										
ECP		26	26										
ECP2		26	26										
ECP2M		26	26										
SC		28	26										
MACHXO		26	26										
XP		26	26										
XP2		26	26										

M3_B1B16

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		52	52										
Cyclone2		52	52										
Cyclone3		52	52										
Stratix		52	52										
Stratix2		48	48										
Stratix3			48										
StratixGX		52	52										
Stratix2GX			48										
Max2		52	52										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		52	52										
Spartan2E		52	52										
Spartan3		52	52										
Spartan3A		52	52										
Spartan3ADSP		52	52										
Spartan3AN		52	52										
Spartan3E		52	52										
Spartan3L		52	52										
Virtex		52	52										
Virtex2		52	52										
Virtex2p		52	52										
VirtexE		52	52										
Virtex4		52	52										
Virtex5		44											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		68											
ProAsic3		68											
ProAsic3E		59											
Fusion		56											
EC		52	52										
ECP		52	52										
ECP2		52	52										
ECP2M		52	52										
SC		54	52										
MACHXO		52	52										
XP		52	52										
XP2		52	52										

FPGA Multiplexer Resource Usage

M3_B1B16_SB

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		52	52										
Cyclone2		52	52										
Cyclone3		52	52										
Stratix		52	52										
Stratix2		48	48										
Stratix3			48										
StratixGX		52	52										
Stratix2GX			48										
Max2		52	52										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		52	52										
Spartan2E		52	52										
Spartan3		52	52										
Spartan3A		52	52										
Spartan3ADSP		52	52										
Spartan3AN		52	52										
Spartan3E		52	52										
Spartan3L		52	52										
Virtex		52	52										
Virtex2		52	52										
Virtex2p		52	52										
VirtexE		52	52										
Virtex4		52	52										
Virtex5		44											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		68											
ProAsic3		68											
ProAsic3E		59											
Fusion		56											
EC		52	52										
ECP		52	52										
ECP2		52	52										
ECP2M		52	52										
SC		54	52										
MACHXO		52	52										
XP		52	52										
XP2		52	52										

M3_B1B16E

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		52	52										
Cyclone2		52	52										
Cyclone3		52	52										
Stratix		52	52										
Stratix2		50	50										
Stratix3			50										
StratixGX		52	52										
Stratix2GX			50										
Max2		52	52										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		52	52										
Spartan2E		52	52										
Spartan3		52	52										
Spartan3A		52	52										
Spartan3ADSP		52	52										
Spartan3AN		52	52										
Spartan3E		52	52										
Spartan3L		52	52										
Virtex		52	52										
Virtex2		52	52										
Virtex2p		52	52										
VirtexE		52	52										
Virtex4		52	52										
Virtex5		62											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		70											
ProAsic3		56											
ProAsic3E		59											
Fusion		56											
EC		52	52										
ECP		52	52										
ECP2		52	52										
ECP2M		52	52										
SC		54	52										
MACHXO		52	52										
XP		52	52										
XP2		52	52										

FPGA Multiplexer Resource Usage

M3_B1B16E_SB

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		52	52										
Cyclone2		52	52										
Cyclone3		52	52										
Stratix		52	52										
Stratix2		50	50										
Stratix3			50										
StratixGX		52	52										
Stratix2GX			50										
Max2		52	52										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		52	52										
Spartan2E		52	52										
Spartan3		52	52										
Spartan3A		52	52										
Spartan3ADSP		52	52										
Spartan3AN		52	52										
Spartan3E		52	52										
Spartan3L		52	52										
Virtex		52	52										
Virtex2		52	52										
Virtex2p		52	52										
VirtexE		52	52										
Virtex4		52	52										
Virtex5		60											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		70											
ProAsic3		56											
ProAsic3E		59											
Fusion		56											
EC		52	52										
ECP		52	52										
ECP2		52	52										
ECP2M		52	52										
SC		54	52										
MACHXO		52	52										
XP		52	52										
XP2		52	52										

M3_B2B1

2x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		3	3										
Stratix3			3										
StratixGX		3	3										
Stratix2GX			3										
Max2		3	3										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		6	3										
ECP		6	3										
ECP2		6	3										
ECP2M		6	3										
SC		6	3										
MACHXO		6	3										
XP		6	3										
XP2		6	3										

FPGA Multiplexer Resource Usage

M3_B2B1E

2x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		3	3										
Stratix3			3										
StratixGX		3	3										
Stratix2GX			3										
Max2		3	3										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		6	3										
ECP		6	3										
ECP2		6	3										
ECP2M		6	3										
SC		6	3										
MACHXO		6	3										
XP		6	3										
XP2		6	3										

M3_B4B1

4x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		3	3										
Stratix3			3										
StratixGX		6	6										
Stratix2GX			3										
Max2		6	6										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		9											
ProAsic3		9											
ProAsic3E		9											
Fusion		9											
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

FPGA Multiplexer Resource Usage

M3_B4B1_SB

4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		3	3										
Stratix3			3										
StratixGX		6	6										
Stratix2GX			3										
Max2		6	6										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		9											
ProAsic3		9											
ProAsic3E		9											
Fusion		9											
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

M3_B4B1E

4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		3	3										
Stratix3			3										
StratixGX		9	9										
Stratix2GX			3										
Max2		9	9										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	9										
ECP		12	9										
ECP2		12	9										
ECP2M		12	9										
SC		12	9										
MACHXO		12	9										
XP		12	9										
XP2		12	9										

FPGA Multiplexer Resource Usage

M3_B4B1E_SB

4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		3	3										
Stratix3			3										
StratixGX		9	9										
Stratix2GX			3										
Max2		9	9										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5			4										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	9										
ECP		12	9										
ECP2		12	9										
ECP2M		12	9										
SC		12	9										
MACHXO		12	9										
XP		12	9										
XP2		12	9										

M3_B8B1

8x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		9	9										
Stratix3			6										
StratixGX		15	15										
Stratix2GX			6										
Max2		15	15										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		42											
ProAsic3		21											
ProAsic3E		21											
Fusion		21											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

FPGA Multiplexer Resource Usage

M3_B8B1_SB

8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		9	9										
Stratix3			6										
StratixGX		15	15										
Stratix2GX			6										
Max2		15	15										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		42											
ProAsic3		21											
ProAsic3E		21											
Fusion		21											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M3_B8B1E

8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		9	9										
Stratix3			9										
StratixGX		15	15										
Stratix2GX			9										
Max2		15	15										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		16	15										
Spartan2E		16	15										
Spartan3		16	15										
Spartan3A		16	15										
Spartan3ADSP		16	15										
Spartan3AN		16	15										
Spartan3E		16	15										
Spartan3L		16	15										
Virtex		16	15										
Virtex2		16	15										
Virtex2p		16	15										
VirtexE		16	15										
Virtex4		16	15										
Virtex5		12											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		45											
ProAsic3		24											
ProAsic3E		24											
Fusion		24											
EC		18	15										
ECP		18	15										
ECP2		18	15										
ECP2M		18	15										
SC		18	15										
MACHXO		18	15										
XP		18	15										
XP2		18	15										

FPGA Multiplexer Resource Usage

M3_B8B1E_SB

8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		9	9										
Stratix3			9										
StratixGX		15	15										
Stratix2GX			9										
Max2		15	15										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		16	15										
Spartan2E		16	15										
Spartan3		16	15										
Spartan3A		16	15										
Spartan3ADSP		16	15										
Spartan3AN		16	15										
Spartan3E		16	15										
Spartan3L		16	15										
Virtex		16	15										
Virtex2		16	15										
Virtex2p		16	15										
VirtexE		16	15										
Virtex4		16	15										
Virtex5		10											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		45											
ProAsic3		24											
ProAsic3E		24											
Fusion		24											
EC		18	15										
ECP		18	15										
ECP2		18	15										
ECP2M		18	15										
SC		18	15										
MACHXO		18	15										
XP		18	15										
XP2		18	15										

M3_B16B1

16x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		15	15										
Stratix3			15										
StratixGX		30	30										
Stratix2GX			15										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		28	27										
Spartan2E		28	27										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		28	27										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		28	27										
Virtex4		24	24										
Virtex5		22											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		87											
ProAsic3		46											
ProAsic3E		47											
Fusion		45											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M3_B16B1_SB

16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		15	15										
Stratix3			15										
StratixGX		30	30										
Stratix2GX			15										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		28	27										
Spartan2E		28	27										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		28	27										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		28	27										
Virtex4		24	24										
Virtex5		24											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		87											
ProAsic3		46											
ProAsic3E		47											
Fusion		45											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M3_B16B1E

16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		33	33										
Cyclone2		33	33										
Cyclone3		33	33										
Stratix		33	33										
Stratix2		15	15										
Stratix3			15										
StratixGX		33	33										
Stratix2GX			18										
Max2		33	33										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		34	33										
Spartan2E		34	33										
Spartan3		34	33										
Spartan3A		28	27										
Spartan3ADSP		28	27										
Spartan3AN		28	27										
Spartan3E		34	33										
Spartan3L		28	27										
Virtex		34	33										
Virtex2		34	33										
Virtex2p		34	33										
VirtexE		34	33										
Virtex4		34	33										
Virtex5		18											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		87											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		30	27										
ECP		30	27										
ECP2		30	27										
ECP2M		30	27										
SC		30	27										
MACHXO		30	27										
XP		30	27										
XP2		30	27										

FPGA Multiplexer Resource Usage

M3_B16B1E_SB

16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		33	33										
Cyclone2		33	33										
Cyclone3		33	33										
Stratix		33	33										
Stratix2		15	15										
Stratix3			15										
StratixGX		33	33										
Stratix2GX			18										
Max2		33	33										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		34	33										
Spartan2E		34	33										
Spartan3		34	33										
Spartan3A		28	27										
Spartan3ADSP		28	27										
Spartan3AN		28	27										
Spartan3E		34	33										
Spartan3L		28	27										
Virtex		34	33										
Virtex2		34	33										
Virtex2p		34	33										
VirtexE		34	33										
Virtex4		34	33										
Virtex5		18											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		87											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		30	27										
ECP		30	27										
ECP2		30	27										
ECP2M		30	27										
SC		30	27										
MACHXO		30	27										
XP		30	27										
XP2		30	27										

M3_S1S2

1x3-Single Wire to 2x3-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

FPGA Multiplexer Resource Usage

M3_S1S2E

1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		8											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

M3_S1S4

1x3-Single Wire to 4x3-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		10											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		16											
ProAsic3	12												
ProAsic3E	12												
Fusion	12												
EC	12	12											
ECP	12	12											
ECP2	12	12											
ECP2M	12	12											
SC	12	12											
MACHXO	12	12											
XP	12	12											
XP2	12	12											

FPGA Multiplexer Resource Usage

M3_S1S4_SB

1x3-Single Wire to 4x3-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		10											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		16											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M3_S1S4E

1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		15											
Fusion		15											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

FPGA Multiplexer Resource Usage

M3_S1S4E_SB

1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		15											
Fusion		15											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M3_S2S1

2x3-Single Wire to 1x3-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		3	3										
Stratix3			3										
StratixGX		3	3										
Stratix2GX			3										
Max2		3	3										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		3											
ProAsic3		3											
ProAsic3E		3											
Fusion		3											
EC		6	3										
ECP		6	3										
ECP2		6	3										
ECP2M		6	3										
SC		6	3										
MACHXO		6	3										
XP		6	3										
XP2		6	3										

FPGA Multiplexer Resource Usage

M3_S2S1E

2x3-Single Wire to 2x3-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		3	3										
Cyclone2		3	3										
Cyclone3		3	3										
Stratix		3	3										
Stratix2		3	3										
Stratix3			3										
StratixGX		3	3										
Stratix2GX			3										
Max2		3	3										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		4	3										
Spartan2E		4	3										
Spartan3		4	3										
Spartan3A		4	3										
Spartan3ADSP		4	3										
Spartan3AN		4	3										
Spartan3E		4	3										
Spartan3L		4	3										
Virtex		4	3										
Virtex2		4	3										
Virtex2p		4	3										
VirtexE		4	3										
Virtex4		4	3										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		6	3										
ECP		6	3										
ECP2		6	3										
ECP2M		6	3										
SC		6	3										
MACHXO		6	3										
XP		6	3										
XP2		6	3										

M3_S4S1

4x3-Single Wire to 1x3-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		3	3										
Stratix3			3										
StratixGX		6	6										
Stratix2GX			3										
Max2		6	6										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		6											
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		9											
ProAsic3		9											
ProAsic3E		9											
Fusion		9											
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

FPGA Multiplexer Resource Usage

M3_S4S1_SB

4x3-Single Wire to 1x3-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		3	3										
Stratix3			3										
StratixGX		6	6										
Stratix2GX			3										
Max2		6	6										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5			4										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus			9										
ProAsic3			9										
ProAsic3E			9										
Fusion			9										
EC		6	6										
ECP		6	6										
ECP2		6	6										
ECP2M		6	6										
SC		6	6										
MACHXO		6	6										
XP		6	6										
XP2		6	6										

M3_S4S1E

4x3-Single Wire to 4x3-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		3	3										
Stratix3			3										
StratixGX		9	9										
Stratix2GX			3										
Max2		9	9										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	9										
ECP		12	9										
ECP2		12	9										
ECP2M		12	9										
SC		12	9										
MACHXO		12	9										
XP		12	9										
XP2		12	9										

FPGA Multiplexer Resource Usage

M3_S4S1E_SB

4x3-Single Wire to 4x3-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			3										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		3	3										
Stratix3			3										
StratixGX		9	9										
Stratix2GX			3										
Max2		9	9										
Max3000a	3	3											
Max7000b	3	3											
Max7000ae	3	3											
Max7000s	3	3											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5			6										
CoolRunner2	3												
CoolRunnerXpla3	3												
Xc9500	3												
Xc9500XL	3												
Xc9500XV	3												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	9										
ECP		12	9										
ECP2		12	9										
ECP2M		12	9										
SC		12	9										
MACHXO		12	9										
XP		12	9										
XP2		12	9										

M4_B1B2

1x4-Bit Bus to 2x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

FPGA Multiplexer Resource Usage

M4_B1B2E

1x4-Bit Bus to 2x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		10											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M4_B1B4

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		20											
ProAsic3		16											
ProAsic3E		18											
Fusion		16											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M4_B1B4_SB

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		10											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		20											
ProAsic3		16											
ProAsic3E		18											
Fusion		16											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M4_B1B4E

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		22											
ProAsic3		20											
ProAsic3E		19											
Fusion		19											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M4_B1B4E_SB

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		16										
Cyclone		16	16									
Cyclone2		16	16									
Cyclone3		16	16									
Stratix		16	16									
Stratix2		16	16									
Stratix3			16									
StratixGX		16	16									
Stratix2GX			16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Spartan3A		16	16									
Spartan3ADSP		16	16									
Spartan3AN		16	16									
Spartan3E		16	16									
Spartan3L		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
Virtex5		16										
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										
ProAsic3		20										
ProAsic3E		19										
Fusion		19										
EC		16	16									
ECP		16	16									
ECP2		16	16									
ECP2M		16	16									
SC		16	16									
MACHXO		16	16									
XP		16	16									
XP2		16	16									

M4_B1B8

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		28											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		44											
ProAsic3		44											
ProAsic3E		38											
Fusion		38											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M4_B1B8_SB

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		28											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		44											
ProAsic3		44											
ProAsic3E		38											
Fusion		38											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M4_B1B8E

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		34	34										
Cyclone2		34	34										
Cyclone3		34	34										
Stratix		34	34										
Stratix2		32	32										
Stratix3			32										
StratixGX		34	34										
Stratix2GX			32										
Max2		34	34										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		34	34										
Spartan2E		34	34										
Spartan3		34	34										
Spartan3A		34	34										
Spartan3ADSP		34	34										
Spartan3AN		34	34										
Spartan3E		34	34										
Spartan3L		34	34										
Virtex		34	34										
Virtex2		34	34										
Virtex2p		34	34										
VirtexE		34	34										
Virtex4		34	34										
Virtex5		32											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		46											
ProAsic3		38											
ProAsic3E		40											
Fusion		40											
EC		34	34										
ECP		34	34										
ECP2		34	34										
ECP2M		34	34										
SC		36	34										
MACHXO		34	34										
XP		34	34										
XP2		34	34										

FPGA Multiplexer Resource Usage

M4_B1B8E_SB

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		32										
Cyclone		34	34									
Cyclone2		34	34									
Cyclone3		34	34									
Stratix		34	34									
Stratix2		32	32									
Stratix3			32									
StratixGX		34	34									
Stratix2GX			32									
Max2		34	34									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		34	34									
Spartan2E		34	34									
Spartan3		34	34									
Spartan3A		34	34									
Spartan3ADSP		34	34									
Spartan3AN		34	34									
Spartan3E		34	34									
Spartan3L		34	34									
Virtex		34	34									
Virtex2		34	34									
Virtex2p		34	34									
VirtexE		34	34									
Virtex4		34	34									
Virtex5		26										
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		46										
ProAsic3		38										
ProAsic3E		40										
Fusion		40										
EC		34	34									
ECP		34	34									
ECP2		34	34									
ECP2M		34	34									
SC		36	34									
MACHXO		34	34									
XP		34	34									
XP2		34	34									

M4_B1B16

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		68	68										
Cyclone2		68	68										
Cyclone3		68	68										
Stratix		68	68										
Stratix2		64	64										
Stratix3			64										
StratixGX		68	68										
Stratix2GX			64										
Max2		68	68										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		68	68										
Spartan2E		68	68										
Spartan3		68	68										
Spartan3A		68	68										
Spartan3ADSP		68	68										
Spartan3AN		68	68										
Spartan3E		68	68										
Spartan3L		68	68										
Virtex		68	68										
Virtex2		68	68										
Virtex2p		68	68										
VirtexE		68	68										
Virtex4		68	68										
Virtex5		60											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		88											
ProAsic3		72											
ProAsic3E		84											
Fusion		80											
EC		68	68										
ECP		68	68										
ECP2		68	68										
ECP2M		68	68										
SC		70	68										
MACHXO		68	68										
XP		68	68										
XP2		68	68										

FPGA Multiplexer Resource Usage

M4_B1B16_SB

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		68	68										
Cyclone2		68	68										
Cyclone3		68	68										
Stratix		68	68										
Stratix2		64	64										
Stratix3			64										
StratixGX		68	68										
Stratix2GX			64										
Max2		68	68										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		68	68										
Spartan2E		68	68										
Spartan3		68	68										
Spartan3A		68	68										
Spartan3ADSP		68	68										
Spartan3AN		68	68										
Spartan3E		68	68										
Spartan3L		68	68										
Virtex		68	68										
Virtex2		68	68										
Virtex2p		68	68										
VirtexE		68	68										
Virtex4		68	68										
Virtex5		60											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		88											
ProAsic3		72											
ProAsic3E		84											
Fusion		80											
EC		68	68										
ECP		68	68										
ECP2		68	68										
ECP2M		68	68										
SC		70	68										
MACHXO		68	68										
XP		68	68										
XP2		68	68										

M4_B1B16E

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			66										
Cyclone		68	68										
Cyclone2		68	68										
Cyclone3		68	68										
Stratix		68	68										
Stratix2		66	66										
Stratix3			68										
StratixGX		68	68										
Stratix2GX			68										
Max2		68	68										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		68	68										
Spartan2E		68	68										
Spartan3		68	68										
Spartan3A		68	68										
Spartan3ADSP		68	68										
Spartan3AN		68	68										
Spartan3E		68	68										
Spartan3L		68	68										
Virtex		68	68										
Virtex2		68	68										
Virtex2p		68	68										
VirtexE		68	68										
Virtex4		68	68										
Virtex5		106											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		90											
ProAsic3		72											
ProAsic3E		84											
Fusion		80											
EC		68	68										
ECP		68	68										
ECP2		68	68										
ECP2M		68	68										
SC		70	68										
MACHXO		68	68										
XP		68	68										
XP2		68	68										

FPGA Multiplexer Resource Usage

M4_B1B16E_SB

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			66										
Cyclone		68	68										
Cyclone2		68	68										
Cyclone3		68	68										
Stratix		68	68										
Stratix2		66	66										
Stratix3			68										
StratixGX		68	68										
Stratix2GX			68										
Max2		68	68										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		68	68										
Spartan2E		68	68										
Spartan3		68	68										
Spartan3A		68	68										
Spartan3ADSP		68	68										
Spartan3AN		68	68										
Spartan3E		68	68										
Spartan3L		68	68										
Virtex		68	68										
Virtex2		68	68										
Virtex2p		68	68										
VirtexE		68	68										
Virtex4		68	68										
Virtex5		94											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		90											
ProAsic3		72											
ProAsic3E		84											
Fusion		80											
EC		68	68										
ECP		68	68										
ECP2		68	68										
ECP2M		68	68										
SC		70	68										
MACHXO		68	68										
XP		68	68										
XP2		68	68										

M4_B2B1

2x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			6										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

FPGA Multiplexer Resource Usage

M4_B2B1E

2x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

M4_B4B1

4x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		4	4										
Stratix3			4										
StratixGX		8	8										
Stratix2GX			4										
Max2		8	8										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		20											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M4_B4B1_SB

4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		4	4										
Stratix3			4										
StratixGX		8	8										
Stratix2GX			4										
Max2		8	8										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		20											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M4_B4B1E

4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		4	4										
Stratix3			4										
StratixGX		12	12										
Stratix2GX			4										
Max2		12	12										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		24											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	12										
ECP		16	12										
ECP2		16	12										
ECP2M		16	12										
SC		16	12										
MACHXO		16	12										
XP		16	12										
XP2		16	12										

FPGA Multiplexer Resource Usage

M4_B4B1E_SB

4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		4	4										
Stratix3			4										
StratixGX		12	12										
Stratix2GX			4										
Max2		12	12										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		24											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	12										
ECP		16	12										
ECP2		16	12										
ECP2M		16	12										
SC		16	12										
MACHXO		16	12										
XP		16	12										
XP2		16	12										

M4_B8B1

8x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		12	12										
Stratix3			8										
StratixGX		20	20										
Stratix2GX			8										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		52											
ProAsic3		28											
ProAsic3E		29											
Fusion		28											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M4_B8B1_SB

8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		12	12										
Stratix3			8										
StratixGX		20	20										
Stratix2GX			8										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		52											
ProAsic3		28											
ProAsic3E		29											
Fusion		28											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M4_B8B1E

8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		12	12										
Stratix3			12										
StratixGX		20	20										
Stratix2GX			12										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		16											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		56											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		24	20										
ECP		24	20										
ECP2		24	20										
ECP2M		24	20										
SC		24	20										
MACHXO		24	20										
XP		24	20										
XP2		24	20										

FPGA Multiplexer Resource Usage

M4_B8B1E_SB

8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		12	12										
Stratix3			12										
StratixGX		20	20										
Stratix2GX			12										
Max2		20	20										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5			14										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		56											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		24	20										
ECP		24	20										
ECP2		24	20										
ECP2M		24	20										
SC		24	20										
MACHXO		24	20										
XP		24	20										
XP2		24	20										

M4_B16B1

16x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		20	20										
Stratix3			20										
StratixGX		40	40										
Stratix2GX			20										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		36	36										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		36	36										
Virtex4		32	32										
Virtex5		32											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		108											
ProAsic3		62											
ProAsic3E		63											
Fusion		60											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M4_B16B1_SB

16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		20	20										
Stratix3			20										
StratixGX		40	40										
Stratix2GX			20										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		36	36										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		36	36										
Virtex4		32	32										
Virtex5		32											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		108											
ProAsic3		62											
ProAsic3E		63											
Fusion		60											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M4_B16B1E

16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		44	44										
Cyclone2		44	44										
Cyclone3		44	44										
Stratix		44	44										
Stratix2		20	20										
Stratix3			20										
StratixGX		44	44										
Stratix2GX			24										
Max2		44	44										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		44	43										
Spartan2E		44	43										
Spartan3		44	43										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		44	43										
Spartan3L		36	36										
Virtex		44	43										
Virtex2		44	43										
Virtex2p		44	43										
VirtexE		44	43										
Virtex4		44	43										
Virtex5		24											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		108											
ProAsic3		66											
ProAsic3E		67											
Fusion		64											
EC		40	36										
ECP		40	36										
ECP2		40	36										
ECP2M		40	36										
SC		40	36										
MACHXO		40	36										
XP		40	36										
XP2		40	36										

FPGA Multiplexer Resource Usage

M4_B16B1E_SB

16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		44	44										
Cyclone2		44	44										
Cyclone3		44	44										
Stratix		44	44										
Stratix2		20	20										
Stratix3			20										
StratixGX		44	44										
Stratix2GX			24										
Max2		44	44										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		44	43										
Spartan2E		44	43										
Spartan3		44	43										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		44	43										
Spartan3L		36	36										
Virtex		44	43										
Virtex2		44	43										
Virtex2p		44	43										
VirtexE		44	43										
Virtex4		44	43										
Virtex5		24											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		108											
ProAsic3		66											
ProAsic3E		67											
Fusion		64											
EC		40	36										
ECP		40	36										
ECP2		40	36										
ECP2M		40	36										
SC		40	36										
MACHXO		40	36										
XP		40	36										
XP2		40	36										

M4_S1S2

1x4-Single Wire to 2x4-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

FPGA Multiplexer Resource Usage

M4_S1S2E

1x4-Single Wire to 2x4-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		10											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M4_S1S4

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		14											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		20											
ProAsic3		16											
ProAsic3E		18											
Fusion		16											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M4_S1S4_SB

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		16										
Cyclone		16	16									
Cyclone2		16	16									
Cyclone3		16	16									
Stratix		16	16									
Stratix2		16	16									
Stratix3			16									
StratixGX		16	16									
Stratix2GX			16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Spartan3A		16	16									
Spartan3ADSP		16	16									
Spartan3AN		16	16									
Spartan3E		16	16									
Spartan3L		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
Virtex5		14										
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		20										
ProAsic3		16										
ProAsic3E		18										
Fusion		16										
EC		16	16									
ECP		16	16									
ECP2		16	16									
ECP2M		16	16									
SC		16	16									
MACHXO		16	16									
XP		16	16									
XP2		16	16									

M4_S1S4E

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		22											
ProAsic3		20											
ProAsic3E		19											
Fusion		19											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M4_S1S4E_SB

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		16										
Cyclone		16	16									
Cyclone2		16	16									
Cyclone3		16	16									
Stratix		16	16									
Stratix2		16	16									
Stratix3			16									
StratixGX		16	16									
Stratix2GX			16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Spartan3A		16	16									
Spartan3ADSP		16	16									
Spartan3AN		16	16									
Spartan3E		16	16									
Spartan3L		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
Virtex5		16										
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										
ProAsic3		20										
ProAsic3E		19										
Fusion		19										
EC		16	16									
ECP		16	16									
ECP2		16	16									
ECP2M		16	16									
SC		16	16									
MACHXO		16	16									
XP		16	16									
XP2		16	16									

M4_S2S1

2x4-Single Wire to 1x4-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		4											
ProAsic3		4											
ProAsic3E		4											
Fusion		4											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

FPGA Multiplexer Resource Usage

M4_S2S1E

2x4-Single Wire to 1x4-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		4	4										
Cyclone2		4	4										
Cyclone3		4	4										
Stratix		4	4										
Stratix2		4	4										
Stratix3			4										
StratixGX		4	4										
Stratix2GX			4										
Max2		4	4										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		4	4										
Spartan2E		4	4										
Spartan3		4	4										
Spartan3A		4	4										
Spartan3ADSP		4	4										
Spartan3AN		4	4										
Spartan3E		4	4										
Spartan3L		4	4										
Virtex		4	4										
Virtex2		4	4										
Virtex2p		4	4										
VirtexE		4	4										
Virtex4		4	4										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		8	4										
ECP		8	4										
ECP2		8	4										
ECP2M		8	4										
SC		8	4										
MACHXO		8	4										
XP		8	4										
XP2		8	4										

M4_S4S1

4x4-Single Wire to 1x4-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		4	4										
Stratix3			4										
StratixGX		8	8										
Stratix2GX			4										
Max2		8	8										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

FPGA Multiplexer Resource Usage

M4_S4S1_SB

4x4-Single Wire to 1x4-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		4	4										
Stratix3			4										
StratixGX		8	8										
Stratix2GX			4										
Max2		8	8										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		8											
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		8	8										
ECP		8	8										
ECP2		8	8										
ECP2M		8	8										
SC		8	8										
MACHXO		8	8										
XP		8	8										
XP2		8	8										

M4_S4S1E

4x4-Single Wire to 1x4-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		4	4										
Stratix3			4										
StratixGX		12	12										
Stratix2GX			4										
Max2		12	12										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	12										
ECP		16	12										
ECP2		16	12										
ECP2M		16	12										
SC		16	12										
MACHXO		16	12										
XP		16	12										
XP2		16	12										

FPGA Multiplexer Resource Usage

M4_S4S1E_SB

4x4-Single Wire to 1x4-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			4										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		4	4										
Stratix3			4										
StratixGX		12	12										
Stratix2GX			4										
Max2		12	12										
Max3000a	4	4											
Max7000b	4	4											
Max7000ae	4	4											
Max7000s	4	4											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			8										
CoolRunner2	4												
CoolRunnerXpla3	4												
Xc9500	4												
Xc9500XL	4												
Xc9500XV	4												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	12										
ECP		16	12										
ECP2		16	12										
ECP2M		16	12										
SC		16	12										
MACHXO		16	12										
XP		16	12										
XP2		16	12										

M5_B1B2

1x5-Bit Bus to 2x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		10											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		10											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		20	10										
ECP		20	10										
ECP2		20	10										
ECP2M		20	10										
SC		20	10										
MACHXO		20	10										
XP		20	10										
XP2		20	10										

FPGA Multiplexer Resource Usage

M5_B1B2E

1x5-Bit Bus to 2x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		12											
ProAsic3	10												
ProAsic3E	10												
Fusion	10												
EC	10	10											
ECP	10	10											
ECP2	10	10											
ECP2M	10	10											
SC	10	10											
MACHXO	10	10											
XP	10	10											
XP2	10	10											

M5_B1B4

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		16											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		24											
ProAsic3		22											
ProAsic3E		22											
Fusion		20											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

FPGA Multiplexer Resource Usage

M5_B1B4_SB

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		20											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		24											
ProAsic3		22											
ProAsic3E		22											
Fusion		20											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

M5_B1B4E

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		20											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		23											
Fusion		23											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

FPGA Multiplexer Resource Usage

M5_B1B4E_SB

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		20											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		26											
ProAsic3		24											
ProAsic3E		23											
Fusion		23											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

M5_B1B8

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		34											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		52											
ProAsic3		48											
ProAsic3E		48											
Fusion		46											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

FPGA Multiplexer Resource Usage

M5_B1B8_SB

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		40											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		52											
ProAsic3		48											
ProAsic3E		48											
Fusion		46											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

M5_B1B8E

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		42	42										
Cyclone2		42	42										
Cyclone3		42	42										
Stratix		42	42										
Stratix2		40	40										
Stratix3			40										
StratixGX		42	42										
Stratix2GX			40										
Max2		42	42										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		42	42										
Spartan2E		42	42										
Spartan3		42	42										
Spartan3A		42	42										
Spartan3ADSP		42	42										
Spartan3AN		42	42										
Spartan3E		42	42										
Spartan3L		42	42										
Virtex		42	42										
Virtex2		42	42										
Virtex2p		42	42										
VirtexE		42	42										
Virtex4		42	42										
Virtex5		34											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		54											
ProAsic3		48											
ProAsic3E		48											
Fusion		48											
EC		42	42										
ECP		42	42										
ECP2		42	42										
ECP2M		42	42										
SC		44	42										
MACHXO		42	42										
XP		42	42										
XP2		80	48										

FPGA Multiplexer Resource Usage

M5_B1B8E_SB

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		42	42										
Cyclone2		42	42										
Cyclone3		42	42										
Stratix		42	42										
Stratix2		40	40										
Stratix3			40										
StratixGX		42	42										
Stratix2GX			40										
Max2		42	42										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		42	42										
Spartan2E		42	42										
Spartan3		42	42										
Spartan3A		42	42										
Spartan3ADSP		42	42										
Spartan3AN		42	42										
Spartan3E		42	42										
Spartan3L		42	42										
Virtex		42	42										
Virtex2		42	42										
Virtex2p		42	42										
VirtexE		42	42										
Virtex4		42	42										
Virtex5		36											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		54											
ProAsic3		48											
ProAsic3E		48											
Fusion		48											
EC		42	42										
ECP		42	42										
ECP2		42	42										
ECP2M		42	42										
SC		44	42										
MACHXO		42	42										
XP		42	42										
XP2		80	48										

M5_B1B16

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		84	84										
Cyclone2		84	84										
Cyclone3		84	84										
Stratix		84	84										
Stratix2		80	80										
Stratix3			80										
StratixGX		84	84										
Stratix2GX			80										
Max2		84	84										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		84	84										
Spartan2E		84	84										
Spartan3		84	84										
Spartan3A		84	84										
Spartan3ADSP		84	84										
Spartan3AN		84	84										
Spartan3E		84	84										
Spartan3L		84	84										
Virtex		84	84										
Virtex2		84	84										
Virtex2p		84	84										
VirtexE		84	84										
Virtex4		84	84										
Virtex5		80											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		104											
ProAsic3		96											
ProAsic3E		101											
Fusion		96											
EC		84	84										
ECP		84	84										
ECP2		84	84										
ECP2M		84	84										
SC		86	84										
MACHXO		84	84										
XP		84	84										
XP2		160	96										

FPGA Multiplexer Resource Usage

M5_B1B16_SB

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		84	84										
Cyclone2		84	84										
Cyclone3		84	84										
Stratix		84	84										
Stratix2		80	80										
Stratix3			80										
StratixGX		84	84										
Stratix2GX			80										
Max2		84	84										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		84	84										
Spartan2E		84	84										
Spartan3		84	84										
Spartan3A		84	84										
Spartan3ADSP		84	84										
Spartan3AN		84	84										
Spartan3E		84	84										
Spartan3L		84	84										
Virtex		84	84										
Virtex2		84	84										
Virtex2p		84	84										
VirtexE		84	84										
Virtex4		84	84										
Virtex5		74											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		104											
ProAsic3		96											
ProAsic3E		101											
Fusion		96											
EC		84	84										
ECP		84	84										
ECP2		84	84										
ECP2M		84	84										
SC		86	84										
MACHXO		84	84										
XP		84	84										
XP2		160	96										

M5_B1B16E

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			82										
Cyclone		84	84										
Cyclone2		84	84										
Cyclone3		84	84										
Stratix		84	84										
Stratix2		82	82										
Stratix3			84										
StratixGX		84	84										
Stratix2GX			84										
Max2		84	84										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		84	84										
Spartan2E		84	84										
Spartan3		84	84										
Spartan3A		84	84										
Spartan3ADSP		84	84										
Spartan3AN		84	84										
Spartan3E		84	84										
Spartan3L		84	84										
Virtex		84	84										
Virtex2		84	84										
Virtex2p		84	84										
VirtexE		84	84										
Virtex4		84	84										
Virtex5		128											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		106											
ProAsic3		96											
ProAsic3E		101											
Fusion		96											
EC		98	98										
ECP		98	98										
ECP2		84	84										
ECP2M		84	84										
SC		86	84										
MACHXO		84	84										
XP		84	84										
XP2		162	98										

FPGA Multiplexer Resource Usage

M5_B1B16E_SB

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			82										
Cyclone		84	84										
Cyclone2		84	84										
Cyclone3		84	84										
Stratix		84	84										
Stratix2		82	82										
Stratix3			84										
StratixGX		84	84										
Stratix2GX			84										
Max2		84	84										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		84	84										
Spartan2E		84	84										
Spartan3		84	84										
Spartan3A		84	84										
Spartan3ADSP		84	84										
Spartan3AN		84	84										
Spartan3E		84	84										
Spartan3L		84	84										
Virtex		84	84										
Virtex2		84	84										
Virtex2p		84	84										
VirtexE		84	84										
Virtex4		84	84										
Virtex5		144											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		106											
ProAsic3		96											
ProAsic3E		101											
Fusion		96											
EC		98	98										
ECP		98	98										
ECP2		84	84										
ECP2M		84	84										
SC		86	84										
MACHXO		84	84										
XP		84	84										
XP2		162	98										

M5_B2B1

2x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		5	5										
Stratix3			5										
StratixGX		5	5										
Stratix2GX			5										
Max2		5	5										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5		8											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		5											
ProAsic3		5											
ProAsic3E		5											
Fusion		5											
EC		10	5										
ECP		10	5										
ECP2		10	5										
ECP2M		10	5										
SC		10	5										
MACHXO		10	5										
XP		10	5										
XP2		10	5										

FPGA Multiplexer Resource Usage

M5_B2B1E

2x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		5	5										
Stratix3			5										
StratixGX		5	5										
Stratix2GX			5										
Max2		5	5										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		10											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		10	5										
ECP		10	5										
ECP2		10	5										
ECP2M		10	5										
SC		10	5										
MACHXO		10	5										
XP		10	5										
XP2		10	5										

M5_B4B1

4x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5			8										
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus			24										
ProAsic3			15										
ProAsic3E			15										
Fusion			15										
EC		10	10										
ECP		10	10										
ECP2		10	10										
ECP2M		10	10										
SC		10	10										
MACHXO		10	10										
XP		10	10										
XP2		10	10										

FPGA Multiplexer Resource Usage

M5_B4B1_SB

4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		5	5										
Stratix3			5										
StratixGX		10	10										
Stratix2GX			5										
Max2		10	10										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		24											
ProAsic3		15											
ProAsic3E		15											
Fusion		15											
EC		10	10										
ECP		10	10										
ECP2		10	10										
ECP2M		10	10										
SC		10	10										
MACHXO		10	10										
XP		10	10										
XP2		10	10										

M5_B4B1E

4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		5	5										
Stratix3			5										
StratixGX		15	15										
Stratix2GX			5										
Max2		15	15										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		16	15										
Spartan2E		16	15										
Spartan3		16	15										
Spartan3A		16	15										
Spartan3ADSP		16	15										
Spartan3AN		16	15										
Spartan3E		16	15										
Spartan3L		16	15										
Virtex		16	15										
Virtex2		16	15										
Virtex2p		16	15										
VirtexE		16	15										
Virtex4		16	15										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		29											
ProAsic3		20											
ProAsic3E		20											
Fusion		20											
EC		20	15										
ECP		20	15										
ECP2		20	15										
ECP2M		20	15										
SC		20	15										
MACHXO		20	15										
XP		20	15										
XP2		20	15										

FPGA Multiplexer Resource Usage

M5_B4B1E_SB

4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		15	15										
Cyclone2		15	15										
Cyclone3		15	15										
Stratix		15	15										
Stratix2		5	5										
Stratix3			5										
StratixGX		15	15										
Stratix2GX			5										
Max2		15	15										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		16	15										
Spartan2E		16	15										
Spartan3		16	15										
Spartan3A		16	15										
Spartan3ADSP		16	15										
Spartan3AN		16	15										
Spartan3E		16	15										
Spartan3L		16	15										
Virtex		16	15										
Virtex2		16	15										
Virtex2p		16	15										
VirtexE		16	15										
Virtex4		16	15										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		29											
ProAsic3		20											
ProAsic3E		20											
Fusion		20											
EC		20	15										
ECP		20	15										
ECP2		20	15										
ECP2M		20	15										
SC		20	15										
MACHXO		20	15										
XP		20	15										
XP2		20	15										

M5_B8B1

8x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		25	25										
Cyclone2		25	25										
Cyclone3		25	25										
Stratix		25	25										
Stratix2		15	15										
Stratix3			10										
StratixGX		25	25										
Stratix2GX			10										
Max2		25	25										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		62											
ProAsic3		36											
ProAsic3E		36											
Fusion		35											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

FPGA Multiplexer Resource Usage

M5_B8B1_SB

8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		25	25										
Cyclone2		25	25										
Cyclone3		25	25										
Stratix		25	25										
Stratix2		15	15										
Stratix3			10										
StratixGX		25	25										
Stratix2GX			10										
Max2		25	25										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		62											
ProAsic3		36											
ProAsic3E		36											
Fusion		35											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

M5_B8B1E

8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		25	25										
Cyclone2		25	25										
Cyclone3		25	25										
Stratix		25	25										
Stratix2		15	15										
Stratix3			15										
StratixGX		25	25										
Stratix2GX			15										
Max2		25	25										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		26	25										
Spartan2E		26	25										
Spartan3		26	25										
Spartan3A		26	25										
Spartan3ADSP		26	25										
Spartan3AN		26	25										
Spartan3E		26	25										
Spartan3L		26	25										
Virtex		26	25										
Virtex2		26	25										
Virtex2p		26	25										
VirtexE		26	25										
Virtex4		26	25										
Virtex5		20											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		67											
ProAsic3		41											
ProAsic3E		41											
Fusion		40											
EC		30	25										
ECP		30	25										
ECP2		30	25										
ECP2M		30	25										
SC		30	25										
MACHXO		30	25										
XP		30	25										
XP2		30	25										

FPGA Multiplexer Resource Usage

M5_B8B1E_SB

8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			15										
Cyclone		25	25										
Cyclone2		25	25										
Cyclone3		25	25										
Stratix		25	25										
Stratix2		15	15										
Stratix3			15										
StratixGX		25	25										
Stratix2GX			15										
Max2		25	25										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		26	25										
Spartan2E		26	25										
Spartan3		26	25										
Spartan3A		26	25										
Spartan3ADSP		26	25										
Spartan3AN		26	25										
Spartan3E		26	25										
Spartan3L		26	25										
Virtex		26	25										
Virtex2		26	25										
Virtex2p		26	25										
VirtexE		26	25										
Virtex4		26	25										
Virtex5		20											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		67											
ProAsic3		41											
ProAsic3E		41											
Fusion		40											
EC		30	25										
ECP		30	25										
ECP2		30	25										
ECP2M		30	25										
SC		30	25										
MACHXO		30	25										
XP		30	25										
XP2		30	25										

M5_B16B1

16x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			25										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		25	25										
Stratix3			25										
StratixGX		50	50										
Stratix2GX			25										
Max2		50	50										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		46	45										
Spartan2E		46	45										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		46	45										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		46	45										
Virtex4		40	40										
Virtex5		40											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		129											
ProAsic3		78											
ProAsic3E		80											
Fusion		75											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

FPGA Multiplexer Resource Usage

M5_B16B1_SB

16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			25										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		25	25										
Stratix3			25										
StratixGX		50	50										
Stratix2GX			25										
Max2		50	50										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		46	45										
Spartan2E		46	45										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		46	45										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		46	45										
Virtex4		40	40										
Virtex5		40											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		129											
ProAsic3		78											
ProAsic3E		80											
Fusion		75											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

M5_B16B1E

16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			25										
Cyclone		55	55										
Cyclone2		55	55										
Cyclone3		55	55										
Stratix		55	55										
Stratix2		25	25										
Stratix3			25										
StratixGX		55	55										
Stratix2GX			30										
Max2		55	55										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		54	53										
Spartan2E		54	53										
Spartan3		54	53										
Spartan3A		46	45										
Spartan3ADSP		46	45										
Spartan3AN		46	45										
Spartan3E		54	53										
Spartan3L		46	45										
Virtex		54	53										
Virtex2		54	53										
Virtex2p		54	53										
VirtexE		54	53										
Virtex4		54	53										
Virtex5		28											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		129											
ProAsic3		83											
ProAsic3E		85											
Fusion		80											
EC		50	45										
ECP		50	45										
ECP2		50	45										
ECP2M		50	45										
SC		50	45										
MACHXO		50	45										
XP		50	45										
XP2		50	45										

FPGA Multiplexer Resource Usage

M5_B16B1E_SB

16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			25										
Cyclone		55	55										
Cyclone2		55	55										
Cyclone3		55	55										
Stratix		55	55										
Stratix2		25	25										
Stratix3			25										
StratixGX		55	55										
Stratix2GX			30										
Max2		55	55										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		54	53										
Spartan2E		54	53										
Spartan3		54	53										
Spartan3A		46	45										
Spartan3ADSP		46	45										
Spartan3AN		46	45										
Spartan3E		54	53										
Spartan3L		46	45										
Virtex		54	53										
Virtex2		54	53										
Virtex2p		54	53										
VirtexE		54	53										
Virtex4		54	53										
Virtex5		30											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		129											
ProAsic3		83											
ProAsic3E		85											
Fusion		80											
EC		50	45										
ECP		50	45										
ECP2		50	45										
ECP2M		50	45										
SC		50	45										
MACHXO		50	45										
XP		50	45										
XP2		50	45										

M5_S1S2

1x5-Single Wire to 1x5-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		10											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		10											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		20	10										
ECP		20	10										
ECP2		20	10										
ECP2M		20	10										
SC		20	10										
MACHXO		20	10										
XP		20	10										
XP2		20	10										

FPGA Multiplexer Resource Usage

M5_S1S2E

1x5-Single Wire to 1x5-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		8											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		12											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		10	10										
ECP		10	10										
ECP2		10	10										
ECP2M		10	10										
SC		10	10										
MACHXO		10	10										
XP		10	10										
XP2		10	10										

M5_S2S1

2x5-Single Wire to 2x5-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		5	5										
Stratix3			5										
StratixGX		5	5										
Stratix2GX			5										
Max2		5	5										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		5											
ProAsic3		5											
ProAsic3E		5											
Fusion		5											
EC		10	5										
ECP		10	5										
ECP2		10	5										
ECP2M		10	5										
SC		10	5										
MACHXO		10	5										
XP		10	5										
XP2		10	5										

FPGA Multiplexer Resource Usage

M5_S2S1E

2x5-Single Wire to 2x5-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			5										
Cyclone		5	5										
Cyclone2		5	5										
Cyclone3		5	5										
Stratix		5	5										
Stratix2		5	5										
Stratix3			5										
StratixGX		5	5										
Stratix2GX			5										
Max2		5	5										
Max3000a	5	5											
Max7000b	5	5											
Max7000ae	5	5											
Max7000s	5	5											
Spartan2		6	5										
Spartan2E		6	5										
Spartan3		6	5										
Spartan3A		6	5										
Spartan3ADSP		6	5										
Spartan3AN		6	5										
Spartan3E		6	5										
Spartan3L		6	5										
Virtex		6	5										
Virtex2		6	5										
Virtex2p		6	5										
VirtexE		6	5										
Virtex4		6	5										
Virtex5		10											
CoolRunner2	5												
CoolRunnerXpla3	5												
Xc9500	5												
Xc9500XL	5												
Xc9500XV	5												
ProAsicPlus		10											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		10	5										
ECP		10	5										
ECP2		10	5										
ECP2M		10	5										
SC		10	5										
MACHXO		10	5										
XP		10	5										
XP2		10	5										

M6_B1B2

1x6-Bit Bus to 2x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		10											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		24	12										
ECP		24	12										
ECP2		24	12										
ECP2M		24	12										
SC		24	12										
MACHXO		24	12										
XP		24	12										
XP2		24	12										

FPGA Multiplexer Resource Usage

M6_B1B2E

1x6-Bit Bus to 2x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		14											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M6_B1B4

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		20											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		28											
ProAsic3		26											
ProAsic3E		28											
Fusion		24											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M6_B1B4_SB

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		24											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		28											
ProAsic3		26											
ProAsic3E		28											
Fusion		24											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M6_B1B4E

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		20											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		30											
ProAsic3		28											
ProAsic3E		28											
Fusion		27											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M6_B1B4E_SB

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		24										
Cyclone		24	24									
Cyclone2		24	24									
Cyclone3		24	24									
Stratix		24	24									
Stratix2		24	24									
Stratix3			24									
StratixGX		24	24									
Stratix2GX			24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Spartan3A		24	24									
Spartan3ADSP		24	24									
Spartan3AN		24	24									
Spartan3E		24	24									
Spartan3L		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
Virtex5		16										
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		30										
ProAsic3		28										
ProAsic3E		28										
Fusion		27										
EC		24	24									
ECP		24	24									
ECP2		24	24									
ECP2M		24	24									
SC		24	24									
MACHXO		24	24									
XP		24	24									
XP2		24	24									

M6_B1B8

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		48	48										
Stratix3			48										
StratixGX		48	48										
Stratix2GX			48										
Max2		48	48										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		40											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		60											
ProAsic3		56											
ProAsic3E		56											
Fusion		54											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

FPGA Multiplexer Resource Usage

M6_B1B8_SB

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		48	48										
Stratix3			48										
StratixGX		48	48										
Stratix2GX			48										
Max2		48	48										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		42											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		60											
ProAsic3		56											
ProAsic3E		56											
Fusion		54											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

M6_B1B8E

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		56	56										
Cyclone2		56	56										
Cyclone3		56	56										
Stratix		56	56										
Stratix2		48	48										
Stratix3			48										
StratixGX		56	56										
Stratix2GX			48										
Max2		56	56										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		50	50										
Spartan2E		50	50										
Spartan3		50	50										
Spartan3A		50	50										
Spartan3ADSP		50	50										
Spartan3AN		50	50										
Spartan3E		50	50										
Spartan3L		50	50										
Virtex		50	50										
Virtex2		50	50										
Virtex2p		50	50										
VirtexE		50	50										
Virtex4		50	50										
Virtex5		44											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		62											
ProAsic3		56											
ProAsic3E		56											
Fusion		56											
EC		50	50										
ECP		50	50										
ECP2		50	50										
ECP2M		50	50										
SC		52	50										
MACHXO		50	50										
XP		50	50										
XP2		96	56										

FPGA Multiplexer Resource Usage

M6_B1B8E_SB

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		56	56										
Cyclone2		56	56										
Cyclone3		56	56										
Stratix		56	56										
Stratix2		48	48										
Stratix3			48										
StratixGX		56	56										
Stratix2GX			48										
Max2		56	56										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		50	50										
Spartan2E		50	50										
Spartan3		50	50										
Spartan3A		50	50										
Spartan3ADSP		50	50										
Spartan3AN		50	50										
Spartan3E		50	50										
Spartan3L		50	50										
Virtex		50	50										
Virtex2		50	50										
Virtex2p		50	50										
VirtexE		50	50										
Virtex4		50	50										
Virtex5		38											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		62											
ProAsic3		56											
ProAsic3E		56											
Fusion		56											
EC		50	50										
ECP		50	50										
ECP2		50	50										
ECP2M		50	50										
SC		52	50										
MACHXO		50	50										
XP		50	50										
XP2		96	56										

M6_B1B16

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		112	112										
Cyclone2		112	112										
Cyclone3		112	112										
Stratix		112	112										
Stratix2		96	96										
Stratix3			96										
StratixGX		112	112										
Stratix2GX			96										
Max2		112	112										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		100	100										
Spartan2E		100	100										
Spartan3		100	100										
Spartan3A		100	100										
Spartan3ADSP		100	100										
Spartan3AN		100	100										
Spartan3E		100	100										
Spartan3L		100	100										
Virtex		100	100										
Virtex2		100	100										
Virtex2p		100	100										
VirtexE		100	100										
Virtex4		100	100										
Virtex5		96											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		120											
ProAsic3		112											
ProAsic3E		118											
Fusion		112											
EC		100	100										
ECP		100	100										
ECP2		100	100										
ECP2M		100	100										
SC		102	100										
MACHXO		102	100										
XP		102	100										
XP2		192	112										

FPGA Multiplexer Resource Usage

M6_B1B16_SB

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		112	112										
Cyclone2		112	112										
Cyclone3		112	112										
Stratix		112	112										
Stratix2		96	96										
Stratix3			96										
StratixGX		112	112										
Stratix2GX			96										
Max2		112	112										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		100	100										
Spartan2E		100	100										
Spartan3		100	100										
Spartan3A		100	100										
Spartan3ADSP		100	100										
Spartan3AN		100	100										
Spartan3E		100	100										
Spartan3L		100	100										
Virtex		100	100										
Virtex2		100	100										
Virtex2p		100	100										
VirtexE		100	100										
Virtex4		100	100										
Virtex5			88										
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		120											
ProAsic3		112											
ProAsic3E		118											
Fusion		112											
EC		100	100										
ECP		100	100										
ECP2		100	100										
ECP2M		100	100										
SC		102	100										
MACHXO		102	100										
XP		102	100										
XP2		192	112										

M6_B1B16E

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			98										
Cyclone		114	114										
Cyclone2		114	114										
Cyclone3		114	114										
Stratix		114	114										
Stratix2		98	98										
Stratix3			100										
StratixGX		114	114										
Stratix2GX			100										
Max2		114	114										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		100	100										
Spartan2E		100	100										
Spartan3		100	100										
Spartan3A		100	100										
Spartan3ADSP		100	100										
Spartan3AN		100	100										
Spartan3E		100	100										
Spartan3L		100	100										
Virtex		100	100										
Virtex2		100	100										
Virtex2p		100	100										
VirtexE		100	100										
Virtex4		100	100										
Virtex5		154											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		122											
ProAsic3		112											
ProAsic3E		118											
Fusion		112											
EC		114	114										
ECP		114	114										
ECP2		100	100										
ECP2M		100	100										
SC		102	100										
MACHXO		102	100										
XP		102	100										
XP2		194	114										

FPGA Multiplexer Resource Usage

M6_B1B16E_SB

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			98										
Cyclone		114	114										
Cyclone2		114	114										
Cyclone3		114	114										
Stratix		114	114										
Stratix2		98	98										
Stratix3			100										
StratixGX		114	114										
Stratix2GX			100										
Max2		114	114										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		100	100										
Spartan2E		100	100										
Spartan3		100	100										
Spartan3A		100	100										
Spartan3ADSP		100	100										
Spartan3AN		100	100										
Spartan3E		100	100										
Spartan3L		100	100										
Virtex		100	100										
Virtex2		100	100										
Virtex2p		100	100										
VirtexE		100	100										
Virtex4		100	100										
Virtex5		156											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		122											
ProAsic3		112											
ProAsic3E		118											
Fusion		112											
EC		114	114										
ECP		114	114										
ECP2		100	100										
ECP2M		100	100										
SC		102	100										
MACHXO		102	100										
XP		102	100										
XP2		194	114										

M6_B2B1

2x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

FPGA Multiplexer Resource Usage

M6_B2B1E

2x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

M6_B4B1

4x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		6	6										
Stratix3			6										
StratixGX		12	12										
Stratix2GX			6										
Max2		12	12										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		28											
ProAsic3		18											
ProAsic3E		18											
Fusion		18											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

FPGA Multiplexer Resource Usage

M6_B4B1_SB

4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		6	6										
Stratix3			6										
StratixGX		12	12										
Stratix2GX			6										
Max2		12	12										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		10											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		28											
ProAsic3		18											
ProAsic3E		18											
Fusion		18											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M6_B4B1E

4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		6	6										
Stratix3			6										
StratixGX		18	18										
Stratix2GX			6										
Max2		18	18										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		34											
ProAsic3		24											
ProAsic3E		24											
Fusion		24											
EC		24	18										
ECP		24	18										
ECP2		24	18										
ECP2M		24	18										
SC		24	18										
MACHXO		24	18										
XP		24	18										
XP2		24	18										

FPGA Multiplexer Resource Usage

M6_B4B1E_SB

4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		6	6										
Stratix3			6										
StratixGX		18	18										
Stratix2GX			6										
Max2		18	18										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		34											
ProAsic3		24											
ProAsic3E		24											
Fusion		24											
EC		24	18										
ECP		24	18										
ECP2		24	18										
ECP2M		24	18										
SC		24	18										
MACHXO		24	18										
XP		24	18										
XP2		24	18										

M6_B8B1

8x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		18	18										
Stratix3			12										
StratixGX		30	30										
Stratix2GX			12										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		72											
ProAsic3		43											
ProAsic3E		44											
Fusion		42											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M6_B8B1_SB

8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		18	18										
Stratix3			12										
StratixGX		30	30										
Stratix2GX			12										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		72											
ProAsic3		43											
ProAsic3E		44											
Fusion		42											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M6_B8B1E

8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		18	18										
Stratix3			18										
StratixGX		30	30										
Stratix2GX			18										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		30	30										
Spartan2E		30	30										
Spartan3		30	30										
Spartan3A		30	30										
Spartan3ADSP		30	30										
Spartan3AN		30	30										
Spartan3E		30	30										
Spartan3L		30	30										
Virtex		30	30										
Virtex2		30	30										
Virtex2p		30	30										
VirtexE		30	30										
Virtex4		30	30										
Virtex5			24										
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		78											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		36	30										
ECP		36	30										
ECP2		36	30										
ECP2M		36	30										
SC		36	30										
MACHXO		36	30										
XP		36	30										
XP2		36	30										

FPGA Multiplexer Resource Usage

M6_B8B1E_SB

8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		18	18										
Stratix3			18										
StratixGX		30	30										
Stratix2GX			18										
Max2		30	30										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		30	30										
Spartan2E		30	30										
Spartan3		30	30										
Spartan3A		30	30										
Spartan3ADSP		30	30										
Spartan3AN		30	30										
Spartan3E		30	30										
Spartan3L		30	30										
Virtex		30	30										
Virtex2		30	30										
Virtex2p		30	30										
VirtexE		30	30										
Virtex4		30	30										
Virtex5		24											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		78											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		36	30										
ECP		36	30										
ECP2		36	30										
ECP2M		36	30										
SC		36	30										
MACHXO		36	30										
XP		36	30										
XP2		36	30										

M6_B16B1

16x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		30	30										
Stratix3			30										
StratixGX		60	60										
Stratix2GX			30										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		54	54										
Spartan2E		54	54										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		54	54										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		54	54										
Virtex4		48	48										
Virtex5		48											
CoolRunner2		6											
CoolRunnerXpla3		6											
Xc9500		6											
Xc9500XL		6											
Xc9500XV		6											
ProAsicPlus		150											
ProAsic3		94											
ProAsic3E		96											
Fusion		90											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

FPGA Multiplexer Resource Usage

M6_B16B1_SB

16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		30	30										
Stratix3			30										
StratixGX		60	60										
Stratix2GX			30										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		54	54										
Spartan2E		54	54										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		54	54										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		54	54										
Virtex4		48	48										
Virtex5		46											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		150											
ProAsic3		94											
ProAsic3E		96											
Fusion		90											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

M6_B16B1E

16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		66	66										
Cyclone2		66	66										
Cyclone3		66	66										
Stratix		66	66										
Stratix2		30	30										
Stratix3			30										
StratixGX		66	66										
Stratix2GX			36										
Max2		66	66										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		62	62										
Spartan2E		62	62										
Spartan3		62	62										
Spartan3A		54	54										
Spartan3ADSP		54	54										
Spartan3AN		54	54										
Spartan3E		62	62										
Spartan3L		54	54										
Virtex		62	62										
Virtex2		62	62										
Virtex2p		62	62										
VirtexE		62	62										
Virtex4		62	62										
Virtex5		34											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		150											
ProAsic3		100											
ProAsic3E		102											
Fusion		96											
EC		60	54										
ECP		60	54										
ECP2		60	54										
ECP2M		60	54										
SC		60	54										
MACHXO		60	54										
XP		60	54										
XP2		60	54										

FPGA Multiplexer Resource Usage

M6_B16B1E_SB

16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		66	66										
Cyclone2		66	66										
Cyclone3		66	66										
Stratix		66	66										
Stratix2		30	30										
Stratix3			30										
StratixGX		66	66										
Stratix2GX			36										
Max2		66	66										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		62	62										
Spartan2E		62	62										
Spartan3		64	63										
Spartan3A		54	54										
Spartan3ADSP		54	54										
Spartan3AN		54	54										
Spartan3E		64	63										
Spartan3L		54	54										
Virtex		62	62										
Virtex2		64	63										
Virtex2p		64	63										
VirtexE		62	62										
Virtex4		64	63										
Virtex5		34											
CoolRunner2		6											
CoolRunnerXpla3		6											
Xc9500		6											
Xc9500XL		6											
Xc9500XV		6											
ProAsicPlus		150											
ProAsic3		100											
ProAsic3E		102											
Fusion		96											
EC		60	54										
ECP		60	54										
ECP2		60	54										
ECP2M		60	54										
SC		60	54										
MACHXO		60	54										
XP		60	54										
XP2		60	54										

M6_S1S2

1x6-Single Wire to 1x6-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		24	12										
ECP		24	12										
ECP2		24	12										
ECP2M		24	12										
SC		24	12										
MACHXO		24	12										
XP		24	12										
XP2		24	12										

FPGA Multiplexer Resource Usage

M6_S1S2E

1x6-Single Wire to 1x6-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		12											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		14											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	12										
ECP		12	12										
ECP2		12	12										
ECP2M		12	12										
SC		12	12										
MACHXO		12	12										
XP		12	12										
XP2		12	12										

M6_S2S1

2x6-Single Wire to 2x6-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5		12											
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		6											
ProAsic3		6											
ProAsic3E		6											
Fusion		6											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

FPGA Multiplexer Resource Usage

M6_S2S1E

2x6-Single Wire to 2x6-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			6										
Cyclone		6	6										
Cyclone2		6	6										
Cyclone3		6	6										
Stratix		6	6										
Stratix2		6	6										
Stratix3			6										
StratixGX		6	6										
Stratix2GX			6										
Max2		6	6										
Max3000a	6	6											
Max7000b	6	6											
Max7000ae	6	6											
Max7000s	6	6											
Spartan2		6	6										
Spartan2E		6	6										
Spartan3		6	6										
Spartan3A		6	6										
Spartan3ADSP		6	6										
Spartan3AN		6	6										
Spartan3E		6	6										
Spartan3L		6	6										
Virtex		6	6										
Virtex2		6	6										
Virtex2p		6	6										
VirtexE		6	6										
Virtex4		6	6										
Virtex5			8										
CoolRunner2	6												
CoolRunnerXpla3	6												
Xc9500	6												
Xc9500XL	6												
Xc9500XV	6												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		12	6										
ECP		12	6										
ECP2		12	6										
ECP2M		12	6										
SC		12	6										
MACHXO		12	6										
XP		12	6										
XP2		12	6										

M7_B1B2

1x7-Bit Bus to 2x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		14	14										
Stratix3			14										
StratixGX		14	14										
Stratix2GX			14										
Max2		14	14										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		14											
CoolRunner2	14												
CoolRunnerXpla3	14												
Xc9500	14												
Xc9500XL	14												
Xc9500XV	14												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		15											
Fusion		14											
EC		28	14										
ECP		28	14										
ECP2		28	14										
ECP2M		28	14										
SC		28	14										
MACHXO		28	14										
XP		28	14										
XP2		28	14										

FPGA Multiplexer Resource Usage

M7_B1B2E

1x7-Bit Bus to 2x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		14	14										
Stratix3			14										
StratixGX		14	14										
Stratix2GX			14										
Max2		14	14										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		14											
CoolRunner2	14												
CoolRunnerXpla3	14												
Xc9500	14												
Xc9500XL	14												
Xc9500XV	14												
ProAsicPlus		16											
ProAsic3		14											
ProAsic3E		16											
Fusion		14											
EC		14	14										
ECP		14	14										
ECP2		14	14										
ECP2M		14	14										
SC		14	14										
MACHXO		14	14										
XP		14	14										
XP2		14	14										

M7_B1B4

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			28										
Cyclone		28	28										
Cyclone2		28	28										
Cyclone3		28	28										
Stratix		28	28										
Stratix2		28	28										
Stratix3			28										
StratixGX		28	28										
Stratix2GX			28										
Max2		28	28										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		26											
CoolRunner2	28												
CoolRunnerXpla3	28												
Xc9500	28												
Xc9500XL	28												
Xc9500XV	28												
ProAsicPlus		32											
ProAsic3		30											
ProAsic3E		32											
Fusion		28											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

FPGA Multiplexer Resource Usage

M7_B1B4_SB

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			28										
Cyclone		28	28										
Cyclone2		28	28										
Cyclone3		28	28										
Stratix		28	28										
Stratix2		28	28										
Stratix3			28										
StratixGX		28	28										
Stratix2GX			28										
Max2		28	28										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		20											
CoolRunner2	28												
CoolRunnerXpla3	28												
Xc9500	28												
Xc9500XL	28												
Xc9500XV	28												
ProAsicPlus		32											
ProAsic3		30											
ProAsic3E		32											
Fusion		28											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

M7_B1B4E

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			28										
Cyclone		28	28										
Cyclone2		28	28										
Cyclone3		28	28										
Stratix		28	28										
Stratix2		28	28										
Stratix3			28										
StratixGX		28	28										
Stratix2GX			28										
Max2		28	28										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		28											
CoolRunner2	28												
CoolRunnerXpla3	28												
Xc9500	28												
Xc9500XL	28												
Xc9500XV	28												
ProAsicPlus		34											
ProAsic3		31											
ProAsic3E		34											
Fusion		32											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

FPGA Multiplexer Resource Usage

M7_B1B4E_SB

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			28										
Cyclone		28	28										
Cyclone2		28	28										
Cyclone3		28	28										
Stratix		28	28										
Stratix2		28	28										
Stratix3			28										
StratixGX		28	28										
Stratix2GX			28										
Max2		28	28										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		26											
CoolRunner2	28												
CoolRunnerXpla3	28												
Xc9500	28												
Xc9500XL	28												
Xc9500XV	28												
ProAsicPlus		34											
ProAsic3		31											
ProAsic3E		34											
Fusion		32											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

M7_B1B8

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			56										
Cyclone		56	56										
Cyclone2		56	56										
Cyclone3		56	56										
Stratix		56	56										
Stratix2		56	56										
Stratix3			56										
StratixGX		56	56										
Stratix2GX			56										
Max2		56	56										
Max3000a	56	56											
Max7000b	56	56											
Max7000ae	56	56											
Max7000s	56	56											
Spartan2		56	56										
Spartan2E		56	56										
Spartan3		56	56										
Spartan3A		56	56										
Spartan3ADSP		56	56										
Spartan3AN		56	56										
Spartan3E		56	56										
Spartan3L		56	56										
Virtex		56	56										
Virtex2		56	56										
Virtex2p		56	56										
VirtexE		56	56										
Virtex4		56	56										
Virtex5		56											
CoolRunner2	56												
CoolRunnerXpla3	56												
Xc9500	56												
Xc9500XL	56												
Xc9500XV	56												
ProAsicPlus		68											
ProAsic3		60											
ProAsic3E		64											
Fusion		64											
EC		56	56										
ECP		56	56										
ECP2		56	56										
ECP2M		56	56										
SC		56	56										
MACHXO		56	56										
XP		56	56										
XP2		56	56										

FPGA Multiplexer Resource Usage

M7_B1B8_SB

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			56										
Cyclone		56	56										
Cyclone2		56	56										
Cyclone3		56	56										
Stratix		56	56										
Stratix2		56	56										
Stratix3			56										
StratixGX		56	56										
Stratix2GX			56										
Max2		56	56										
Max3000a	56	56											
Max7000b	56	56											
Max7000ae	56	56											
Max7000s	56	56											
Spartan2		56	56										
Spartan2E		56	56										
Spartan3		56	56										
Spartan3A		56	56										
Spartan3ADSP		56	56										
Spartan3AN		56	56										
Spartan3E		56	56										
Spartan3L		56	56										
Virtex		56	56										
Virtex2		56	56										
Virtex2p		56	56										
VirtexE		56	56										
Virtex4		56	56										
Virtex5		52											
CoolRunner2	56												
CoolRunnerXpla3	56												
Xc9500	56												
Xc9500XL	56												
Xc9500XV	56												
ProAsicPlus		68											
ProAsic3		60											
ProAsic3E		64											
Fusion		64											
EC		56	56										
ECP		56	56										
ECP2		56	56										
ECP2M		56	56										
SC		56	56										
MACHXO		56	56										
XP		56	56										
XP2		56	56										

M7_B1B8E

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			56										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		56	56										
Stratix3			56										
StratixGX		64	64										
Stratix2GX			56										
Max2		64	64										
Max3000a	56	56											
Max7000b	56	56											
Max7000ae	56	56											
Max7000s	56	56											
Spartan2		58	58										
Spartan2E		58	58										
Spartan3		58	58										
Spartan3A		58	58										
Spartan3ADSP		58	58										
Spartan3AN		58	58										
Spartan3E		58	58										
Spartan3L		58	58										
Virtex		58	58										
Virtex2		58	58										
Virtex2p		58	58										
VirtexE		58	58										
Virtex4		58	58										
Virtex5		54											
CoolRunner2	56												
CoolRunnerXpla3	56												
Xc9500	56												
Xc9500XL	56												
Xc9500XV	56												
ProAsicPlus		70											
ProAsic3		64											
ProAsic3E		70											
Fusion		70											
EC		58	58										
ECP		58	58										
ECP2		58	58										
ECP2M		58	58										
SC		60	58										
MACHXO		58	58										
XP		58	58										
XP2		112	64										

FPGA Multiplexer Resource Usage

M7_B1B8E_SB

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			56										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		56	56										
Stratix3			56										
StratixGX		64	64										
Stratix2GX			56										
Max2		64	64										
Max3000a	56	56											
Max7000b	56	56											
Max7000ae	56	56											
Max7000s	56	56											
Spartan2		58	58										
Spartan2E		58	58										
Spartan3		58	58										
Spartan3A		58	58										
Spartan3ADSP		58	58										
Spartan3AN		58	58										
Spartan3E		58	58										
Spartan3L		58	58										
Virtex		58	58										
Virtex2		58	58										
Virtex2p		58	58										
VirtexE		58	58										
Virtex4		58	58										
Virtex5		50											
CoolRunner2	56												
CoolRunnerXpla3	56												
Xc9500	56												
Xc9500XL	56												
Xc9500XV	56												
ProAsicPlus		70											
ProAsic3		64											
ProAsic3E		70											
Fusion		70											
EC		58	58										
ECP		58	58										
ECP2		58	58										
ECP2M		58	58										
SC		60	58										
MACHXO		58	58										
XP		58	58										
XP2		112	64										

M7_B1B16

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			112										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		112	112										
Stratix3			112										
StratixGX		128	128										
Stratix2GX			112										
Max2		128	128										
Max3000a	112	112											
Max7000b	112	112											
Max7000ae	112	112											
Max7000s	112	112											
Spartan2		120	120										
Spartan2E		120	120										
Spartan3		120	120										
Spartan3A		118	118										
Spartan3ADSP		116	116										
Spartan3AN		118	117										
Spartan3E		120	120										
Spartan3L		120	120										
Virtex		120	120										
Virtex2		120	120										
Virtex2p		120	120										
VirtexE		120	120										
Virtex4		120	120										
Virtex5		110											
CoolRunner2	112												
CoolRunnerXpla3	112												
Xc9500	112												
Xc9500XL	112												
Xc9500XV	112												
ProAsicPlus		136											
ProAsic3		128											
ProAsic3E		143											
Fusion													
EC		116	116										
ECP		116	116										
ECP2		116	116										
ECP2M		116	116										
SC		118	116										
MACHXO		124	118										
XP		118	116										
XP2		224	128										

FPGA Multiplexer Resource Usage

M7_B1B16_SB

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			112										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		112	112										
Stratix3			112										
StratixGX		128	128										
Stratix2GX			112										
Max2		128	128										
Max3000a	112	112											
Max7000b	112	112											
Max7000ae	112	112											
Max7000s	112	112											
Spartan2		120	120										
Spartan2E		120	120										
Spartan3		120	120										
Spartan3A		120	118										
Spartan3ADSP		116	116										
Spartan3AN		118	117										
Spartan3E		120	120										
Spartan3L		120	120										
Virtex		120	120										
Virtex2		120	120										
Virtex2p		120	120										
VirtexE		120	120										
Virtex4		120	120										
Virtex5		108											
CoolRunner2	112												
CoolRunnerXpla3	112												
Xc9500	112												
Xc9500XL	112												
Xc9500XV	112												
ProAsicPlus		136											
ProAsic3		128											
ProAsic3E		143											
Fusion													
EC		116	116										
ECP		116	116										
ECP2		116	116										
ECP2M		116	116										
SC		118	116										
MACHXO		130	118										
XP		118	116										
XP2		224	128										

M7_B1B16E

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			114										
Cyclone		130	130										
Cyclone2		130	130										
Cyclone3		130	130										
Stratix		130	130										
Stratix2		114	114										
Stratix3			116										
StratixGX		130	130										
Stratix2GX			116										
Max2		130	130										
Max3000a	112	112											
Max7000b	112	112											
Max7000ae	112	112											
Max7000s	112	112											
Spartan2		120	120										
Spartan2E		120	120										
Spartan3		120	120										
Spartan3A		116	116										
Spartan3ADSP		116	116										
Spartan3AN		116	116										
Spartan3E		122	120										
Spartan3L		120	120										
Virtex		120	120										
Virtex2		120	120										
Virtex2p		120	120										
VirtexE		120	120										
Virtex4		120	120										
Virtex5		206											
CoolRunner2	112												
CoolRunnerXpla3	112												
Xc9500	112												
Xc9500XL	112												
Xc9500XV	112												
ProAsicPlus		138											
ProAsic3		128											
ProAsic3E		143											
Fusion													
EC		130	130										
ECP		130	130										
ECP2		116	116										
ECP2M		116	116										
SC		118	116										
MACHXO		118	116										
XP		118	116										
XP2		226	130										

FPGA Multiplexer Resource Usage

M7_B1B16E_SB

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			114										
Cyclone		130	130										
Cyclone2		130	130										
Cyclone3		130	130										
Stratix		130	130										
Stratix2		114	114										
Stratix3			116										
StratixGX		130	130										
Stratix2GX			116										
Max2		130	130										
Max3000a	112	112											
Max7000b	112	112											
Max7000ae	112	112											
Max7000s	112	112											
Spartan2		122	120										
Spartan2E		122	120										
Spartan3		122	120										
Spartan3A		116	116										
Spartan3ADSP		116	116										
Spartan3AN		116	116										
Spartan3E		120	120										
Spartan3L		120	120										
Virtex		122	120										
Virtex2		122	120										
Virtex2p		122	120										
VirtexE		122	120										
Virtex4		122	120										
Virtex5		200											
CoolRunner2	112												
CoolRunnerXpla3	112												
Xc9500	112												
Xc9500XL	112												
Xc9500XV	112												
ProAsicPlus		138											
ProAsic3		128											
ProAsic3E		143											
Fusion													
EC		130	130										
ECP		130	130										
ECP2		116	116										
ECP2M		116	116										
SC		118	116										
MACHXO		118	116										
XP		118	116										
XP2		226	130										

M7_B2B1

2x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		7	7										
Cyclone2		7	7										
Cyclone3		7	7										
Stratix		7	7										
Stratix2		7	7										
Stratix3			7										
StratixGX		7	7										
Stratix2GX			7										
Max2		7	7										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		8	7										
Spartan2E		8	7										
Spartan3		8	7										
Spartan3A		8	7										
Spartan3ADSP		8	7										
Spartan3AN		8	7										
Spartan3E		8	7										
Spartan3L		8	7										
Virtex		8	7										
Virtex2		8	7										
Virtex2p		8	7										
VirtexE		8	7										
Virtex4		8	7										
Virtex5		10											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		7											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		14	7										
ECP		14	7										
ECP2		14	7										
ECP2M		14	7										
SC		14	7										
MACHXO		14	7										
XP		14	7										
XP2		14	7										

FPGA Multiplexer Resource Usage

M7_B2B1E

2x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		7	7										
Cyclone2		7	7										
Cyclone3		7	7										
Stratix		7	7										
Stratix2		7	7										
Stratix3			7										
StratixGX		7	7										
Stratix2GX			7										
Max2		7	7										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		8	7										
Spartan2E		8	7										
Spartan3		8	7										
Spartan3A		8	7										
Spartan3ADSP		8	7										
Spartan3AN		8	7										
Spartan3E		8	7										
Spartan3L		8	7										
Virtex		8	7										
Virtex2		8	7										
Virtex2p		8	7										
VirtexE		8	7										
Virtex4		8	7										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		14	7										
ECP		14	7										
ECP2		14	7										
ECP2M		14	7										
SC		14	7										
MACHXO		14	7										
XP		14	7										
XP2		14	7										

M7_B4B1

4x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		7	7										
Stratix3			7										
StratixGX		14	14										
Stratix2GX			7										
Max2		14	14										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		32											
ProAsic3		21											
ProAsic3E		22											
Fusion		21											
EC		14	14										
ECP		14	14										
ECP2		14	14										
ECP2M		14	14										
SC		14	14										
MACHXO		14	14										
XP		14	14										
XP2		14	14										

FPGA Multiplexer Resource Usage

M7_B4B1_SB

4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		7	7										
Stratix3			7										
StratixGX		14	14										
Stratix2GX			7										
Max2		14	14										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		32											
ProAsic3		21											
ProAsic3E		22											
Fusion		21											
EC		14	14										
ECP		14	14										
ECP2		14	14										
ECP2M		14	14										
SC		14	14										
MACHXO		14	14										
XP		14	14										
XP2		14	14										

M7_B4B1E

4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		21	21										
Cyclone2		21	21										
Cyclone3		21	21										
Stratix		21	21										
Stratix2		7	7										
Stratix3			7										
StratixGX		21	21										
Stratix2GX			7										
Max2		21	21										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		22	21										
Spartan2E		22	21										
Spartan3		22	21										
Spartan3A		22	21										
Spartan3ADSP		22	21										
Spartan3AN		22	21										
Spartan3E		22	21										
Spartan3L		22	21										
Virtex		22	21										
Virtex2		22	21										
Virtex2p		22	21										
VirtexE		22	21										
Virtex4		22	21										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		39											
ProAsic3		28											
ProAsic3E		29											
Fusion		28											
EC		28	21										
ECP		28	21										
ECP2		28	21										
ECP2M		28	21										
SC		28	21										
MACHXO		28	21										
XP		28	21										
XP2		28	21										

FPGA Multiplexer Resource Usage

M7_B4B1E_SB

4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		21	21										
Cyclone2		21	21										
Cyclone3		21	21										
Stratix		21	21										
Stratix2		7	7										
Stratix3			7										
StratixGX		21	21										
Stratix2GX			7										
Max2		21	21										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		22	21										
Spartan2E		22	21										
Spartan3		22	21										
Spartan3A		22	21										
Spartan3ADSP		22	21										
Spartan3AN		22	21										
Spartan3E		22	21										
Spartan3L		22	21										
Virtex		22	21										
Virtex2		22	21										
Virtex2p		22	21										
VirtexE		22	21										
Virtex4		22	21										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		39											
ProAsic3		28											
ProAsic3E		29											
Fusion		28											
EC		28	21										
ECP		28	21										
ECP2		28	21										
ECP2M		28	21										
SC		28	21										
MACHXO		28	21										
XP		28	21										
XP2		28	21										

M7_B8B1

8x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		35	35										
Cyclone2		35	35										
Cyclone3		35	35										
Stratix		35	35										
Stratix2		21	21										
Stratix3			14										
StratixGX		35	35										
Stratix2GX			14										
Max2		35	35										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		82											
ProAsic3		50											
ProAsic3E		52											
Fusion		49											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

FPGA Multiplexer Resource Usage

M7_B8B1_SB

8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		35	35										
Cyclone2		35	35										
Cyclone3		35	35										
Stratix		35	35										
Stratix2		21	21										
Stratix3			14										
StratixGX		35	35										
Stratix2GX			14										
Max2		35	35										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		28	28										
Spartan2E		28	28										
Spartan3		28	28										
Spartan3A		28	28										
Spartan3ADSP		28	28										
Spartan3AN		28	28										
Spartan3E		28	28										
Spartan3L		28	28										
Virtex		28	28										
Virtex2		28	28										
Virtex2p		28	28										
VirtexE		28	28										
Virtex4		28	28										
Virtex5		14											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		82											
ProAsic3		50											
ProAsic3E		52											
Fusion		49											
EC		28	28										
ECP		28	28										
ECP2		28	28										
ECP2M		28	28										
SC		28	28										
MACHXO		28	28										
XP		28	28										
XP2		28	28										

M7_B8B1E

8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			21										
Cyclone		35	35										
Cyclone2		35	35										
Cyclone3		35	35										
Stratix		35	35										
Stratix2		21	21										
Stratix3			21										
StratixGX		35	35										
Stratix2GX			21										
Max2		35	35										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		36	35										
Spartan2E		36	35										
Spartan3		36	35										
Spartan3A		36	35										
Spartan3ADSP		36	35										
Spartan3AN		36	35										
Spartan3E		36	35										
Spartan3L		36	35										
Virtex		36	35										
Virtex2		36	35										
Virtex2p		36	35										
VirtexE		36	35										
Virtex4		36	35										
Virtex5		28											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		89											
ProAsic3		57											
ProAsic3E		59											
Fusion		56											
EC		42	35										
ECP		42	35										
ECP2		42	35										
ECP2M		42	35										
SC		42	35										
MACHXO		42	35										
XP		42	35										
XP2		42	35										

FPGA Multiplexer Resource Usage

M7_B8B1E_SB

8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			21										
Cyclone		35	35										
Cyclone2		35	35										
Cyclone3		35	35										
Stratix		35	35										
Stratix2		21	21										
Stratix3			21										
StratixGX		35	35										
Stratix2GX			21										
Max2		35	35										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		36	35										
Spartan2E		36	35										
Spartan3		36	35										
Spartan3A		36	35										
Spartan3ADSP		36	35										
Spartan3AN		36	35										
Spartan3E		36	35										
Spartan3L		36	35										
Virtex		36	35										
Virtex2		36	35										
Virtex2p		36	35										
VirtexE		36	35										
Virtex4		36	35										
Virtex5		28											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		89											
ProAsic3		57											
ProAsic3E		59											
Fusion		56											
EC		42	35										
ECP		42	35										
ECP2		42	35										
ECP2M		42	35										
SC		42	35										
MACHXO		42	35										
XP		42	35										
XP2		42	35										

M7_B16B1

16x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			35										
Cyclone		70	70										
Cyclone2		70	70										
Cyclone3		70	70										
Stratix		70	70										
Stratix2		35	35										
Stratix3			35										
StratixGX		70	70										
Stratix2GX			35										
Max2		70	70										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		64	63										
Spartan2E		64	63										
Spartan3		56	56										
Spartan3A		56	56										
Spartan3ADSP		56	56										
Spartan3AN		56	56										
Spartan3E		56	56										
Spartan3L		56	56										
Virtex		64	63										
Virtex2		56	56										
Virtex2p		56	56										
VirtexE		64	63										
Virtex4		56	56										
Virtex5		54											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		171											
ProAsic3		106											
ProAsic3E		108											
Fusion													
EC		56	56										
ECP		56	56										
ECP2		56	56										
ECP2M		56	56										
SC		56	56										
MACHXO		56	56										
XP		56	56										
XP2		56	56										

FPGA Multiplexer Resource Usage

M7_B16B1_SB

16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			35										
Cyclone		70	70										
Cyclone2		70	70										
Cyclone3		70	70										
Stratix		70	70										
Stratix2		35	35										
Stratix3			35										
StratixGX		70	70										
Stratix2GX			35										
Max2		70	70										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		64	63										
Spartan2E		64	63										
Spartan3		56	56										
Spartan3A		56	56										
Spartan3ADSP		56	56										
Spartan3AN		56	56										
Spartan3E		56	56										
Spartan3L		56	56										
Virtex		64	63										
Virtex2		56	56										
Virtex2p		56	56										
VirtexE		64	63										
Virtex4		56	56										
Virtex5		56											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		171											
ProAsic3		106											
ProAsic3E		108											
Fusion													
EC		56	56										
ECP		56	56										
ECP2		56	56										
ECP2M		56	56										
SC		56	56										
MACHXO		56	56										
XP		56	56										
XP2		56	56										

M7_B16B1E

16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			35										
Cyclone		77	77										
Cyclone2		77	77										
Cyclone3		77	77										
Stratix		77	77										
Stratix2		35	35										
Stratix3			35										
StratixGX		77	77										
Stratix2GX			42										
Max2		77	77										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		72	72										
Spartan3A		64	63										
Spartan3ADSP		64	63										
Spartan3AN		64	63										
Spartan3E		72	72										
Spartan3L		64	63										
Virtex		72	72										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		72	72										
Virtex4		72	72										
Virtex5		42											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		171											
ProAsic3		113											
ProAsic3E		115											
Fusion													
EC		70	63										
ECP		70	63										
ECP2		70	63										
ECP2M		70	63										
SC		70	63										
MACHXO		70	63										
XP		70	63										
XP2		70	63										

FPGA Multiplexer Resource Usage

M7_B16B1E_SB

16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			35										
Cyclone		77	77										
Cyclone2		77	77										
Cyclone3		77	77										
Stratix		77	77										
Stratix2		35	35										
Stratix3			35										
StratixGX		77	77										
Stratix2GX			42										
Max2		77	77										
Max3000a	28	28											
Max7000b	28	28											
Max7000ae	28	28											
Max7000s	28	28											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		72	72										
Spartan3A		64	63										
Spartan3ADSP		64	63										
Spartan3AN		64	63										
Spartan3E		72	72										
Spartan3L		64	63										
Virtex		72	72										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		72	72										
Virtex4		72	72										
Virtex5		42											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		171											
ProAsic3		113											
ProAsic3E		115											
Fusion													
EC		70	63										
ECP		70	63										
ECP2		70	63										
ECP2M		70	63										
SC		70	63										
MACHXO		70	63										
XP		70	63										
XP2		70	63										

M7_S1S2

1x7-Single Wire to 1x7-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		14	14										
Stratix3			14										
StratixGX		14	14										
Stratix2GX			14										
Max2		14	14										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		14											
CoolRunner2	14												
CoolRunnerXpla3	14												
Xc9500	14												
Xc9500XL	14												
Xc9500XV	14												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		15											
Fusion		14											
EC		28	14										
ECP		28	14										
ECP2		28	14										
ECP2M		28	14										
SC		28	14										
MACHXO		28	14										
XP		28	14										
XP2		28	14										

FPGA Multiplexer Resource Usage

M7_S1S2E

1x7-Single Wire to 1x7-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			14										
Cyclone		14	14										
Cyclone2		14	14										
Cyclone3		14	14										
Stratix		14	14										
Stratix2		14	14										
Stratix3			14										
StratixGX		14	14										
Stratix2GX			14										
Max2		14	14										
Max3000a	14	14											
Max7000b	14	14											
Max7000ae	14	14											
Max7000s	14	14											
Spartan2		14	14										
Spartan2E		14	14										
Spartan3		14	14										
Spartan3A		14	14										
Spartan3ADSP		14	14										
Spartan3AN		14	14										
Spartan3E		14	14										
Spartan3L		14	14										
Virtex		14	14										
Virtex2		14	14										
Virtex2p		14	14										
VirtexE		14	14										
Virtex4		14	14										
Virtex5		12											
CoolRunner2	14												
CoolRunnerXpla3	14												
Xc9500	14												
Xc9500XL	14												
Xc9500XV	14												
ProAsicPlus		16											
ProAsic3		14											
ProAsic3E		16											
Fusion		14											
EC		14	14										
ECP		14	14										
ECP2		14	14										
ECP2M		14	14										
SC		14	14										
MACHXO		14	14										
XP		14	14										
XP2		14	14										

M7_S2S1

2x7-Single Wire to 2x7-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		7	7										
Cyclone2		7	7										
Cyclone3		7	7										
Stratix		7	7										
Stratix2		7	7										
Stratix3			7										
StratixGX		7	7										
Stratix2GX			7										
Max2		7	7										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		8	7										
Spartan2E		8	7										
Spartan3		8	7										
Spartan3A		8	7										
Spartan3ADSP		8	7										
Spartan3AN		8	7										
Spartan3E		8	7										
Spartan3L		8	7										
Virtex		8	7										
Virtex2		8	7										
Virtex2p		8	7										
VirtexE		8	7										
Virtex4		8	7										
Virtex5		12											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		7											
ProAsic3		7											
ProAsic3E		7											
Fusion		7											
EC		14	7										
ECP		14	7										
ECP2		14	7										
ECP2M		14	7										
SC		14	7										
MACHXO		14	7										
XP		14	7										
XP2		14	7										

FPGA Multiplexer Resource Usage

M7_S2S1E

2x7-Single Wire to 2x7-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			7										
Cyclone		7	7										
Cyclone2		7	7										
Cyclone3		7	7										
Stratix		7	7										
Stratix2		7	7										
Stratix3			7										
StratixGX		7	7										
Stratix2GX			7										
Max2		7	7										
Max3000a	7	7											
Max7000b	7	7											
Max7000ae	7	7											
Max7000s	7	7											
Spartan2		8	7										
Spartan2E		8	7										
Spartan3		8	7										
Spartan3A		8	7										
Spartan3ADSP		8	7										
Spartan3AN		8	7										
Spartan3E		8	7										
Spartan3L		8	7										
Virtex		8	7										
Virtex2		8	7										
Virtex2p		8	7										
VirtexE		8	7										
Virtex4		8	7										
Virtex5		12											
CoolRunner2	7												
CoolRunnerXpla3	7												
Xc9500	7												
Xc9500XL	7												
Xc9500XV	7												
ProAsicPlus		14											
ProAsic3		14											
ProAsic3E		14											
Fusion		14											
EC		14	7										
ECP		14	7										
ECP2		14	7										
ECP2M		14	7										
SC		14	7										
MACHXO		14	7										
XP		14	7										
XP2		14	7										

M8_B1B2

1x8-Bit Bus to 2x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		17											
Fusion		16											
EC		32	16										
ECP		32	16										
ECP2		32	16										
ECP2M		32	16										
SC		32	16										
MACHXO		32	16										
XP		32	16										
XP2		32	16										

FPGA Multiplexer Resource Usage

M8_B1B2E

1x8-Bit Bus to 2x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		18											
Fusion		16											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M8_B1B4

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		30											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		36											
ProAsic3		36											
ProAsic3E		36											
Fusion		32											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M8_B1B4_SB

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		32											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		36											
ProAsic3		36											
ProAsic3E		36											
Fusion		32											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M8_B1B4E

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		30											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		38											
ProAsic3		36											
ProAsic3E		38											
Fusion		36											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M8_B1B4E_SB

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		28											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		38											
ProAsic3		36											
ProAsic3E		38											
Fusion		36											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M8_B1B8

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		54											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		76											
ProAsic3		68											
ProAsic3E		72											
Fusion		72											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M8_B1B8_SB

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		58											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		76											
ProAsic3		68											
ProAsic3E		72											
Fusion		72											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

M8_B1B8E

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		72	72										
Cyclone2		72	72										
Cyclone3		72	72										
Stratix		72	72										
Stratix2		64	64										
Stratix3			64										
StratixGX		72	72										
Stratix2GX			64										
Max2		72	72										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		66	66										
Spartan2E		66	66										
Spartan3		66	66										
Spartan3A		66	66										
Spartan3ADSP		66	66										
Spartan3AN		66	66										
Spartan3E		66	66										
Spartan3L		66	66										
Virtex		66	66										
Virtex2		66	66										
Virtex2p		66	66										
VirtexE		66	66										
Virtex4		66	66										
Virtex5		60											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		78											
ProAsic3		72											
ProAsic3E		78											
Fusion		78											
EC		66	66										
ECP		66	66										
ECP2		66	66										
ECP2M		66	66										
SC		68	66										
MACHXO		66	66										
XP		66	66										
XP2		128	72										

FPGA Multiplexer Resource Usage

M8_B1B8E_SB

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		72	72										
Cyclone2		72	72										
Cyclone3		72	72										
Stratix		72	72										
Stratix2		64	64										
Stratix3			64										
StratixGX		72	72										
Stratix2GX			64										
Max2		72	72										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		66	66										
Spartan2E		66	66										
Spartan3		66	66										
Spartan3A		66	66										
Spartan3ADSP		66	66										
Spartan3AN		66	66										
Spartan3E		66	66										
Spartan3L		66	66										
Virtex		66	66										
Virtex2		66	66										
Virtex2p		66	66										
VirtexE		66	66										
Virtex4		66	66										
Virtex5			62										
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		78											
ProAsic3		72											
ProAsic3E		78											
Fusion		78											
EC		66	66										
ECP		66	66										
ECP2		66	66										
ECP2M		66	66										
SC		68	66										
MACHXO		66	66										
XP		66	66										
XP2		128	72										

M8_B1B16

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		144	144										
Cyclone2		144	144										
Cyclone3		144	144										
Stratix		144	144										
Stratix2		128	128										
Stratix3			128										
StratixGX		144	144										
Stratix2GX			128										
Max2		144	144										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		136	136										
Spartan2E		136	136										
Spartan3		136	136										
Spartan3A		140	139										
Spartan3ADSP		132	132										
Spartan3AN		140	139										
Spartan3E		136	136										
Spartan3L		136	136										
Virtex		136	136										
Virtex2		136	136										
Virtex2p		136	136										
VirtexE		136	136										
Virtex4		136	136										
Virtex5		134											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		152											
ProAsic3		144											
ProAsic3E		160											
Fusion													
EC		132	132										
ECP		132	132										
ECP2		132	132										
ECP2M		132	132										
SC		134	132										
MACHXO		202	139										
XP		134	132										
XP2		256	144										

FPGA Multiplexer Resource Usage

M8_B1B16_SB

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		144	144										
Cyclone2		144	144										
Cyclone3		144	144										
Stratix		144	144										
Stratix2		128	128										
Stratix3			128										
StratixGX		144	144										
Stratix2GX			128										
Max2		144	144										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		136	136										
Spartan2E		136	136										
Spartan3		136	136										
Spartan3A		140	139										
Spartan3ADSP		132	132										
Spartan3AN		140	139										
Spartan3E		136	136										
Spartan3L		136	136										
Virtex		136	136										
Virtex2		136	136										
Virtex2p		136	136										
VirtexE		136	136										
Virtex4		136	136										
Virtex5		128											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		152											
ProAsic3		144											
ProAsic3E		160											
Fusion													
EC		132	132										
ECP		132	132										
ECP2		132	132										
ECP2M		132	132										
SC		134	132										
MACHXO		202	139										
XP		134	132										
XP2		256	144										

M8_B1B16E

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			130										
Cyclone		146	146										
Cyclone2		146	146										
Cyclone3		146	146										
Stratix		146	146										
Stratix2		130	130										
Stratix3			134										
StratixGX		146	146										
Stratix2GX			134										
Max2		146	146										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		136	136										
Spartan2E		136	136										
Spartan3		136	136										
Spartan3A		132	132										
Spartan3ADSP		132	132										
Spartan3AN		132	132										
Spartan3E		136	136										
Spartan3L		136	136										
Virtex		136	136										
Virtex2		136	136										
Virtex2p		136	136										
VirtexE		136	136										
Virtex4		136	136										
Virtex5		232											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		154											
ProAsic3		144											
ProAsic3E		160											
Fusion													
EC		146	146										
ECP		146	146										
ECP2		132	132										
ECP2M		132	132										
SC		134	132										
MACHXO		134	132										
XP		134	132										
XP2		258	146										

FPGA Multiplexer Resource Usage

M8_B1B16E_SB

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			130										
Cyclone		146	146										
Cyclone2		146	146										
Cyclone3		146	146										
Stratix		146	146										
Stratix2		130	130										
Stratix3			134										
StratixGX		146	146										
Stratix2GX			134										
Max2		146	146										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		138	136										
Spartan2E		138	136										
Spartan3		138	136										
Spartan3A		132	132										
Spartan3ADSP		132	132										
Spartan3AN		132	132										
Spartan3E		138	136										
Spartan3L		136	136										
Virtex		138	136										
Virtex2		138	136										
Virtex2p		138	136										
VirtexE		138	136										
Virtex4		138	136										
Virtex5		224											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		154											
ProAsic3		144											
ProAsic3E		160											
Fusion													
EC		146	146										
ECP		146	146										
ECP2		132	132										
ECP2M		132	132										
SC		134	132										
MACHXO		134	132										
XP		134	132										
XP2		258	146										

M8_B2B1

2x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		12											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

FPGA Multiplexer Resource Usage

M8_B2B1E

2x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5			14										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

M8_B4B1

4x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		8	8										
Stratix3			8										
StratixGX		16	16										
Stratix2GX			8										
Max2		16	16										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5			14										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		36											
ProAsic3		24											
ProAsic3E		25											
Fusion		24											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

FPGA Multiplexer Resource Usage

M8_B4B1_SB

4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		8	8										
Stratix3			8										
StratixGX		16	16										
Stratix2GX			8										
Max2		16	16										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		16											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		36											
ProAsic3		24											
ProAsic3E		25											
Fusion		24											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M8_B4B1E

4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		8	8										
Stratix3			8										
StratixGX		24	24										
Stratix2GX			8										
Max2		24	24										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		16											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		44											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		32	24										
ECP		32	24										
ECP2		32	24										
ECP2M		32	24										
SC		32	24										
MACHXO		32	24										
XP		32	24										
XP2		32	24										

FPGA Multiplexer Resource Usage

M8_B4B1E_SB

4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		8	8										
Stratix3			8										
StratixGX		24	24										
Stratix2GX			8										
Max2		24	24										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		16											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		44											
ProAsic3		32											
ProAsic3E		33											
Fusion		32											
EC		32	24										
ECP		32	24										
ECP2		32	24										
ECP2M		32	24										
SC		32	24										
MACHXO		32	24										
XP		32	24										
XP2		32	24										

M8_B8B1

8x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		24	24										
Stratix3			16										
StratixGX		40	40										
Stratix2GX			16										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5			16										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		92											
ProAsic3		58											
ProAsic3E		59											
Fusion		56											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M8_B8B1_SB

8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		24	24										
Stratix3			16										
StratixGX		40	40										
Stratix2GX			16										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5			16										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		92											
ProAsic3		58											
ProAsic3E		59											
Fusion		56											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M8_B8B1E

8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		24	24										
Stratix3			24										
StratixGX		40	40										
Stratix2GX			24										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		32											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		100											
ProAsic3		66											
ProAsic3E		67											
Fusion		64											
EC		48	40										
ECP		48	40										
ECP2		48	40										
ECP2M		48	40										
SC		48	40										
MACHXO		48	40										
XP		48	40										
XP2		48	40										

FPGA Multiplexer Resource Usage

M8_B8B1E_SB

8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		24	24										
Stratix3			24										
StratixGX		40	40										
Stratix2GX			24										
Max2		40	40										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		28											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		100											
ProAsic3		66											
ProAsic3E		67											
Fusion		64											
EC		48	40										
ECP		48	40										
ECP2		48	40										
ECP2M		48	40										
SC		48	40										
MACHXO		48	40										
XP		48	40										
XP2		48	40										

M8_B16B1

16x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		40	40										
Stratix3			40										
StratixGX		80	80										
Stratix2GX			40										
Max2		80	80										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		72	72										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		72	72										
Virtex4		64	64										
Virtex5		64											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		192											
ProAsic3		122											
ProAsic3E		123											
Fusion													
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M8_B16B1_SB

16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		40	40										
Stratix3			40										
StratixGX		80	80										
Stratix2GX			40										
Max2		80	80										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		72	72										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		72	72										
Virtex4		64	64										
Virtex5		62											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		192											
ProAsic3		122											
ProAsic3E		123											
Fusion													
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

M8_B16B1E

16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		88	88										
Cyclone2		88	88										
Cyclone3		88	88										
Stratix		88	88										
Stratix2		40	40										
Stratix3			40										
StratixGX		88	88										
Stratix2GX			48										
Max2		88	88										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		82	82										
Spartan2E		82	82										
Spartan3		82	82										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		82	82										
Spartan3L		72	72										
Virtex		82	82										
Virtex2		82	82										
Virtex2p		82	82										
VirtexE		82	82										
Virtex4		82	82										
Virtex5		46											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		192											
ProAsic3		130											
ProAsic3E		131											
Fusion													
EC		80	72										
ECP		80	72										
ECP2		80	72										
ECP2M		80	72										
SC		80	72										
MACHXO		80	72										
XP		80	72										
XP2		80	72										

FPGA Multiplexer Resource Usage

M8_B16B1E_SB

16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		88	88										
Cyclone2		88	88										
Cyclone3		88	88										
Stratix		88	88										
Stratix2		40	40										
Stratix3			40										
StratixGX		88	88										
Stratix2GX			48										
Max2		88	88										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		82	82										
Spartan2E		82	82										
Spartan3		82	82										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		82	82										
Spartan3L		72	72										
Virtex		82	82										
Virtex2		82	82										
Virtex2p		82	82										
VirtexE		82	82										
Virtex4		82	82										
Virtex5		46											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		192											
ProAsic3		130											
ProAsic3E		131											
Fusion													
EC		80	72										
ECP		80	72										
ECP2		80	72										
ECP2M		80	72										
SC		80	72										
MACHXO		80	72										
XP		80	72										
XP2		80	72										

M8_S1S2

1x8-Single Wire to 2x8-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		14											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		17											
Fusion		16											
EC		32	16										
ECP		32	16										
ECP2		32	16										
ECP2M		32	16										
SC		32	16										
MACHXO		32	16										
XP		32	16										
XP2		32	16										

FPGA Multiplexer Resource Usage

M8_S1S2E

1x8-Single Wire to 2x8-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		14											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		18											
ProAsic3		16											
ProAsic3E		18											
Fusion		16											
EC		16	16										
ECP		16	16										
ECP2		16	16										
ECP2M		16	16										
SC		16	16										
MACHXO		16	16										
XP		16	16										
XP2		16	16										

M8_S2S1

2x8-Single Wire to 1x8-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5			14										
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		8											
ProAsic3		8											
ProAsic3E		8											
Fusion		8											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

FPGA Multiplexer Resource Usage

M8_S2S1E

2x8-Single Wire to 1x8-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			8										
Cyclone		8	8										
Cyclone2		8	8										
Cyclone3		8	8										
Stratix		8	8										
Stratix2		8	8										
Stratix3			8										
StratixGX		8	8										
Stratix2GX			8										
Max2		8	8										
Max3000a	8	8											
Max7000b	8	8											
Max7000ae	8	8											
Max7000s	8	8											
Spartan2		8	8										
Spartan2E		8	8										
Spartan3		8	8										
Spartan3A		8	8										
Spartan3ADSP		8	8										
Spartan3AN		8	8										
Spartan3E		8	8										
Spartan3L		8	8										
Virtex		8	8										
Virtex2		8	8										
Virtex2p		8	8										
VirtexE		8	8										
Virtex4		8	8										
Virtex5		16											
CoolRunner2	8												
CoolRunnerXpla3	8												
Xc9500	8												
Xc9500XL	8												
Xc9500XV	8												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		16											
Fusion		16											
EC		16	8										
ECP		16	8										
ECP2		16	8										
ECP2M		16	8										
SC		16	8										
MACHXO		16	8										
XP		16	8										
XP2		16	8										

M9_B1B2

1x9-Bit Bus to 2x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		18	18										
Stratix3			18										
StratixGX		18	18										
Stratix2GX			18										
Max2		18	18										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		18											
CoolRunner2	18												
CoolRunnerXpla3	18												
Xc9500	18												
Xc9500XL	18												
Xc9500XV	18												
ProAsicPlus		18											
ProAsic3		19											
ProAsic3E		19											
Fusion		18											
EC		36	18										
ECP		36	18										
ECP2		36	18										
ECP2M		36	18										
SC		36	18										
MACHXO		36	18										
XP		36	18										
XP2		36	18										

FPGA Multiplexer Resource Usage

M9_B1B2E

1x9-Bit Bus to 2x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		18	18										
Stratix3			18										
StratixGX		18	18										
Stratix2GX			18										
Max2		18	18										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		14											
CoolRunner2	18												
CoolRunnerXpla3	18												
Xc9500	18												
Xc9500XL	18												
Xc9500XV	18												
ProAsicPlus		20											
ProAsic3		20											
ProAsic3E		20											
Fusion		18											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M9_B1B4

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		36	36										
Stratix3			36										
StratixGX		36	36										
Stratix2GX			36										
Max2		36	36										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		34											
CoolRunner2	36												
CoolRunnerXpla3	36												
Xc9500	36												
Xc9500XL	36												
Xc9500XV	36												
ProAsicPlus		40											
ProAsic3		40											
ProAsic3E		42											
Fusion		36											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

FPGA Multiplexer Resource Usage

M9_B1B4_SB

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		36	36										
Stratix3			36										
StratixGX		36	36										
Stratix2GX			36										
Max2		36	36										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		32											
CoolRunner2	36												
CoolRunnerXpla3	36												
Xc9500	36												
Xc9500XL	36												
Xc9500XV	36												
ProAsicPlus		40											
ProAsic3		40											
ProAsic3E		42											
Fusion		36											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

M9_B1B4E

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		36	36										
Stratix3			36										
StratixGX		36	36										
Stratix2GX			36										
Max2		36	36										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		32											
CoolRunner2	36												
CoolRunnerXpla3	36												
Xc9500	36												
Xc9500XL	36												
Xc9500XV	36												
ProAsicPlus		42											
ProAsic3		42											
ProAsic3E		43											
Fusion		40											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

FPGA Multiplexer Resource Usage

M9_B1B4E_SB

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		36	36										
Stratix3			36										
StratixGX		36	36										
Stratix2GX			36										
Max2		36	36										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		32											
CoolRunner2	36												
CoolRunnerXpla3	36												
Xc9500	36												
Xc9500XL	36												
Xc9500XV	36												
ProAsicPlus		42											
ProAsic3		42											
ProAsic3E		43											
Fusion		40											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

M9_B1B8

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			72										
Cyclone		72	72										
Cyclone2		72	72										
Cyclone3		72	72										
Stratix		72	72										
Stratix2		72	72										
Stratix3			72										
StratixGX		72	72										
Stratix2GX			72										
Max2		72	72										
Max3000a	72	72											
Max7000b	72	72											
Max7000ae	72	72											
Max7000s	72	72											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		72	72										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		72	72										
Spartan3L		72	72										
Virtex		72	72										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		72	72										
Virtex4		72	72										
Virtex5		66											
CoolRunner2	72												
CoolRunnerXpla3	72												
Xc9500	72												
Xc9500XL	72												
Xc9500XV	72												
ProAsicPlus		84											
ProAsic3		80											
ProAsic3E		80											
Fusion		80											
EC		72	72										
ECP		72	72										
ECP2		72	72										
ECP2M		72	72										
SC		72	72										
MACHXO		72	72										
XP		72	72										
XP2		72	72										

FPGA Multiplexer Resource Usage

M9_B1B8_SB

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			72										
Cyclone		72	72										
Cyclone2		72	72										
Cyclone3		72	72										
Stratix		72	72										
Stratix2		72	72										
Stratix3			72										
StratixGX		72	72										
Stratix2GX			72										
Max2		72	72										
Max3000a	72	72											
Max7000b	72	72											
Max7000ae	72	72											
Max7000s	72	72											
Spartan2		72	72										
Spartan2E		72	72										
Spartan3		72	72										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		72	72										
Spartan3L		72	72										
Virtex		72	72										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		72	72										
Virtex4		72	72										
Virtex5		68											
CoolRunner2	72												
CoolRunnerXpla3	72												
Xc9500	72												
Xc9500XL	72												
Xc9500XV	72												
ProAsicPlus		84											
ProAsic3		80											
ProAsic3E		80											
Fusion		80											
EC		72	72										
ECP		72	72										
ECP2		72	72										
ECP2M		72	72										
SC		72	72										
MACHXO		72	72										
XP		72	72										
XP2		72	72										

M9_B1B8E

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			72										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		72	72										
Stratix3			72										
StratixGX		80	80										
Stratix2GX			72										
Max2		80	80										
Max3000a		72	72										
Max7000b		72	72										
Max7000ae		72	72										
Max7000s		72	72										
Spartan2		74	74										
Spartan2E		74	74										
Spartan3		74	74										
Spartan3A		74	74										
Spartan3ADSP		74	74										
Spartan3AN		74	74										
Spartan3E		74	74										
Spartan3L		74	74										
Virtex		74	74										
Virtex2		74	74										
Virtex2p		74	74										
VirtexE		74	74										
Virtex4		74	74										
Virtex5		70											
CoolRunner2		72											
CoolRunnerXpla3		72											
Xc9500		72											
Xc9500XL		72											
Xc9500XV		72											
ProAsicPlus		86											
ProAsic3		86											
ProAsic3E		86											
Fusion		86											
EC		74	74										
ECP		74	74										
ECP2		74	74										
ECP2M		74	74										
SC		76	74										
MACHXO		74	74										
XP		74	74										
XP2		144	80										

FPGA Multiplexer Resource Usage

M9_B1B8E_SB

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			72										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		72	72										
Stratix3			72										
StratixGX		80	80										
Stratix2GX			72										
Max2		80	80										
Max3000a	72	72											
Max7000b	72	72											
Max7000ae	72	72											
Max7000s	72	72											
Spartan2		74	74										
Spartan2E		74	74										
Spartan3		74	74										
Spartan3A		74	74										
Spartan3ADSP		74	74										
Spartan3AN		74	74										
Spartan3E		74	74										
Spartan3L		74	74										
Virtex		74	74										
Virtex2		74	74										
Virtex2p		74	74										
VirtexE		74	74										
Virtex4		74	74										
Virtex5		70											
CoolRunner2	72												
CoolRunnerXpla3	72												
Xc9500	72												
Xc9500XL	72												
Xc9500XV	72												
ProAsicPlus		86											
ProAsic3		86											
ProAsic3E		86											
Fusion		86											
EC		74	74										
ECP		74	74										
ECP2		74	74										
ECP2M		74	74										
SC		76	74										
MACHXO		74	74										
XP		74	74										
XP2		144	80										

M9_B1B16

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			144										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		144	144										
Stratix3			144										
StratixGX		160	160										
Stratix2GX			144										
Max2		160	160										
Max3000a	144	144											
Max7000b	144	144											
Max7000ae	144	144											
Max7000s	144	144											
Spartan2		154	152										
Spartan2E		154	152										
Spartan3		154	152										
Spartan3A		156	155										
Spartan3ADSP		148	148										
Spartan3AN		156	155										
Spartan3E		152	152										
Spartan3L		148	148										
Virtex		154	152										
Virtex2		154	152										
Virtex2p		154	152										
VirtexE		154	152										
Virtex4		152	152										
Virtex5		158											
CoolRunner2	144												
CoolRunnerXpla3	144												
Xc9500	144												
Xc9500XL	144												
Xc9500XV	144												
ProAsicPlus		168											
ProAsic3		168											
ProAsic3E		177											
Fusion													
EC		148	148										
ECP		148	148										
ECP2		204	155										
ECP2M		204	155										
SC		204	155										
MACHXO		226	155										
XP		150	148										
XP2		288	160										

FPGA Multiplexer Resource Usage

M9_B1B16_SB

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			144										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		144	144										
Stratix3			144										
StratixGX		160	160										
Stratix2GX			144										
Max2		160	160										
Max3000a	144	144											
Max7000b	144	144											
Max7000ae	144	144											
Max7000s	144	144											
Spartan2		152	152										
Spartan2E		152	152										
Spartan3		152	152										
Spartan3A		156	155										
Spartan3ADSP		156	155										
Spartan3AN		156	155										
Spartan3E		152	152										
Spartan3L		156	155										
Virtex		152	152										
Virtex2		152	152										
Virtex2p		152	152										
VirtexE		152	152										
Virtex4		152	152										
Virtex5		136											
CoolRunner2	144												
CoolRunnerXpla3	144												
Xc9500	144												
Xc9500XL	144												
Xc9500XV	144												
ProAsicPlus		168											
ProAsic3		168											
ProAsic3E		177											
Fusion													
EC		148	148										
ECP		148	148										
ECP2		226	155										
ECP2M		226	155										
SC		228	155										
MACHXO		226	155										
XP		150	148										
XP2		288	160										

M9_B1B16E

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		162	162										
Cyclone2		162	162										
Cyclone3		162	162										
Stratix		162	162										
Stratix2		146	146										
Stratix3			160										
StratixGX		162	162										
Stratix2GX			160										
Max2		162	162										
Max3000a	144	144											
Max7000b	144	144											
Max7000ae	144	144											
Max7000s	144	144											
Spartan2		152	152										
Spartan2E		152	152										
Spartan3		152	152										
Spartan3A		150	148										
Spartan3ADSP		148	148										
Spartan3AN		150	148										
Spartan3E		154	152										
Spartan3L		152	152										
Virtex		152	152										
Virtex2		152	152										
Virtex2p		152	152										
VirtexE		152	152										
Virtex4		152	152										
Virtex5		248											
CoolRunner2	144												
CoolRunnerXpla3	144												
Xc9500	144												
Xc9500XL	144												
Xc9500XV	144												
ProAsicPlus		170											
ProAsic3		168											
ProAsic3E		177											
Fusion													
EC		162	162										
ECP		162	162										
ECP2		148	148										
ECP2M		148	148										
SC		150	148										
MACHXO		148	148										
XP		150	148										
XP2		290	162										

FPGA Multiplexer Resource Usage

M9_B1B16E_SB

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		162	162										
Cyclone2		162	162										
Cyclone3		162	162										
Stratix		162	162										
Stratix2		146	146										
Stratix3			160										
StratixGX		162	162										
Stratix2GX			160										
Max2		162	162										
Max3000a	144	144											
Max7000b	144	144											
Max7000ae	144	144											
Max7000s	144	144											
Spartan2		154	152										
Spartan2E		154	152										
Spartan3		154	152										
Spartan3A		148	148										
Spartan3ADSP		150	148										
Spartan3AN		148	148										
Spartan3E		152	152										
Spartan3L		152	152										
Virtex		154	152										
Virtex2		154	152										
Virtex2p		154	152										
VirtexE		154	152										
Virtex4		152	152										
Virtex5		246											
CoolRunner2	144												
CoolRunnerXpla3	144												
Xc9500	144												
Xc9500XL	144												
Xc9500XV	144												
ProAsicPlus		170											
ProAsic3		168											
ProAsic3E		177											
Fusion													
EC		162	162										
ECP		162	162										
ECP2		148	148										
ECP2M		148	148										
SC		150	148										
MACHXO		148	148										
XP		150	148										
XP2		290	162										

M9_B2B1

2x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		9	9										
Stratix3			9										
StratixGX		9	9										
Stratix2GX			9										
Max2		9	9										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		16											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		9											
ProAsic3		9											
ProAsic3E		9											
Fusion		9											
EC		18	9										
ECP		18	9										
ECP2		18	9										
ECP2M		18	9										
SC		18	9										
MACHXO		18	9										
XP		18	9										
XP2		18	9										

FPGA Multiplexer Resource Usage

M9_B2B1E

2x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		9	9										
Cyclone2		9	9										
Cyclone3		9	9										
Stratix		9	9										
Stratix2		9	9										
Stratix3			9										
StratixGX		9	9										
Stratix2GX			9										
Max2		9	9										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		10	9										
Spartan2E		10	9										
Spartan3		10	9										
Spartan3A		10	9										
Spartan3ADSP		10	9										
Spartan3AN		10	9										
Spartan3E		10	9										
Spartan3L		10	9										
Virtex		10	9										
Virtex2		10	9										
Virtex2p		10	9										
VirtexE		10	9										
Virtex4		10	9										
Virtex5		14											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		18											
ProAsic3		18											
ProAsic3E		18											
Fusion		18											
EC		18	9										
ECP		18	9										
ECP2		18	9										
ECP2M		18	9										
SC		18	9										
MACHXO		18	9										
XP		18	9										
XP2		18	9										

M9_B4B1

4x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		9	9										
Stratix3			9										
StratixGX		18	18										
Stratix2GX			9										
Max2		18	18										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		18											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		40											
ProAsic3		28											
ProAsic3E		28											
Fusion		27											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

FPGA Multiplexer Resource Usage

M9_B4B1_SB

4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		18	18										
Cyclone2		18	18										
Cyclone3		18	18										
Stratix		18	18										
Stratix2		9	9										
Stratix3			9										
StratixGX		18	18										
Stratix2GX			9										
Max2		18	18										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		18	18										
Spartan2E		18	18										
Spartan3		18	18										
Spartan3A		18	18										
Spartan3ADSP		18	18										
Spartan3AN		18	18										
Spartan3E		18	18										
Spartan3L		18	18										
Virtex		18	18										
Virtex2		18	18										
Virtex2p		18	18										
VirtexE		18	18										
Virtex4		18	18										
Virtex5		16											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		40											
ProAsic3		28											
ProAsic3E		28											
Fusion		27											
EC		18	18										
ECP		18	18										
ECP2		18	18										
ECP2M		18	18										
SC		18	18										
MACHXO		18	18										
XP		18	18										
XP2		18	18										

M9_B4B1E

4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		27	27										
Cyclone2		27	27										
Cyclone3		27	27										
Stratix		27	27										
Stratix2		9	9										
Stratix3			9										
StratixGX		27	27										
Stratix2GX			9										
Max2		27	27										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		28	27										
Spartan2E		28	27										
Spartan3		28	27										
Spartan3A		28	27										
Spartan3ADSP		28	27										
Spartan3AN		28	27										
Spartan3E		28	27										
Spartan3L		28	27										
Virtex		28	27										
Virtex2		28	27										
Virtex2p		28	27										
VirtexE		28	27										
Virtex4		28	27										
Virtex5		18											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		49											
ProAsic3		37											
ProAsic3E		37											
Fusion		36											
EC		36	27										
ECP		36	27										
ECP2		36	27										
ECP2M		36	27										
SC		36	27										
MACHXO		36	27										
XP		36	27										
XP2		36	27										

FPGA Multiplexer Resource Usage

M9_B4B1E_SB

4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			9										
Cyclone		27	27										
Cyclone2		27	27										
Cyclone3		27	27										
Stratix		27	27										
Stratix2		9	9										
Stratix3			9										
StratixGX		27	27										
Stratix2GX			9										
Max2		27	27										
Max3000a	9	9											
Max7000b	9	9											
Max7000ae	9	9											
Max7000s	9	9											
Spartan2		28	27										
Spartan2E		28	27										
Spartan3		28	27										
Spartan3A		28	27										
Spartan3ADSP		28	27										
Spartan3AN		28	27										
Spartan3E		28	27										
Spartan3L		28	27										
Virtex		28	27										
Virtex2		28	27										
Virtex2p		28	27										
VirtexE		28	27										
Virtex4		28	27										
Virtex5		18											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		49											
ProAsic3		37											
ProAsic3E		37											
Fusion		36											
EC		36	27										
ECP		36	27										
ECP2		36	27										
ECP2M		36	27										
SC		36	27										
MACHXO		36	27										
XP		36	27										
XP2		36	27										

M9_B8B1

8x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		45	45										
Cyclone2		45	45										
Cyclone3		45	45										
Stratix		45	45										
Stratix2		27	27										
Stratix3			18										
StratixGX		45	45										
Stratix2GX			18										
Max2		45	45										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		18											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		102											
ProAsic3		66											
ProAsic3E		67											
Fusion		63											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

FPGA Multiplexer Resource Usage

M9_B8B1_SB

8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			18										
Cyclone		45	45										
Cyclone2		45	45										
Cyclone3		45	45										
Stratix		45	45										
Stratix2		27	27										
Stratix3			18										
StratixGX		45	45										
Stratix2GX			18										
Max2		45	45										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		18											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		102											
ProAsic3		66											
ProAsic3E		67											
Fusion		63											
EC		36	36										
ECP		36	36										
ECP2		36	36										
ECP2M		36	36										
SC		36	36										
MACHXO		36	36										
XP		36	36										
XP2		36	36										

M9_B8B1E

8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			27										
Cyclone		45	45										
Cyclone2		45	45										
Cyclone3		45	45										
Stratix		45	45										
Stratix2		27	27										
Stratix3			27										
StratixGX		45	45										
Stratix2GX			27										
Max2		45	45										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		46	45										
Spartan2E		46	45										
Spartan3		46	45										
Spartan3A		46	45										
Spartan3ADSP		46	45										
Spartan3AN		46	45										
Spartan3E		46	45										
Spartan3L		46	45										
Virtex		46	45										
Virtex2		46	45										
Virtex2p		46	45										
VirtexE		46	45										
Virtex4		46	45										
Virtex5		34											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		111											
ProAsic3		75											
ProAsic3E		76											
Fusion		72											
EC		54	45										
ECP		54	45										
ECP2		54	45										
ECP2M		54	45										
SC		54	45										
MACHXO		54	45										
XP		54	45										
XP2		54	45										

FPGA Multiplexer Resource Usage

M9_B8B1E_SB

8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			27										
Cyclone		45	45										
Cyclone2		45	45										
Cyclone3		45	45										
Stratix		45	45										
Stratix2		27	27										
Stratix3			27										
StratixGX		45	45										
Stratix2GX			27										
Max2		45	45										
Max3000a	18	18											
Max7000b	18	18											
Max7000ae	18	18											
Max7000s	18	18											
Spartan2		46	45										
Spartan2E		46	45										
Spartan3		46	45										
Spartan3A		46	45										
Spartan3ADSP		46	45										
Spartan3AN		46	45										
Spartan3E		46	45										
Spartan3L		46	45										
Virtex		46	45										
Virtex2		46	45										
Virtex2p		46	45										
VirtexE		46	45										
Virtex4		46	45										
Virtex5		36											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		111											
ProAsic3		75											
ProAsic3E		76											
Fusion		72											
EC		54	45										
ECP		54	45										
ECP2		54	45										
ECP2M		54	45										
SC		54	45										
MACHXO		54	45										
XP		54	45										
XP2		54	45										

M9_B16B1

16x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			45										
Cyclone		90	90										
Cyclone2		90	90										
Cyclone3		90	90										
Stratix		90	90										
Stratix2		45	45										
Stratix3			45										
StratixGX		90	90										
Stratix2GX			45										
Max2		90	90										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		82	81										
Spartan2E		82	81										
Spartan3		72	72										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		72	72										
Spartan3L		72	72										
Virtex		82	81										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		82	81										
Virtex4		72	72										
Virtex5		72											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		213											
ProAsic3		138											
ProAsic3E		139											
Fusion													
EC		72	72										
ECP		72	72										
ECP2		72	72										
ECP2M		72	72										
SC		72	72										
MACHXO		72	72										
XP		72	72										
XP2		72	72										

FPGA Multiplexer Resource Usage

M9_B16B1_SB

16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			45										
Cyclone		90	90										
Cyclone2		90	90										
Cyclone3		90	90										
Stratix		90	90										
Stratix2		45	45										
Stratix3			45										
StratixGX		90	90										
Stratix2GX			45										
Max2		90	90										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		82	81										
Spartan2E		82	81										
Spartan3		72	72										
Spartan3A		72	72										
Spartan3ADSP		72	72										
Spartan3AN		72	72										
Spartan3E		72	72										
Spartan3L		72	72										
Virtex		82	81										
Virtex2		72	72										
Virtex2p		72	72										
VirtexE		82	81										
Virtex4		72	72										
Virtex5		70											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		213											
ProAsic3		138											
ProAsic3E		139											
Fusion													
EC		72	72										
ECP		72	72										
ECP2		72	72										
ECP2M		72	72										
SC		72	72										
MACHXO		72	72										
XP		72	72										
XP2		72	72										

M9_B16B1E

16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			45										
Cyclone		99	99										
Cyclone2		99	99										
Cyclone3		99	99										
Stratix		99	99										
Stratix2		45	45										
Stratix3			45										
StratixGX		99	99										
Stratix2GX			54										
Max2		99	99										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		84	83										
Spartan2E		84	83										
Spartan3		84	83										
Spartan3A		82	81										
Spartan3ADSP		82	81										
Spartan3AN		82	81										
Spartan3E		84	83										
Spartan3L		82	81										
Virtex		84	83										
Virtex2		84	83										
Virtex2p		84	83										
VirtexE		84	83										
Virtex4		84	83										
Virtex5		54											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		213											
ProAsic3		147											
ProAsic3E		148											
Fusion													
EC		90	81										
ECP		90	81										
ECP2		90	81										
ECP2M		90	81										
SC		186	113										
MACHXO		90	81										
XP		186	113										
XP2		90	81										

FPGA Multiplexer Resource Usage

M9_B16B1E_SB

16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			45										
Cyclone		99	99										
Cyclone2		99	99										
Cyclone3		99	99										
Stratix		99	99										
Stratix2		45	45										
Stratix3			45										
StratixGX		99	99										
Stratix2GX			54										
Max2		99	99										
Max3000a	36	36											
Max7000b	36	36											
Max7000ae	36	36											
Max7000s	36	36											
Spartan2		84	83										
Spartan2E		84	83										
Spartan3		84	83										
Spartan3A		82	81										
Spartan3ADSP		82	81										
Spartan3AN		82	81										
Spartan3E		84	83										
Spartan3L		82	81										
Virtex		84	83										
Virtex2		84	83										
Virtex2p		84	83										
VirtexE		84	83										
Virtex4		84	83										
Virtex5		54											
CoolRunner2	9												
CoolRunnerXpla3	9												
Xc9500	9												
Xc9500XL	9												
Xc9500XV	9												
ProAsicPlus		213											
ProAsic3		147											
ProAsic3E		148											
Fusion													
EC		90	81										
ECP		90	81										
ECP2		90	81										
ECP2M		90	81										
SC		186	113										
MACHXO		90	81										
XP		186	113										
XP2		90	81										

M10_B1B2

1x10-Bit Bus to 2x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		18											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		20											
ProAsic3		21											
ProAsic3E		21											
Fusion		20											
EC		40	20										
ECP		40	20										
ECP2		40	20										
ECP2M		40	20										
SC		40	20										
MACHXO		40	20										
XP		40	20										
XP2		40	20										

FPGA Multiplexer Resource Usage

M10_B1B2E

1x10-Bit Bus to 2x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		20	20										
Stratix3			20										
StratixGX		20	20										
Stratix2GX			20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		18											
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		22											
ProAsic3		22											
ProAsic3E		22											
Fusion		20											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

M10_B1B4

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		38											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		44											
ProAsic3		44											
ProAsic3E		48											
Fusion		40											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

FPGA Multiplexer Resource Usage

M10_B1B4_SB

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		38											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		44											
ProAsic3		44											
ProAsic3E		48											
Fusion		40											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

M10_B1B4E

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		36											
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus		46											
ProAsic3		44											
ProAsic3E		48											
Fusion		44											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

FPGA Multiplexer Resource Usage

M10_B1B4E_SB

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			40										
Cyclone		40	40										
Cyclone2		40	40										
Cyclone3		40	40										
Stratix		40	40										
Stratix2		40	40										
Stratix3			40										
StratixGX		40	40										
Stratix2GX			40										
Max2		40	40										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5			36										
CoolRunner2	40												
CoolRunnerXpla3	40												
Xc9500	40												
Xc9500XL	40												
Xc9500XV	40												
ProAsicPlus			46										
ProAsic3			44										
ProAsic3E			48										
Fusion			44										
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

M10_B1B8

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		80	80										
Stratix3			80										
StratixGX		80	80										
Stratix2GX			80										
Max2		80	80										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		80	80										
Spartan2E		80	80										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		80	80										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		80	80										
Virtex4		80	80										
Virtex5		74											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		92											
ProAsic3		88											
ProAsic3E		88											
Fusion		88											
EC		80	80										
ECP		80	80										
ECP2		80	80										
ECP2M		80	80										
SC		80	80										
MACHXO		80	80										
XP		80	80										
XP2		80	80										

FPGA Multiplexer Resource Usage

M10_B1B8_SB

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		80	80										
Stratix3			80										
StratixGX		80	80										
Stratix2GX			80										
Max2		80	80										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		80	80										
Spartan2E		80	80										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		80	80										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		80	80										
Virtex4		80	80										
Virtex5		66											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		92											
ProAsic3		88											
ProAsic3E		88											
Fusion		88											
EC		80	80										
ECP		80	80										
ECP2		80	80										
ECP2M		80	80										
SC		80	80										
MACHXO		80	80										
XP		80	80										
XP2		80	80										

M10_B1B8E

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		88	88										
Cyclone2		88	88										
Cyclone3		88	88										
Stratix		88	88										
Stratix2		80	80										
Stratix3			80										
StratixGX		88	88										
Stratix2GX			80										
Max2		88	88										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		82	82										
Spartan2E		82	82										
Spartan3		82	82										
Spartan3A		82	82										
Spartan3ADSP		82	82										
Spartan3AN		82	82										
Spartan3E		82	82										
Spartan3L		82	82										
Virtex		82	82										
Virtex2		82	82										
Virtex2p		82	82										
VirtexE		82	82										
Virtex4		82	82										
Virtex5		78											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		94											
ProAsic3		94											
ProAsic3E		96											
Fusion		96											
EC		82	82										
ECP		82	82										
ECP2		82	82										
ECP2M		82	82										
SC		84	82										
MACHXO		82	82										
XP		82	82										
XP2		160	88										

FPGA Multiplexer Resource Usage

M10_B1B8E_SB

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		88	88										
Cyclone2		88	88										
Cyclone3		88	88										
Stratix		88	88										
Stratix2		80	80										
Stratix3			80										
StratixGX		88	88										
Stratix2GX			80										
Max2		88	88										
Max3000a	80	80											
Max7000b	80	80											
Max7000ae	80	80											
Max7000s	80	80											
Spartan2		82	82										
Spartan2E		82	82										
Spartan3		82	82										
Spartan3A		82	82										
Spartan3ADSP		82	82										
Spartan3AN		82	82										
Spartan3E		82	82										
Spartan3L		82	82										
Virtex		82	82										
Virtex2		82	82										
Virtex2p		82	82										
VirtexE		82	82										
Virtex4		82	82										
Virtex5		76											
CoolRunner2	80												
CoolRunnerXpla3	80												
Xc9500	80												
Xc9500XL	80												
Xc9500XV	80												
ProAsicPlus		94											
ProAsic3		94											
ProAsic3E		96											
Fusion		96											
EC		82	82										
ECP		82	82										
ECP2		82	82										
ECP2M		82	82										
SC		84	82										
MACHXO		82	82										
XP		82	82										
XP2		160	88										

M10_B1B16

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		176	176										
Cyclone2		176	176										
Cyclone3		176	176										
Stratix		176	176										
Stratix2		160	160										
Stratix3			160										
StratixGX		176	176										
Stratix2GX			160										
Max2		176	176										
Max3000a	160	160											
Max7000b	160	160											
Max7000ae	160	160											
Max7000s	160	160											
Spartan2		168	168										
Spartan2E		168	168										
Spartan3		168	168										
Spartan3A		172	171										
Spartan3ADSP		164	164										
Spartan3AN		172	171										
Spartan3E		168	168										
Spartan3L		164	164										
Virtex		168	168										
Virtex2		168	168										
Virtex2p		168	168										
VirtexE		168	168										
Virtex4		170	168										
Virtex5		146											
CoolRunner2	160												
CoolRunnerXpla3	160												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		184											
ProAsic3		184											
ProAsic3E		206											
Fusion													
EC		164	164										
ECP		164	164										
ECP2		228	171										
ECP2M		228	171										
SC		226	171										
MACHXO		252	171										
XP		166	164										
XP2		320	176										

FPGA Multiplexer Resource Usage

M10_B1B16_SB

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		176	176										
Cyclone2		176	176										
Cyclone3		176	176										
Stratix		176	176										
Stratix2		160	160										
Stratix3			160										
StratixGX		176	176										
Stratix2GX			160										
Max2		176	176										
Max3000a	160	160											
Max7000b	160	160											
Max7000ae	160	160											
Max7000s	160	160											
Spartan2		168	168										
Spartan2E		168	168										
Spartan3		168	168										
Spartan3A		172	171										
Spartan3ADSP		172	171										
Spartan3AN		172	171										
Spartan3E		168	168										
Spartan3L		172	171										
Virtex		168	168										
Virtex2		168	168										
Virtex2p		168	168										
VirtexE		168	168										
Virtex4		168	168										
Virtex5		150											
CoolRunner2	160												
CoolRunnerXpla3	160												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		184											
ProAsic3		184											
ProAsic3E		206											
Fusion													
EC		164	164										
ECP		164	164										
ECP2		252	171										
ECP2M		252	171										
SC		254	171										
MACHXO		252	171										
XP		166	164										
XP2		320	176										

M10_B1B16E

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			176										
Cyclone		178	178										
Cyclone2		178	178										
Cyclone3		178	178										
Stratix		178	178										
Stratix2		162	162										
Stratix3			176										
StratixGX		178	178										
Stratix2GX			176										
Max2		178	178										
Max3000a	160	160											
Max7000b	160	160											
Max7000ae	160	160											
Max7000s	160	160											
Spartan2		168	168										
Spartan2E		168	168										
Spartan3		168	168										
Spartan3A		164	164										
Spartan3ADSP		164	164										
Spartan3AN		164	164										
Spartan3E		168	168										
Spartan3L		168	168										
Virtex		168	168										
Virtex2		168	168										
Virtex2p		168	168										
VirtexE		168	168										
Virtex4		168	168										
Virtex5		260											
CoolRunner2	160												
CoolRunnerXpla3	160												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		186											
ProAsic3		184											
ProAsic3E		207											
Fusion													
EC		178	178										
ECP		178	178										
ECP2		166	164										
ECP2M		166	164										
SC		166	164										
MACHXO		166	164										
XP		166	164										
XP2		322	178										

FPGA Multiplexer Resource Usage

M10_B1B16E_SB

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			176										
Cyclone		178	178										
Cyclone2		178	178										
Cyclone3		178	178										
Stratix		178	178										
Stratix2		162	162										
Stratix3			176										
StratixGX		178	178										
Stratix2GX			176										
Max2		178	178										
Max3000a	160	160											
Max7000b	160	160											
Max7000ae	160	160											
Max7000s	160	160											
Spartan2		168	168										
Spartan2E		168	168										
Spartan3		168	168										
Spartan3A		164	164										
Spartan3ADSP		166	164										
Spartan3AN		164	164										
Spartan3E		170	168										
Spartan3L		170	168										
Virtex		168	168										
Virtex2		168	168										
Virtex2p		168	168										
VirtexE		168	168										
Virtex4		168	168										
Virtex5		276											
CoolRunner2	160												
CoolRunnerXpla3	160												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		186											
ProAsic3		184											
ProAsic3E		207											
Fusion													
EC		178	178										
ECP		178	178										
ECP2		164	164										
ECP2M		164	164										
SC		166	164										
MACHXO		166	164										
XP		166	164										
XP2		322	178										

M10_B2B1

2x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		20											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		10											
ProAsic3		10											
ProAsic3E		10											
Fusion		10											
EC		20	10										
ECP		20	10										
ECP2		20	10										
ECP2M		20	10										
SC		20	10										
MACHXO		20	10										
XP		20	10										
XP2		20	10										

FPGA Multiplexer Resource Usage

M10_B2B1E

2x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		10	10										
Cyclone2		10	10										
Cyclone3		10	10										
Stratix		10	10										
Stratix2		10	10										
Stratix3			10										
StratixGX		10	10										
Stratix2GX			10										
Max2		10	10										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		10	10										
Spartan2E		10	10										
Spartan3		10	10										
Spartan3A		10	10										
Spartan3ADSP		10	10										
Spartan3AN		10	10										
Spartan3E		10	10										
Spartan3L		10	10										
Virtex		10	10										
Virtex2		10	10										
Virtex2p		10	10										
VirtexE		10	10										
Virtex4		10	10										
Virtex5		18											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		20											
ProAsic3		20											
ProAsic3E		20											
Fusion		20											
EC		20	10										
ECP		20	10										
ECP2		20	10										
ECP2M		20	10										
SC		20	10										
MACHXO		20	10										
XP		20	10										
XP2		20	10										

M10_B4B1

4x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		10	10										
Stratix3			10										
StratixGX		20	20										
Stratix2GX			10										
Max2		20	20										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		18											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		44											
ProAsic3		31											
ProAsic3E		31											
Fusion		30											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

FPGA Multiplexer Resource Usage

M10_B4B1_SB

4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		20	20										
Cyclone2		20	20										
Cyclone3		20	20										
Stratix		20	20										
Stratix2		10	10										
Stratix3			10										
StratixGX		20	20										
Stratix2GX			10										
Max2		20	20										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Spartan3A		20	20										
Spartan3ADSP		20	20										
Spartan3AN		20	20										
Spartan3E		20	20										
Spartan3L		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
Virtex5		20											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		44											
ProAsic3		31											
ProAsic3E		31											
Fusion		30											
EC		20	20										
ECP		20	20										
ECP2		20	20										
ECP2M		20	20										
SC		20	20										
MACHXO		20	20										
XP		20	20										
XP2		20	20										

M10_B4B1E

4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		10	10										
Stratix3			10										
StratixGX		30	30										
Stratix2GX			10										
Max2		30	30										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		30	30										
Spartan2E		30	30										
Spartan3		30	30										
Spartan3A		30	30										
Spartan3ADSP		30	30										
Spartan3AN		30	30										
Spartan3E		30	30										
Spartan3L		30	30										
Virtex		30	30										
Virtex2		30	30										
Virtex2p		30	30										
VirtexE		30	30										
Virtex4		30	30										
Virtex5		18											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		54											
ProAsic3		41											
ProAsic3E		41											
Fusion		40											
EC		40	30										
ECP		40	30										
ECP2		40	30										
ECP2M		40	30										
SC		40	30										
MACHXO		40	30										
XP		40	30										
XP2		40	30										

FPGA Multiplexer Resource Usage

M10_B4B1E_SB

4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			10										
Cyclone		30	30										
Cyclone2		30	30										
Cyclone3		30	30										
Stratix		30	30										
Stratix2		10	10										
Stratix3			10										
StratixGX		30	30										
Stratix2GX			10										
Max2		30	30										
Max3000a	10	10											
Max7000b	10	10											
Max7000ae	10	10											
Max7000s	10	10											
Spartan2		30	30										
Spartan2E		30	30										
Spartan3		30	30										
Spartan3A		30	30										
Spartan3ADSP		30	30										
Spartan3AN		30	30										
Spartan3E		30	30										
Spartan3L		30	30										
Virtex		30	30										
Virtex2		30	30										
Virtex2p		30	30										
VirtexE		30	30										
Virtex4		30	30										
Virtex5		20											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		54											
ProAsic3		41											
ProAsic3E		41											
Fusion		40											
EC		40	30										
ECP		40	30										
ECP2		40	30										
ECP2M		40	30										
SC		40	30										
MACHXO		40	30										
XP		40	30										
XP2		40	30										

M10_B8B1

8x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		30	30										
Stratix3			20										
StratixGX		50	50										
Stratix2GX			20										
Max2		50	50										
Max3000a		20	20										
Max7000b		20	20										
Max7000ae		20	20										
Max7000s		20	20										
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		20											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		112											
ProAsic3		73											
ProAsic3E		75											
Fusion		70											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

FPGA Multiplexer Resource Usage

M10_B8B1_SB

8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			20										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		30	30										
Stratix3			20										
StratixGX		50	50										
Stratix2GX			20										
Max2		50	50										
Max3000a		20	20										
Max7000b		20	20										
Max7000ae		20	20										
Max7000s		20	20										
Spartan2		40	40										
Spartan2E		40	40										
Spartan3		40	40										
Spartan3A		40	40										
Spartan3ADSP		40	40										
Spartan3AN		40	40										
Spartan3E		40	40										
Spartan3L		40	40										
Virtex		40	40										
Virtex2		40	40										
Virtex2p		40	40										
VirtexE		40	40										
Virtex4		40	40										
Virtex5		20											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		112											
ProAsic3		73											
ProAsic3E		75											
Fusion		70											
EC		40	40										
ECP		40	40										
ECP2		40	40										
ECP2M		40	40										
SC		40	40										
MACHXO		40	40										
XP		40	40										
XP2		40	40										

M10_B8B1E

8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		30	30										
Stratix3			30										
StratixGX		50	50										
Stratix2GX			30										
Max2		50	50										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		50	50										
Spartan2E		50	50										
Spartan3		50	50										
Spartan3A		50	50										
Spartan3ADSP		50	50										
Spartan3AN		50	50										
Spartan3E		50	50										
Spartan3L		50	50										
Virtex		50	50										
Virtex2		50	50										
Virtex2p		50	50										
VirtexE		50	50										
Virtex4		50	50										
Virtex5		40											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		122											
ProAsic3		83											
ProAsic3E		85											
Fusion		80											
EC		60	50										
ECP		60	50										
ECP2		60	50										
ECP2M		60	50										
SC		60	50										
MACHXO		60	50										
XP		60	50										
XP2		60	50										

FPGA Multiplexer Resource Usage

M10_B8B1E_SB

8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			30										
Cyclone		50	50										
Cyclone2		50	50										
Cyclone3		50	50										
Stratix		50	50										
Stratix2		30	30										
Stratix3			30										
StratixGX		50	50										
Stratix2GX			30										
Max2		50	50										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		50	50										
Spartan2E		50	50										
Spartan3		50	50										
Spartan3A		50	50										
Spartan3ADSP		50	50										
Spartan3AN		50	50										
Spartan3E		50	50										
Spartan3L		50	50										
Virtex		50	50										
Virtex2		50	50										
Virtex2p		50	50										
VirtexE		50	50										
Virtex4		50	50										
Virtex5		38											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		122											
ProAsic3		83											
ProAsic3E		85											
Fusion		80											
EC		60	50										
ECP		60	50										
ECP2		60	50										
ECP2M		60	50										
SC		60	50										
MACHXO		60	50										
XP		60	50										
XP2		60	50										

M10_B16B1

16x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		100	100										
Cyclone2		100	100										
Cyclone3		100	100										
Stratix		100	100										
Stratix2		50	50										
Stratix3			50										
StratixGX		100	100										
Stratix2GX			50										
Max2		100	100										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		90	90										
Spartan2E		90	90										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		90	90										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		90	90										
Virtex4		80	80										
Virtex5		80											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		234											
ProAsic3		153											
ProAsic3E		155											
Fusion													
EC		80	80										
ECP		80	80										
ECP2		80	80										
ECP2M		80	80										
SC		80	80										
MACHXO		80	80										
XP		80	80										
XP2		80	80										

FPGA Multiplexer Resource Usage

M10_B16B1_SB

16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		100	100										
Cyclone2		100	100										
Cyclone3		100	100										
Stratix		100	100										
Stratix2		50	50										
Stratix3			50										
StratixGX		100	100										
Stratix2GX			50										
Max2		100	100										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		90	90										
Spartan2E		90	90										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		90	90										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		90	90										
Virtex4		80	80										
Virtex5		80											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		234											
ProAsic3		153											
ProAsic3E		155											
Fusion													
EC		80	80										
ECP		80	80										
ECP2		80	80										
ECP2M		80	80										
SC		80	80										
MACHXO		80	80										
XP		80	80										
XP2		80	80										

M10_B16B1E

16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		110	110										
Cyclone2		110	110										
Cyclone3		110	110										
Stratix		110	110										
Stratix2		50	50										
Stratix3			50										
StratixGX		110	110										
Stratix2GX			60										
Max2		110	110										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		94	92										
Spartan2E		94	92										
Spartan3		94	92										
Spartan3A		90	90										
Spartan3ADSP		90	90										
Spartan3AN		90	90										
Spartan3E		94	92										
Spartan3L		90	90										
Virtex		94	92										
Virtex2		94	92										
Virtex2p		94	92										
VirtexE		94	92										
Virtex4		94	92										
Virtex5		56											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		234											
ProAsic3		163											
ProAsic3E		165											
Fusion													
EC		100	90										
ECP		100	90										
ECP2		100	90										
ECP2M		100	90										
SC		208	126										
MACHXO		100	90										
XP		208	126										
XP2		100	90										

FPGA Multiplexer Resource Usage

M10_B16B1E_SB

16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			50										
Cyclone		110	110										
Cyclone2		110	110										
Cyclone3		110	110										
Stratix		110	110										
Stratix2		50	50										
Stratix3			50										
StratixGX		110	110										
Stratix2GX			60										
Max2		110	110										
Max3000a	40	40											
Max7000b	40	40											
Max7000ae	40	40											
Max7000s	40	40											
Spartan2		94	92										
Spartan2E		94	92										
Spartan3		94	92										
Spartan3A		90	90										
Spartan3ADSP		90	90										
Spartan3AN		90	90										
Spartan3E		94	92										
Spartan3L		90	90										
Virtex		94	92										
Virtex2		94	92										
Virtex2p		94	92										
VirtexE		94	92										
Virtex4		94	92										
Virtex5		60											
CoolRunner2	10												
CoolRunnerXpla3	10												
Xc9500	10												
Xc9500XL	10												
Xc9500XV	10												
ProAsicPlus		234											
ProAsic3		163											
ProAsic3E		165											
Fusion													
EC		100	90										
ECP		100	90										
ECP2		100	90										
ECP2M		100	90										
SC		208	126										
MACHXO		100	90										
XP		208	126										
XP2		100	90										

M12_B1B2

1x12-Bit Bus to 2x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		24											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		24											
ProAsic3		25											
ProAsic3E		26											
Fusion		24											
EC		48	24										
ECP		48	24										
ECP2		48	24										
ECP2M		48	24										
SC		48	24										
MACHXO		48	24										
XP		48	24										
XP2		48	24										

FPGA Multiplexer Resource Usage

M12_B1B2E

1x12-Bit Bus to 2x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		24	24										
Stratix3			24										
StratixGX		24	24										
Stratix2GX			24										
Max2		24	24										
Max3000a	24	24											
Max7000b	24	24											
Max7000ae	24	24											
Max7000s	24	24											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		22											
CoolRunner2	24												
CoolRunnerXpla3	24												
Xc9500	24												
Xc9500XL	24												
Xc9500XV	24												
ProAsicPlus		26											
ProAsic3		26											
ProAsic3E		28											
Fusion		24											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M12_B1B4

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		48	48										
Stratix3			48										
StratixGX		48	48										
Stratix2GX			48										
Max2		48	48										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		46											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		52											
ProAsic3		54											
ProAsic3E		56											
Fusion		48											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

FPGA Multiplexer Resource Usage

M12_B1B4_SB

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		48	48										
Stratix3			48										
StratixGX		48	48										
Stratix2GX			48										
Max2		48	48										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		46											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		52											
ProAsic3		54											
ProAsic3E		56											
Fusion		48											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

M12_B1B4E

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		48	48										
Stratix3			48										
StratixGX		48	48										
Stratix2GX			48										
Max2		48	48										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		42											
CoolRunner2	48												
CoolRunnerXpla3	48												
Xc9500	48												
Xc9500XL	48												
Xc9500XV	48												
ProAsicPlus		54											
ProAsic3		52											
ProAsic3E		56											
Fusion		52											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

FPGA Multiplexer Resource Usage

M12_B1B4E_SB

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		48										
Cyclone		48	48									
Cyclone2		48	48									
Cyclone3		48	48									
Stratix		48	48									
Stratix2		48	48									
Stratix3			48									
StratixGX		48	48									
Stratix2GX			48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Spartan3A		48	48									
Spartan3ADSP		48	48									
Spartan3AN		48	48									
Spartan3E		48	48									
Spartan3L		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
Virtex5		44										
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		54										
ProAsic3		52										
ProAsic3E		56										
Fusion		52										
EC		48	48									
ECP		48	48									
ECP2		48	48									
ECP2M		48	48									
SC		48	48									
MACHXO		48	48									
XP		48	48									
XP2		48	48									

M12_B1B8

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		96	96										
Cyclone2		96	96										
Cyclone3		96	96										
Stratix		96	96										
Stratix2		96	96										
Stratix3			96										
StratixGX		96	96										
Stratix2GX			96										
Max2		96	96										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		96	96										
Spartan2E		96	96										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		96	96										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		96	96										
Virtex4		96	96										
Virtex5		92											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		108											
ProAsic3		104											
ProAsic3E		104											
Fusion		104											
EC		96	96										
ECP		96	96										
ECP2		96	96										
ECP2M		96	96										
SC		96	96										
MACHXO		96	96										
XP		96	96										
XP2		96	96										

FPGA Multiplexer Resource Usage

M12_B1B8_SB

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		96	96										
Cyclone2		96	96										
Cyclone3		96	96										
Stratix		96	96										
Stratix2		96	96										
Stratix3			96										
StratixGX		96	96										
Stratix2GX			96										
Max2		96	96										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		96	96										
Spartan2E		96	96										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		96	96										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		96	96										
Virtex4		96	96										
Virtex5		88											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		108											
ProAsic3		104											
ProAsic3E		104											
Fusion		104											
EC		96	96										
ECP		96	96										
ECP2		96	96										
ECP2M		96	96										
SC		96	96										
MACHXO		96	96										
XP		96	96										
XP2		96	96										

M12_B1B8E

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		104	104										
Cyclone2		104	104										
Cyclone3		104	104										
Stratix		104	104										
Stratix2		96	96										
Stratix3			96										
StratixGX		104	104										
Stratix2GX			96										
Max2		104	104										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		98	98										
Spartan2E		98	98										
Spartan3		98	98										
Spartan3A		98	98										
Spartan3ADSP		98	98										
Spartan3AN		98	98										
Spartan3E		98	98										
Spartan3L		98	98										
Virtex		98	98										
Virtex2		98	98										
Virtex2p		98	98										
VirtexE		98	98										
Virtex4		98	98										
Virtex5		88											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		110											
ProAsic3		110											
ProAsic3E		112											
Fusion		112											
EC		98	98										
ECP		98	98										
ECP2		98	98										
ECP2M		98	98										
SC		100	98										
MACHXO		98	98										
XP		98	98										
XP2		192	104										

FPGA Multiplexer Resource Usage

M12_B1B8E_SB

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		104	104										
Cyclone2		104	104										
Cyclone3		104	104										
Stratix		104	104										
Stratix2		96	96										
Stratix3			96										
StratixGX		104	104										
Stratix2GX			96										
Max2		104	104										
Max3000a	96	96											
Max7000b	96	96											
Max7000ae	96	96											
Max7000s	96	96											
Spartan2		98	98										
Spartan2E		98	98										
Spartan3		98	98										
Spartan3A		98	98										
Spartan3ADSP		98	98										
Spartan3AN		98	98										
Spartan3E		98	98										
Spartan3L		98	98										
Virtex		98	98										
Virtex2		98	98										
Virtex2p		98	98										
VirtexE		98	98										
Virtex4		98	98										
Virtex5		90											
CoolRunner2	96												
CoolRunnerXpla3	96												
Xc9500	96												
Xc9500XL	96												
Xc9500XV	96												
ProAsicPlus		110											
ProAsic3		110											
ProAsic3E		112											
Fusion		112											
EC		98	98										
ECP		98	98										
ECP2		98	98										
ECP2M		98	98										
SC		100	98										
MACHXO		98	98										
XP		98	98										
XP2		192	104										

M12_B1B16

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			192										
Cyclone		208	208										
Cyclone2		208	208										
Cyclone3		208	208										
Stratix		208	208										
Stratix2		192	192										
Stratix3			192										
StratixGX		208	208										
Stratix2GX			192										
Max2		208	208										
Max3000a	192	192											
Max7000b	192	192											
Max7000ae	192	192											
Max7000s	192	192											
Spartan2		200	200										
Spartan2E		200	200										
Spartan3		200	200										
Spartan3A		204	203										
Spartan3ADSP		198	196										
Spartan3AN		204	203										
Spartan3E		200	200										
Spartan3L		198	196										
Virtex		200	200										
Virtex2		200	200										
Virtex2p		200	200										
VirtexE		200	200										
Virtex4		200	200										
Virtex5		188											
CoolRunner2	192												
CoolRunnerXpla3	192												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		216											
ProAsic3		216											
ProAsic3E		240											
Fusion													
EC		196	196										
ECP		196	196										
ECP2		270	203										
ECP2M		270	203										
SC		272	203										
MACHXO		302	203										
XP		198	196										
XP2		384	208										

FPGA Multiplexer Resource Usage

M12_B1B16_SB

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			192										
Cyclone		208	208										
Cyclone2		208	208										
Cyclone3		208	208										
Stratix		208	208										
Stratix2		192	192										
Stratix3			192										
StratixGX		208	208										
Stratix2GX			192										
Max2		208	208										
Max3000a	192	192											
Max7000b	192	192											
Max7000ae	192	192											
Max7000s	192	192											
Spartan2		202	200										
Spartan2E		202	200										
Spartan3		200	200										
Spartan3A		204	203										
Spartan3ADSP		204	203										
Spartan3AN		204	203										
Spartan3E		202	200										
Spartan3L		204	203										
Virtex		202	200										
Virtex2		200	200										
Virtex2p		200	200										
VirtexE		202	200										
Virtex4		202	200										
Virtex5		190											
CoolRunner2	192												
CoolRunnerXpla3	192												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		216											
ProAsic3		216											
ProAsic3E		240											
Fusion													
EC		196	196										
ECP		196	196										
ECP2		302	203										
ECP2M		302	203										
SC		304	203										
MACHXO		302	203										
XP		198	196										
XP2		384	208										

M12_B1B16E

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			208										
Cyclone		210	210										
Cyclone2		210	210										
Cyclone3		210	210										
Stratix		210	210										
Stratix2		208	208										
Stratix3			208										
StratixGX		210	210										
Stratix2GX			208										
Max2		210	210										
Max3000a	192	192											
Max7000b	192	192											
Max7000ae	192	192											
Max7000s	192	192											
Spartan2		200	200										
Spartan2E		200	200										
Spartan3		200	200										
Spartan3A		212	212										
Spartan3ADSP		196	196										
Spartan3AN		196	196										
Spartan3E		200	200										
Spartan3L		200	200										
Virtex		200	200										
Virtex2		200	200										
Virtex2p		200	200										
VirtexE		200	200										
Virtex4		200	200										
Virtex5		314											
CoolRunner2	192												
CoolRunnerXpla3	192												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		218											
ProAsic3		216											
ProAsic3E		241											
Fusion													
EC		210	210										
ECP		210	210										
ECP2		196	196										
ECP2M		196	196										
SC		198	196										
MACHXO		198	196										
XP		198	196										
XP2		384	210										

FPGA Multiplexer Resource Usage

M12_B1B16E_SB

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			208										
Cyclone		210	210										
Cyclone2		210	210										
Cyclone3		210	210										
Stratix		210	210										
Stratix2		208	208										
Stratix3			208										
StratixGX		210	210										
Stratix2GX			208										
Max2		210	210										
Max3000a	192	192											
Max7000b	192	192											
Max7000ae	192	192											
Max7000s	192	192											
Spartan2		200	200										
Spartan2E		200	200										
Spartan3		200	200										
Spartan3A		212	212										
Spartan3ADSP		196	196										
Spartan3AN		198	196										
Spartan3E		200	200										
Spartan3L		200	200										
Virtex		200	200										
Virtex2		200	200										
Virtex2p		200	200										
VirtexE		200	200										
Virtex4		200	200										
Virtex5		328											
CoolRunner2	192												
CoolRunnerXpla3	192												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		218											
ProAsic3		216											
ProAsic3E		241											
Fusion													
EC		210	210										
ECP		210	210										
ECP2		196	196										
ECP2M		196	196										
SC		198	196										
MACHXO		198	196										
XP		198	196										
XP2		384	210										

M12_B2B1

2x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5			24										
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		12											
ProAsic3		12											
ProAsic3E		12											
Fusion		12											
EC		24	12										
ECP		24	12										
ECP2		24	12										
ECP2M		24	12										
SC		24	12										
MACHXO		24	12										
XP		24	12										
XP2		24	12										

FPGA Multiplexer Resource Usage

M12_B2B1E

2x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		12	12										
Cyclone2		12	12										
Cyclone3		12	12										
Stratix		12	12										
Stratix2		12	12										
Stratix3			12										
StratixGX		12	12										
Stratix2GX			12										
Max2		12	12										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		12	12										
Spartan2E		12	12										
Spartan3		12	12										
Spartan3A		12	12										
Spartan3ADSP		12	12										
Spartan3AN		12	12										
Spartan3E		12	12										
Spartan3L		12	12										
Virtex		12	12										
Virtex2		12	12										
Virtex2p		12	12										
VirtexE		12	12										
Virtex4		12	12										
Virtex5		20											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		24											
ProAsic3		24											
ProAsic3E		24											
Fusion		24											
EC		24	12										
ECP		24	12										
ECP2		24	12										
ECP2M		24	12										
SC		24	12										
MACHXO		24	12										
XP		24	12										
XP2		24	12										

M12_B4B1

4x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		12	12										
Stratix3			12										
StratixGX		24	24										
Stratix2GX			12										
Max2		24	24										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		24											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		52											
ProAsic3		37											
ProAsic3E		38											
Fusion		36											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

FPGA Multiplexer Resource Usage

M12_B4B1_SB

4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		24	24										
Cyclone2		24	24										
Cyclone3		24	24										
Stratix		24	24										
Stratix2		12	12										
Stratix3			12										
StratixGX		24	24										
Stratix2GX			12										
Max2		24	24										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		24	24										
Spartan2E		24	24										
Spartan3		24	24										
Spartan3A		24	24										
Spartan3ADSP		24	24										
Spartan3AN		24	24										
Spartan3E		24	24										
Spartan3L		24	24										
Virtex		24	24										
Virtex2		24	24										
Virtex2p		24	24										
VirtexE		24	24										
Virtex4		24	24										
Virtex5		22											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		52											
ProAsic3		37											
ProAsic3E		38											
Fusion		36											
EC		24	24										
ECP		24	24										
ECP2		24	24										
ECP2M		24	24										
SC		24	24										
MACHXO		24	24										
XP		24	24										
XP2		24	24										

M12_B4B1E

4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		12	12										
Stratix3			12										
StratixGX		36	36										
Stratix2GX			12										
Max2		36	36										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		24											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		64											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		48	36										
ECP		48	36										
ECP2		48	36										
ECP2M		48	36										
SC		48	36										
MACHXO		48	36										
XP		48	36										
XP2		48	36										

FPGA Multiplexer Resource Usage

M12_B4B1E_SB

4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			12										
Cyclone		36	36										
Cyclone2		36	36										
Cyclone3		36	36										
Stratix		36	36										
Stratix2		12	12										
Stratix3			12										
StratixGX		36	36										
Stratix2GX			12										
Max2		36	36										
Max3000a	12	12											
Max7000b	12	12											
Max7000ae	12	12											
Max7000s	12	12											
Spartan2		36	36										
Spartan2E		36	36										
Spartan3		36	36										
Spartan3A		36	36										
Spartan3ADSP		36	36										
Spartan3AN		36	36										
Spartan3E		36	36										
Spartan3L		36	36										
Virtex		36	36										
Virtex2		36	36										
Virtex2p		36	36										
VirtexE		36	36										
Virtex4		36	36										
Virtex5		22											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		64											
ProAsic3		49											
ProAsic3E		50											
Fusion		48											
EC		48	36										
ECP		48	36										
ECP2		48	36										
ECP2M		48	36										
SC		48	36										
MACHXO		48	36										
XP		48	36										
XP2		48	36										

M12_B8B1

8x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		36	36										
Stratix3			24										
StratixGX		60	60										
Stratix2GX			24										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		24											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		132											
ProAsic3		88											
ProAsic3E		90											
Fusion		84											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

FPGA Multiplexer Resource Usage

M12_B8B1_SB

8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			24										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		36	36										
Stratix3			24										
StratixGX		60	60										
Stratix2GX			24										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		24											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		132											
ProAsic3		88											
ProAsic3E		90											
Fusion		84											
EC		48	48										
ECP		48	48										
ECP2		48	48										
ECP2M		48	48										
SC		48	48										
MACHXO		48	48										
XP		48	48										
XP2		48	48										

M12_B8B1E

8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		36	36										
Stratix3			36										
StratixGX		60	60										
Stratix2GX			36										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		60	60										
Spartan2E		60	60										
Spartan3		60	60										
Spartan3A		60	60										
Spartan3ADSP		60	60										
Spartan3AN		60	60										
Spartan3E		60	60										
Spartan3L		60	60										
Virtex		60	60										
Virtex2		60	60										
Virtex2p		60	60										
VirtexE		60	60										
Virtex4		60	60										
Virtex5		46											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500	12												
Xc9500XL	12												
Xc9500XV	12												
ProAsicPlus		144											
ProAsic3		100											
ProAsic3E		102											
Fusion		96											
EC		72	60										
ECP		72	60										
ECP2		72	60										
ECP2M		72	60										
SC		72	60										
MACHXO		72	60										
XP		72	60										
XP2		72	60										

FPGA Multiplexer Resource Usage

M12_B8B1E_SB

8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			36										
Cyclone		60	60										
Cyclone2		60	60										
Cyclone3		60	60										
Stratix		60	60										
Stratix2		36	36										
Stratix3			36										
StratixGX		60	60										
Stratix2GX			36										
Max2		60	60										
Max3000a		24	24										
Max7000b		24	24										
Max7000ae		24	24										
Max7000s		24	24										
Spartan2		60	60										
Spartan2E		60	60										
Spartan3		60	60										
Spartan3A		60	60										
Spartan3ADSP		60	60										
Spartan3AN		60	60										
Spartan3E		60	60										
Spartan3L		60	60										
Virtex		60	60										
Virtex2		60	60										
Virtex2p		60	60										
VirtexE		60	60										
Virtex4		60	60										
Virtex5		48											
CoolRunner2		12											
CoolRunnerXpla3		12											
Xc9500		12											
Xc9500XL		12											
Xc9500XV		12											
ProAsicPlus		144											
ProAsic3		100											
ProAsic3E		102											
Fusion		96											
EC		72	60										
ECP		72	60										
ECP2		72	60										
ECP2M		72	60										
SC		72	60										
MACHXO		72	60										
XP		72	60										
XP2		72	60										

M12_B16B1

16x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			60										
Cyclone		120	120										
Cyclone2		120	120										
Cyclone3		120	120										
Stratix		120	120										
Stratix2		60	60										
Stratix3			60										
StratixGX		120	120										
Stratix2GX			60										
Max2		120	120										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		108	108										
Spartan2E		108	108										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		108	108										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		108	108										
Virtex4		96	96										
Virtex5		96											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		184											
ProAsic3E		186											
Fusion													
EC		96	96										
ECP		96	96										
ECP2		96	96										
ECP2M		96	96										
SC		96	96										
MACHXO		96	96										
XP		96	96										
XP2		96	96										

FPGA Multiplexer Resource Usage

M12_B16B1_SB

16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			60										
Cyclone		120	120										
Cyclone2		120	120										
Cyclone3		120	120										
Stratix		120	120										
Stratix2		60	60										
Stratix3			60										
StratixGX		120	120										
Stratix2GX			60										
Max2		120	120										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		108	108										
Spartan2E		108	108										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		108	108										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		108	108										
Virtex4		96	96										
Virtex5		96											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		184											
ProAsic3E		186											
Fusion													
EC		96	96										
ECP		96	96										
ECP2		96	96										
ECP2M		96	96										
SC		96	96										
MACHXO		96	96										
XP		96	96										
XP2		96	96										

M12_B16B1E

16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			60										
Cyclone		132	132										
Cyclone2		132	132										
Cyclone3		132	132										
Stratix		132	132										
Stratix2		60	60										
Stratix3			60										
StratixGX		132	132										
Stratix2GX			72										
Max2		132	132										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		112	110										
Spartan2E		112	110										
Spartan3		112	110										
Spartan3A		108	108										
Spartan3ADSP		108	108										
Spartan3AN		108	108										
Spartan3E		112	110										
Spartan3L		108	108										
Virtex		112	110										
Virtex2		112	110										
Virtex2p		112	110										
VirtexE		112	110										
Virtex4		112	110										
Virtex5		70											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		196											
ProAsic3E		198											
Fusion													
EC		120	108										
ECP		120	108										
ECP2		120	108										
ECP2M		120	108										
SC		252	152										
MACHXO		120	108										
XP		252	152										
XP2		120	108										

FPGA Multiplexer Resource Usage

M12_B16B1E_SB

16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			60										
Cyclone		132	132										
Cyclone2		132	132										
Cyclone3		132	132										
Stratix		132	132										
Stratix2		60	60										
Stratix3			60										
StratixGX		132	132										
Stratix2GX			72										
Max2		132	132										
Max3000a	48	48											
Max7000b	48	48											
Max7000ae	48	48											
Max7000s	48	48											
Spartan2		112	110										
Spartan2E		112	110										
Spartan3		112	110										
Spartan3A		108	108										
Spartan3ADSP		108	108										
Spartan3AN		108	108										
Spartan3E		112	110										
Spartan3L		108	108										
Virtex		112	110										
Virtex2		112	110										
Virtex2p		112	110										
VirtexE		112	110										
Virtex4		112	110										
Virtex5		72											
CoolRunner2	12												
CoolRunnerXpla3	12												
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		196											
ProAsic3E		198											
Fusion													
EC		120	108										
ECP		120	108										
ECP2		120	108										
ECP2M		120	108										
SC		252	152										
MACHXO		120	108										
XP		252	152										
XP2		120	108										

M16_B1B2

1x16-Bit Bus to 2x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		30											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		32											
ProAsic3		34											
ProAsic3E		34											
Fusion		32											
EC		64	32										
ECP		64	32										
ECP2		64	32										
ECP2M		64	32										
SC		64	32										
MACHXO		64	32										
XP		64	32										
XP2		64	32										

FPGA Multiplexer Resource Usage

M16_B1B2E

1x16-Bit Bus to 2x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		24											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		34											
ProAsic3		36											
ProAsic3E		36											
Fusion		32											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M16_B1B4

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		62											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		68											
ProAsic3		64											
ProAsic3E		64											
Fusion		64											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M16_B1B4_SB

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		64											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		68											
ProAsic3		64											
ProAsic3E		64											
Fusion		64											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

M16_B1B4E

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		64											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		70											
ProAsic3		68											
ProAsic3E		70											
Fusion		71											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M16_B1B4E_SB

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements 4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx		64										
Cyclone		64	64									
Cyclone2		64	64									
Cyclone3		64	64									
Stratix		64	64									
Stratix2		64	64									
Stratix3			64									
StratixGX		64	64									
Stratix2GX			64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Spartan3A		64	64									
Spartan3ADSP		64	64									
Spartan3AN		64	64									
Spartan3E		64	64									
Spartan3L		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
Virtex5		62										
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		70										
ProAsic3		68										
ProAsic3E		70										
Fusion		71										
EC		64	64									
ECP		64	64									
ECP2		64	64									
ECP2M		64	64									
SC		64	64									
MACHXO		64	64									
XP		64	64									
XP2		64	64									

M16_B1B8

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		134	134										
Spartan2E		134	134										
Spartan3		134	134										
Spartan3A		132	132										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		136	134										
Virtex		134	134										
Virtex2		134	134										
Virtex2p		134	134										
VirtexE		134	134										
Virtex4		134	134										
Virtex5		140											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		140											
ProAsic3		136											
ProAsic3E		140											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

FPGA Multiplexer Resource Usage

M16_B1B8_SB

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		134	134										
Spartan2E		134	134										
Spartan3		134	134										
Spartan3A		134	132										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		136	134										
Virtex		134	134										
Virtex2		134	134										
Virtex2p		134	134										
VirtexE		134	134										
Virtex4		134	134										
Virtex5		124											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		140											
ProAsic3		136											
ProAsic3E		140											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

M16_B1B8E

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		136	136										
Cyclone2		136	136										
Cyclone3		136	136										
Stratix		136	136										
Stratix2		128	128										
Stratix3			128										
StratixGX		136	136										
Stratix2GX			128										
Max2		136	136										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		134	134										
Spartan2E		134	134										
Spartan3		134	134										
Spartan3A		130	130										
Spartan3ADSP		130	130										
Spartan3AN		130	130										
Spartan3E		136	134										
Spartan3L		134	134										
Virtex		134	134										
Virtex2		134	134										
Virtex2p		134	134										
VirtexE		134	134										
Virtex4		134	134										
Virtex5		132											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		142											
ProAsic3		138											
ProAsic3E		142											
Fusion													
EC		130	130										
ECP		130	130										
ECP2		130	130										
ECP2M		130	130										
SC		132	130										
MACHXO		130	130										
XP		130	130										
XP2		256	136										

FPGA Multiplexer Resource Usage

M16_B1B8E_SB

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		136	136										
Cyclone2		136	136										
Cyclone3		136	136										
Stratix		136	136										
Stratix2		128	128										
Stratix3			128										
StratixGX		136	136										
Stratix2GX			128										
Max2		136	136										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		134	134										
Spartan2E		134	134										
Spartan3		134	134										
Spartan3A		130	130										
Spartan3ADSP		130	130										
Spartan3AN		130	130										
Spartan3E		136	134										
Spartan3L		134	134										
Virtex		134	134										
Virtex2		134	134										
Virtex2p		134	134										
VirtexE		134	134										
Virtex4		134	134										
Virtex5		122											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		142											
ProAsic3		138											
ProAsic3E		142											
Fusion													
EC		130	130										
ECP		130	130										
ECP2		130	130										
ECP2M		130	130										
SC		132	130										
MACHXO		130	130										
XP		130	130										
XP2		256	136										

M16_B1B16

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		272	272										
Cyclone2		272	272										
Cyclone3		272	272										
Stratix		272	272										
Stratix2		256	256										
Stratix3			256										
StratixGX		272	272										
Stratix2GX			256										
Max2		272	272										
Max3000a	256	256											
Max7000b	256	256											
Max7000ae	256	256											
Max7000s	256	256											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		266	265										
Spartan3ADSP		260	260										
Spartan3AN		264	263										
Spartan3E		268	266										
Spartan3L		266	264										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		274											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		280											
ProAsic3		270											
ProAsic3E		292											
Fusion													
EC		260	260										
ECP		260	260										
ECP2		358	267										
ECP2M		358	267										
SC		358	267										
MACHXO		358	267										
XP		262	260										
XP2		512	272										

FPGA Multiplexer Resource Usage

M16_B1B16_SB

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		272	272										
Cyclone2		272	272										
Cyclone3		272	272										
Stratix		272	272										
Stratix2		256	256										
Stratix3			256										
StratixGX		272	272										
Stratix2GX			256										
Max2		272	272										
Max3000a	256	256											
Max7000b	256	256											
Max7000ae	256	256											
Max7000s	256	256											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		268	267										
Spartan3ADSP		268	267										
Spartan3AN		268	267										
Spartan3E		268	266										
Spartan3L		268	267										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		260											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		280											
ProAsic3		270											
ProAsic3E		292											
Fusion													
EC		260	260										
ECP		260	260										
ECP2		402	267										
ECP2M		402	267										
SC		404	267										
MACHXO		402	267										
XP		262	260										
XP2		512	272										

M16_B1B16E

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			272										
Cyclone		274	274										
Cyclone2		274	274										
Cyclone3		274	274										
Stratix		274	274										
Stratix2		272	272										
Stratix3			272										
StratixGX		274	274										
Stratix2GX			272										
Max2		274	274										
Max3000a	272	272											
Max7000b	272	272											
Max7000ae	272	272											
Max7000s	272	272											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		260	260										
Spartan3ADSP		260	260										
Spartan3AN		260	260										
Spartan3E		268	266										
Spartan3L		268	266										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		408											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		282											
ProAsic3		270											
ProAsic3E		293											
Fusion													
EC		274	274										
ECP		274	274										
ECP2		260	260										
ECP2M		260	260										
SC		262	260										
MACHXO		262	260										
XP		262	260										
XP2		512	274										

FPGA Multiplexer Resource Usage

M16_B1B16E_SB

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			272										
Cyclone		274	274										
Cyclone2		274	274										
Cyclone3		274	274										
Stratix		274	274										
Stratix2		272	272										
Stratix3			272										
StratixGX		274	274										
Stratix2GX			272										
Max2		274	274										
Max3000a	272	272											
Max7000b	272	272											
Max7000ae	272	272											
Max7000s	272	272											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		260	260										
Spartan3ADSP		264	262										
Spartan3AN		260	260										
Spartan3E		268	266										
Spartan3L		264	262										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		420											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		282											
ProAsic3		270											
ProAsic3E		293											
Fusion													
EC		274	274										
ECP		274	274										
ECP2		260	260										
ECP2M		260	260										
SC		262	260										
MACHXO		262	260										
XP		514	274										
XP2		514	274										

M16_B2B1

2x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		32											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		16											
ProAsic3		16											
ProAsic3E		17											
Fusion		16											
EC		32	16										
ECP		32	16										
ECP2		32	16										
ECP2M		32	16										
SC		32	16										
MACHXO		32	16										
XP		32	16										
XP2		32	16										

FPGA Multiplexer Resource Usage

M16_B2B1E

2x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		16	16										
Cyclone2		16	16										
Cyclone3		16	16										
Stratix		16	16										
Stratix2		16	16										
Stratix3			16										
StratixGX		16	16										
Stratix2GX			16										
Max2		16	16										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		16	16										
Spartan2E		16	16										
Spartan3		16	16										
Spartan3A		16	16										
Spartan3ADSP		16	16										
Spartan3AN		16	16										
Spartan3E		16	16										
Spartan3L		16	16										
Virtex		16	16										
Virtex2		16	16										
Virtex2p		16	16										
VirtexE		16	16										
Virtex4		16	16										
Virtex5		28											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		32											
ProAsic3		32											
ProAsic3E		34											
Fusion		32											
EC		32	16										
ECP		32	16										
ECP2		32	16										
ECP2M		32	16										
SC		32	16										
MACHXO		32	16										
XP		32	16										
XP2		32	16										

M16_B4B1

4x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		16	16										
Stratix3			16										
StratixGX		32	32										
Stratix2GX			16										
Max2		32	32										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		32											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		68											
ProAsic3		50											
ProAsic3E		51											
Fusion		48											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

FPGA Multiplexer Resource Usage

M16_B4B1_SB

4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		16	16										
Stratix3			16										
StratixGX		32	32										
Stratix2GX			16										
Max2		32	32										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		32											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		68											
ProAsic3		50											
ProAsic3E		51											
Fusion		48											
EC		32	32										
ECP		32	32										
ECP2		32	32										
ECP2M		32	32										
SC		32	32										
MACHXO		32	32										
XP		32	32										
XP2		32	32										

M16_B4B1E

4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		16	16										
Stratix3			16										
StratixGX		48	48										
Stratix2GX			16										
Max2		48	48										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		28											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		84											
ProAsic3		66											
ProAsic3E		68											
Fusion		64											
EC		64	48										
ECP		64	48										
ECP2		64	48										
ECP2M		64	48										
SC		64	48										
MACHXO		64	48										
XP		64	48										
XP2		64	48										

FPGA Multiplexer Resource Usage

M16_B4B1E_SB

4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			16										
Cyclone		48	48										
Cyclone2		48	48										
Cyclone3		48	48										
Stratix		48	48										
Stratix2		16	16										
Stratix3			16										
StratixGX		48	48										
Stratix2GX			16										
Max2		48	48										
Max3000a	16	16											
Max7000b	16	16											
Max7000ae	16	16											
Max7000s	16	16											
Spartan2		48	48										
Spartan2E		48	48										
Spartan3		48	48										
Spartan3A		48	48										
Spartan3ADSP		48	48										
Spartan3AN		48	48										
Spartan3E		48	48										
Spartan3L		48	48										
Virtex		48	48										
Virtex2		48	48										
Virtex2p		48	48										
VirtexE		48	48										
Virtex4		48	48										
Virtex5		32											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		84											
ProAsic3		66											
ProAsic3E		68											
Fusion		64											
EC		64	48										
ECP		64	48										
ECP2		64	48										
ECP2M		64	48										
SC		64	48										
MACHXO		64	48										
XP		64	48										
XP2		64	48										

M16_B8B1

8x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		48	48										
Stratix3			32										
StratixGX		80	80										
Stratix2GX			32										
Max2		80	80										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		32											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		172											
ProAsic3		114											
ProAsic3E		115											
Fusion													
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M16_B8B1_SB

8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		48	48										
Stratix3			32										
StratixGX		80	80										
Stratix2GX			32										
Max2		80	80										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5			32										
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		172											
ProAsic3		114											
ProAsic3E		115											
Fusion													
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

M16_B8B1E

8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		48	48										
Stratix3			48										
StratixGX		80	80										
Stratix2GX			48										
Max2		80	80										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		80	80										
Spartan2E		80	80										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		80	80										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		80	80										
Virtex4		80	80										
Virtex5		62											
CoolRunner2		16											
CoolRunnerXpla3		16											
Xc9500		16											
Xc9500XL		16											
Xc9500XV		16											
ProAsicPlus		188											
ProAsic3		130											
ProAsic3E		132											
Fusion													
EC		96	80										
ECP		96	80										
ECP2		96	80										
ECP2M		96	80										
SC		96	80										
MACHXO		96	80										
XP		96	80										
XP2		96	80										

FPGA Multiplexer Resource Usage

M16_B8B1E_SB

8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			48										
Cyclone		80	80										
Cyclone2		80	80										
Cyclone3		80	80										
Stratix		80	80										
Stratix2		48	48										
Stratix3			48										
StratixGX		80	80										
Stratix2GX			48										
Max2		80	80										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		80	80										
Spartan2E		80	80										
Spartan3		80	80										
Spartan3A		80	80										
Spartan3ADSP		80	80										
Spartan3AN		80	80										
Spartan3E		80	80										
Spartan3L		80	80										
Virtex		80	80										
Virtex2		80	80										
Virtex2p		80	80										
VirtexE		80	80										
Virtex4		80	80										
Virtex5		64											
CoolRunner2	16												
CoolRunnerXpla3	16												
Xc9500	16												
Xc9500XL	16												
Xc9500XV	16												
ProAsicPlus		188											
ProAsic3		130											
ProAsic3E		132											
Fusion													
EC		96	80										
ECP		96	80										
ECP2		96	80										
ECP2M		96	80										
SC		96	80										
MACHXO		96	80										
XP		96	80										
XP2		96	80										

M16_B16B1

16x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		80	80										
Stratix3			80										
StratixGX		160	160										
Stratix2GX			80										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		146	146										
Spartan2E		146	146										
Spartan3		130	130										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		130	130										
Spartan3L		130	130										
Virtex		146	146										
Virtex2		130	130										
Virtex2p		130	130										
VirtexE		146	146										
Virtex4		130	130										
Virtex5		128											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		360											
ProAsic3		242											
ProAsic3E		243											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

FPGA Multiplexer Resource Usage

M16_B16B1_SB

16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		80	80										
Stratix3			80										
StratixGX		160	160										
Stratix2GX			80										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		146	146										
Spartan2E		146	146										
Spartan3		130	130										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		130	130										
Spartan3L		130	130										
Virtex		146	146										
Virtex2		130	130										
Virtex2p		130	130										
VirtexE		146	146										
Virtex4		130	130										
Virtex5		126											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		360											
ProAsic3													
ProAsic3E													
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

M16_B16B1E

16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		176	176										
Cyclone2		176	176										
Cyclone3		176	176										
Stratix		176	176										
Stratix2		80	80										
Stratix3			80										
StratixGX		176	176										
Stratix2GX			96										
Max2		176	176										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		148	146										
Spartan2E		148	146										
Spartan3		148	146										
Spartan3A		144	144										
Spartan3ADSP		144	144										
Spartan3AN		144	144										
Spartan3E		148	146										
Spartan3L		146	146										
Virtex		148	146										
Virtex2		148	146										
Virtex2p		148	146										
VirtexE		148	146										
Virtex4		148	146										
Virtex5		96											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		360											
ProAsic3		258											
ProAsic3E		260											
Fusion													
EC		160	144										
ECP		160	144										
ECP2		160	144										
ECP2M		160	144										
SC		340	204										
MACHXO		160	144										
XP		340	204										
XP2		160	144										

FPGA Multiplexer Resource Usage

M16_B16B1E_SB

16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			80										
Cyclone		176	176										
Cyclone2		176	176										
Cyclone3		176	176										
Stratix		176	176										
Stratix2		80	80										
Stratix3			80										
StratixGX		176	176										
Stratix2GX			96										
Max2		176	176										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		148	146										
Spartan2E		148	146										
Spartan3		148	146										
Spartan3A		144	144										
Spartan3ADSP		144	144										
Spartan3AN		144	144										
Spartan3E		148	146										
Spartan3L		146	146										
Virtex		148	146										
Virtex2		148	146										
Virtex2p		148	146										
VirtexE		148	146										
Virtex4		148	146										
Virtex5		96											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		360											
ProAsic3		258											
ProAsic3E		260											
Fusion													
EC		160	144										
ECP		160	144										
ECP2		160	144										
ECP2M		160	144										
SC		340	204										
MACHXO		160	144										
XP		340	204										
XP2		160	144										

M32_B1B2

1x32-Bit Bus to 2x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		64											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		64											
ProAsic3		64											
ProAsic3E		64											
Fusion		64											
EC		128	64										
ECP		128	64										
ECP2		128	64										
ECP2M		128	64										
SC		128	64										
MACHXO		128	64										
XP		128	64										
XP2		128	64										

FPGA Multiplexer Resource Usage

M32_B1B2E

1x32-Bit Bus to 2x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		64	64										
Stratix3			64										
StratixGX		64	64										
Stratix2GX			64										
Max2		64	64										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5		58											
CoolRunner2	64												
CoolRunnerXpla3	64												
Xc9500	64												
Xc9500XL	64												
Xc9500XV	64												
ProAsicPlus		68											
ProAsic3		64											
ProAsic3E		64											
Fusion		64											
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

M32_B1B4

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		128	128										
Spartan2E		128	128										
Spartan3		128	128										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		134	132										
Virtex		128	128										
Virtex2		128	128										
Virtex2p		128	128										
VirtexE		128	128										
Virtex4		128	128										
Virtex5		182											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		136											
ProAsic3		128											
ProAsic3E		128											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

FPGA Multiplexer Resource Usage

M32_B1B4_SB

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		128	128										
Spartan2E		128	128										
Spartan3		128	128										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		134	132										
Virtex		128	128										
Virtex2		128	128										
Virtex2p		128	128										
VirtexE		128	128										
Virtex4		128	128										
Virtex5		172											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		136											
ProAsic3		128											
ProAsic3E		128											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

M32_B1B4E

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	132	132											
Max7000b	132	132											
Max7000ae	132	132											
Max7000s	132	132											
Spartan2		136	134										
Spartan2E		136	134										
Spartan3		136	134										
Spartan3A		132	132										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		128	128										
Virtex		136	134										
Virtex2		136	134										
Virtex2p		136	134										
VirtexE		136	134										
Virtex4		134	134										
Virtex5		166											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		138											
ProAsic3		136											
ProAsic3E		130											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

FPGA Multiplexer Resource Usage

M32_B1B4E_SB

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			128										
Cyclone		128	128										
Cyclone2		128	128										
Cyclone3		128	128										
Stratix		128	128										
Stratix2		128	128										
Stratix3			128										
StratixGX		128	128										
Stratix2GX			128										
Max2		128	128										
Max3000a	132	132											
Max7000b	132	132											
Max7000ae	132	132											
Max7000s	132	132											
Spartan2		136	134										
Spartan2E		136	134										
Spartan3		136	134										
Spartan3A		132	132										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		128	128										
Spartan3L		128	128										
Virtex		136	134										
Virtex2		136	134										
Virtex2p		136	134										
VirtexE		136	134										
Virtex4		134	134										
Virtex5		166											
CoolRunner2	128												
CoolRunnerXpla3	128												
Xc9500	128												
Xc9500XL	128												
Xc9500XV	128												
ProAsicPlus		138											
ProAsic3		136											
ProAsic3E		130											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

M32_B1B8

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		256	256										
Cyclone2		256	256										
Cyclone3		256	256										
Stratix		256	256										
Stratix2		256	256										
Stratix3			256										
StratixGX		256	256										
Stratix2GX			256										
Max2		256	256										
Max3000a	256	256											
Max7000b	256	256											
Max7000ae	256	256											
Max7000s	256	256											
Spartan2		268	265										
Spartan2E		268	265										
Spartan3		268	265										
Spartan3A		264	264										
Spartan3ADSP		256	256										
Spartan3AN		256	256										
Spartan3E		256	256										
Spartan3L		256	256										
Virtex		268	265										
Virtex2		268	265										
Virtex2p		268	265										
VirtexE		268	265										
Virtex4		268	265										
Virtex5		302											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		272											
ProAsic3E		260											
Fusion													
EC		256	256										
ECP		256	256										
ECP2		256	256										
ECP2M		256	256										
SC		256	256										
MACHXO		256	256										
XP		256	256										
XP2		256	256										

FPGA Multiplexer Resource Usage

M32_B1B8_SB

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		256	256										
Cyclone2		256	256										
Cyclone3		256	256										
Stratix		256	256										
Stratix2		256	256										
Stratix3			256										
StratixGX		256	256										
Stratix2GX			256										
Max2		256	256										
Max3000a	256	256											
Max7000b	256	256											
Max7000ae	256	256											
Max7000s	256	256											
Spartan2		268	265										
Spartan2E		268	265										
Spartan3		268	265										
Spartan3A		264	264										
Spartan3ADSP		256	256										
Spartan3AN		256	256										
Spartan3E		256	256										
Spartan3L		256	256										
Virtex		268	265										
Virtex2		268	265										
Virtex2p		268	265										
VirtexE		268	265										
Virtex4		268	265										
Virtex5		296											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		276											
ProAsic3		272											
ProAsic3E		260											
Fusion													
EC		256	256										
ECP		256	256										
ECP2		256	256										
ECP2M		256	256										
SC		256	256										
MACHXO		256	256										
XP		256	256										
XP2		256	256										

M32_B1B8E

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		264	264										
Cyclone2		272	272										
Cyclone3		264	264										
Stratix		264	264										
Stratix2		256	256										
Stratix3			256										
StratixGX		264	264										
Stratix2GX			256										
Max2		264	264										
Max3000a	264	264											
Max7000b	264	264											
Max7000ae	264	264											
Max7000s	264	264											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		258	258										
Spartan3ADSP		258	258										
Spartan3AN		258	258										
Spartan3E		268	266										
Spartan3L		268	266										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		294											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		278											
ProAsic3		262											
ProAsic3E		262											
Fusion													
EC		266	266										
ECP		266	266										
ECP2		264	264										
ECP2M		264	264										
SC		266	264										
MACHXO		264	264										
XP		264	264										
XP2		512	264										

FPGA Multiplexer Resource Usage

M32_B1B8E_SB

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			256										
Cyclone		264	264										
Cyclone2		272	272										
Cyclone3		264	264										
Stratix		264	264										
Stratix2		256	256										
Stratix3			256										
StratixGX		264	264										
Stratix2GX			256										
Max2		264	264										
Max3000a	264	264											
Max7000b	264	264											
Max7000ae	264	264											
Max7000s	264	264											
Spartan2		268	266										
Spartan2E		268	266										
Spartan3		268	266										
Spartan3A		258	258										
Spartan3ADSP		258	258										
Spartan3AN		258	258										
Spartan3E		268	266										
Spartan3L		268	266										
Virtex		268	266										
Virtex2		268	266										
Virtex2p		268	266										
VirtexE		268	266										
Virtex4		268	266										
Virtex5		304											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		278											
ProAsic3		262											
ProAsic3E		262											
Fusion													
EC		266	266										
ECP		266	266										
ECP2		264	264										
ECP2M		264	264										
SC		266	264										
MACHXO		264	264										
XP		264	264										
XP2		512	264										

M32_B1B16

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			512										
Cyclone		528	528										
Cyclone2		545	544										
Cyclone3		528	528										
Stratix		528	528										
Stratix2		513	512										
Stratix3			512										
StratixGX		529	528									2	
Stratix2GX			512										
Max2		528	528										
Max3000a	512	512											
Max7000b	512	512											
Max7000ae	512	512											
Max7000s	512	512											
Spartan2		534	532										
Spartan2E		534	532										
Spartan3		534	532										
Spartan3A		550	521										
Spartan3ADSP		516	516										
Spartan3AN		518	517										
Spartan3E		534	532										
Spartan3L		538	523										
Virtex		534	532										
Virtex2		534	532										
Virtex2p		534	532										
VirtexE		534	532										
Virtex4		534	532										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		552											
ProAsic3		536											
ProAsic3E		576											
Fusion													
EC		540	540										
ECP		540	540										
ECP2		676	522										
ECP2M		676	522										
SC		678	522										
MACHXO		650	524										
XP		532	530										
XP2		1024	528										

FPGA Multiplexer Resource Usage

M32_B1B16_SB

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			512										
Cyclone		528	528										
Cyclone2		545	544										
Cyclone3		528	528										
Stratix		528	528										
Stratix2		513	512										
Stratix3			512										
StratixGX		529	528									2	
Stratix2GX			512										
Max2		528	528										
Max3000a	512	512											
Max7000b	512	512											
Max7000ae	512	512											
Max7000s	512	512											
Spartan2		534	532										
Spartan2E		534	532										
Spartan3		534	532										
Spartan3A		524	523										
Spartan3ADSP		548	523										
Spartan3AN		524	523										
Spartan3E		532	532										
Spartan3L		534	533										
Virtex		534	532										
Virtex2		534	532										
Virtex2p		534	532										
VirtexE		534	532										
Virtex4		534	532										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		552											
ProAsic3		536											
ProAsic3E		576											
Fusion													
EC		532	532										
ECP		532	532										
ECP2		804	526										
ECP2M		804	526										
SC		808	526										
MACHXO		804	526										
XP		532	530										
XP2		1024	528										

M32_B1B16E

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			528										
Cyclone		530	530										
Cyclone2		547	546										
Cyclone3		530	530										
Stratix		530	530										
Stratix2		529	528										
Stratix3			544										
StratixGX		531	530									2	
Stratix2GX			544										
Max2		530	530										
Max3000a	528	528											
Max7000b	528	528											
Max7000ae	528	528											
Max7000s	528	528											
Spartan2		534	532										
Spartan2E		534	532										
Spartan3		532	529										
Spartan3A		516	516										
Spartan3ADSP		516	516										
Spartan3AN		516	516										
Spartan3E		532	532										
Spartan3L		532	532										
Virtex		534	532										
Virtex2		532	529										
Virtex2p		532	529										
VirtexE		534	532										
Virtex4		532	529										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		554											
ProAsic3		537											
ProAsic3E		578											
Fusion													
EC		530	530										
ECP		530	530										
ECP2		584	580										
ECP2M		584	580										
SC		586	580										
MACHXO		580	580										
XP		528	528										
XP2		1024	530										

FPGA Multiplexer Resource Usage

M32_B1B16E_SB

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			528										
Cyclone		530	530										
Cyclone2		547	546										
Cyclone3		530	530										
Stratix		530	530										
Stratix2		529	528										
Stratix3			544										
StratixGX		531	530									2	
Stratix2GX			544										
Max2		530	530										
Max3000a	528	528											
Max7000b	528	528											
Max7000ae	528	528											
Max7000s	528	528											
Spartan2		532	532										
Spartan2E		532	532										
Spartan3		532	532										
Spartan3A		516	516										
Spartan3ADSP		518	516										
Spartan3AN		516	516										
Spartan3E		530	530										
Spartan3L		518	516										
Virtex		532	532										
Virtex2		532	532										
Virtex2p		532	532										
VirtexE		532	532										
Virtex4		532	532										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		554											
ProAsic3		537											
ProAsic3E		578											
Fusion													
EC		530	530										
ECP		530	530										
ECP2		528	528										
ECP2M		528	528										
SC		528	528										
MACHXO		530	529										
XP		1026	530										
XP2		1026	530										

M32_B2B1

2x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		58											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		32											
ProAsic3		34											
ProAsic3E		34											
Fusion		32											
EC		64	32										
ECP		64	32										
ECP2		64	32										
ECP2M		64	32										
SC		64	32										
MACHXO		64	32										
XP		64	32										
XP2		64	32										

FPGA Multiplexer Resource Usage

M32_B2B1E

2x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		32	32										
Cyclone2		32	32										
Cyclone3		32	32										
Stratix		32	32										
Stratix2		32	32										
Stratix3			32										
StratixGX		32	32										
Stratix2GX			32										
Max2		32	32										
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2		32	32										
Spartan2E		32	32										
Spartan3		32	32										
Spartan3A		32	32										
Spartan3ADSP		32	32										
Spartan3AN		32	32										
Spartan3E		32	32										
Spartan3L		32	32										
Virtex		32	32										
Virtex2		32	32										
Virtex2p		32	32										
VirtexE		32	32										
Virtex4		32	32										
Virtex5		60											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		88											
ProAsic3		68											
ProAsic3E		68											
Fusion		64											
EC		64	32										
ECP		64	32										
ECP2		64	32										
ECP2M		64	32										
SC		64	32										
MACHXO		64	32										
XP		64	32										
XP2		64	32										

M32_B4B1

4x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		64	64										
Cyclone2		64	64										
Cyclone3		64	64										
Stratix		64	64										
Stratix2		32	32										
Stratix3			32										
StratixGX		64	64										
Stratix2GX			32										
Max2		64	64										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		64	64										
Spartan2E		64	64										
Spartan3		64	64										
Spartan3A		64	64										
Spartan3ADSP		64	64										
Spartan3AN		64	64										
Spartan3E		64	64										
Spartan3L		64	64										
Virtex		64	64										
Virtex2		64	64										
Virtex2p		64	64										
VirtexE		64	64										
Virtex4		64	64										
Virtex5			62										
CoolRunner2		32											
CoolRunnerXpla3		32											
Xc9500		32											
Xc9500XL		32											
Xc9500XV		32											
ProAsicPlus			136										
ProAsic3			98										
ProAsic3E			98										
Fusion													
EC		64	64										
ECP		64	64										
ECP2		64	64										
ECP2M		64	64										
SC		64	64										
MACHXO		64	64										
XP		64	64										
XP2		64	64										

FPGA Multiplexer Resource Usage

M32_B4B1_SB

4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone	64	64											
Cyclone2	64	64											
Cyclone3	64	64											
Stratix	64	64											
Stratix2	32	32											
Stratix3		32											
StratixGX	64	64											
Stratix2GX		32											
Max2	64	64											
Max3000a	32	32											
Max7000b	32	32											
Max7000ae	32	32											
Max7000s	32	32											
Spartan2	64	64											
Spartan2E	64	64											
Spartan3	64	64											
Spartan3A	64	64											
Spartan3ADSP	64	64											
Spartan3AN	64	64											
Spartan3E	64	64											
Spartan3L	64	64											
Virtex	64	64											
Virtex2	64	64											
Virtex2p	64	64											
VirtexE	64	64											
Virtex4	64	64											
Virtex5		62											
CoolRunner2	32												
CoolRunnerXpla3	32												
Xc9500	32												
Xc9500XL	32												
Xc9500XV	32												
ProAsicPlus		136											
ProAsic3		98											
ProAsic3E		98											
Fusion													
EC	64	64											
ECP	64	64											
ECP2	64	64											
ECP2M	64	64											
SC	64	64											
MACHXO	64	64											
XP	64	64											
XP2	64	64											

M32_B4B1E

4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		96	96										
Cyclone2		96	96										
Cyclone3		96	96										
Stratix		96	96										
Stratix2		32	32										
Stratix3			32										
StratixGX		96	96										
Stratix2GX			32										
Max2		96	96										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		96	96										
Spartan2E		96	96										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		96	96										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		96	96										
Virtex4		96	96										
Virtex5			64										
CoolRunner2		32											
CoolRunnerXpla3		32											
Xc9500		32											
Xc9500XL		32											
Xc9500XV		32											
ProAsicPlus			168										
ProAsic3			132										
ProAsic3E			132										
Fusion													
EC		128	96										
ECP		128	96										
ECP2		128	96										
ECP2M		128	96										
SC		128	96										
MACHXO		128	96										
XP		128	96										
XP2		128	96										

FPGA Multiplexer Resource Usage

M32_B4B1E_SB

4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			32										
Cyclone		96	96										
Cyclone2		96	96										
Cyclone3		96	96										
Stratix		96	96										
Stratix2		32	32										
Stratix3			32										
StratixGX		96	96										
Stratix2GX			32										
Max2		96	96										
Max3000a		32	32										
Max7000b		32	32										
Max7000ae		32	32										
Max7000s		32	32										
Spartan2		96	96										
Spartan2E		96	96										
Spartan3		96	96										
Spartan3A		96	96										
Spartan3ADSP		96	96										
Spartan3AN		96	96										
Spartan3E		96	96										
Spartan3L		96	96										
Virtex		96	96										
Virtex2		96	96										
Virtex2p		96	96										
VirtexE		96	96										
Virtex4		96	96										
Virtex5			62										
CoolRunner2		32											
CoolRunnerXpla3		32											
Xc9500		32											
Xc9500XL		32											
Xc9500XV		32											
ProAsicPlus			168										
ProAsic3			132										
ProAsic3E			132										
Fusion													
EC		128	96										
ECP		128	96										
ECP2		128	96										
ECP2M		128	96										
SC		128	96										
MACHXO		128	96										
XP		128	96										
XP2		128	96										

M32_B8B1

8x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		96	96										
Stratix3			64										
StratixGX		160	160										
Stratix2GX			64										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		130	130										
Spartan2E		130	130										
Spartan3		130	130										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		130	130										
Spartan3L		130	130										
Virtex		130	130										
Virtex2		130	130										
Virtex2p		130	130										
VirtexE		130	130										
Virtex4		130	130										
Virtex5		64											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		340											
ProAsic3		226											
ProAsic3E		226											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

FPGA Multiplexer Resource Usage

M32_B8B1_SB

8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			64										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		96	96										
Stratix3			64										
StratixGX		160	160										
Stratix2GX			64										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		130	130										
Spartan2E		130	130										
Spartan3		130	130										
Spartan3A		128	128										
Spartan3ADSP		128	128										
Spartan3AN		128	128										
Spartan3E		130	130										
Spartan3L		130	130										
Virtex		130	130										
Virtex2		130	130										
Virtex2p		130	130										
VirtexE		130	130										
Virtex4		130	130										
Virtex5		64											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		340											
ProAsic3		226											
ProAsic3E		226											
Fusion													
EC		128	128										
ECP		128	128										
ECP2		128	128										
ECP2M		128	128										
SC		128	128										
MACHXO		128	128										
XP		128	128										
XP2		128	128										

M32_B8B1E

8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		96	96										
Stratix3			96										
StratixGX		160	160										
Stratix2GX			96										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		162	162										
Spartan2E		162	162										
Spartan3		162	162										
Spartan3A		160	160										
Spartan3ADSP		160	160										
Spartan3AN		160	160										
Spartan3E		162	162										
Spartan3L		162	162										
Virtex		162	162										
Virtex2		162	162										
Virtex2p		162	162										
VirtexE		162	162										
Virtex4		162	162										
Virtex5		128											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		372											
ProAsic3		260											
ProAsic3E		260											
Fusion													
EC		192	160										
ECP		192	160										
ECP2		192	160										
ECP2M		192	160										
SC		192	160										
MACHXO		192	160										
XP		192	160										
XP2		192	160										

FPGA Multiplexer Resource Usage

M32_B8B1E_SB

8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			96										
Cyclone		160	160										
Cyclone2		160	160										
Cyclone3		160	160										
Stratix		160	160										
Stratix2		96	96										
Stratix3			96										
StratixGX		160	160										
Stratix2GX			96										
Max2		160	160										
Max3000a	64	64											
Max7000b	64	64											
Max7000ae	64	64											
Max7000s	64	64											
Spartan2		162	162										
Spartan2E		162	162										
Spartan3		162	162										
Spartan3A		160	160										
Spartan3ADSP		160	160										
Spartan3AN		160	160										
Spartan3E		162	162										
Spartan3L		162	162										
Virtex		162	162										
Virtex2		162	162										
Virtex2p		162	162										
VirtexE		162	162										
Virtex4		162	162										
Virtex5		128											
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		372											
ProAsic3		260											
ProAsic3E		260											
Fusion													
EC		192	160										
ECP		192	160										
ECP2		192	160										
ECP2M		192	160										
SC		192	160										
MACHXO		192	160										
XP		192	160										
XP2		192	160										

M32_B16B1

16x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		320	320										
Cyclone2		321	320										
Cyclone3		320	320										
Stratix		320	320										
Stratix2		160	160										
Stratix3			160										
StratixGX		321	320									2	
Stratix2GX			160										
Max2		320	320										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		292	291										
Spartan2E		292	291										
Spartan3		260	259										
Spartan3A		256	256										
Spartan3ADSP		256	256										
Spartan3AN		256	256										
Spartan3E		262	261										
Spartan3L		262	261										
Virtex		292	291										
Virtex2		260	259										
Virtex2p		260	259										
VirtexE		292	291										
Virtex4		260	259										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		712											
ProAsic3		482											
ProAsic3E		482											
Fusion													
EC		256	256										
ECP		256	256										
ECP2		256	256										
ECP2M		256	256										
SC		256	256										
MACHXO		256	256										
XP		256	256										
XP2		256	256										

FPGA Multiplexer Resource Usage

M32_B16B1_SB

16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		320	320										
Cyclone2		321	320										
Cyclone3		320	320										
Stratix		320	320										
Stratix2		160	160										
Stratix3			160										
StratixGX		321	320									2	
Stratix2GX			160										
Max2		320	320										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		292	291										
Spartan2E		292	291										
Spartan3		260	259										
Spartan3A		256	256										
Spartan3ADSP		256	256										
Spartan3AN		256	256										
Spartan3E		262	261										
Spartan3L		262	261										
Virtex		292	291										
Virtex2		260	259										
Virtex2p		260	259										
VirtexE		292	291										
Virtex4		260	259										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		712											
ProAsic3		482											
ProAsic3E		482											
Fusion													
EC		256	256										
ECP		256	256										
ECP2		256	256										
ECP2M		256	256										
SC		256	256										
MACHXO		256	256										
XP		256	256										
XP2		256	256										

M32_B16B1E

16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160										
Cyclone		352	352										
Cyclone2		353	352										
Cyclone3		352	352										
Stratix		352	352										
Stratix2		160	160										
Stratix3			160										
StratixGX		353	352									2	
Stratix2GX			192										
Max2		352	352										
Max3000a	128	128											
Max7000b	128	128											
Max7000ae	128	128											
Max7000s	128	128											
Spartan2		294	292										
Spartan2E		294	292										
Spartan3		294	292										
Spartan3A		288	288										
Spartan3ADSP		288	288										
Spartan3AN		288	288										
Spartan3E		294	292										
Spartan3L		294	293										
Virtex		294	292										
Virtex2		294	292										
Virtex2p		294	292										
VirtexE		294	292										
Virtex4		294	292										
Virtex5													
CoolRunner2													
CoolRunnerXpla3													
Xc9500													
Xc9500XL													
Xc9500XV													
ProAsicPlus		712											
ProAsic3		516											
ProAsic3E		516											
Fusion													
EC		320	288										
ECP		320	288										
ECP2		320	288										
ECP2M		320	288										
SC		320	288										
MACHXO		320	288										
XP		320	288										
XP2		320	288										

FPGA Multiplexer Resource Usage

M32_B16B1E_SB

16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable and Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (8Kx18)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Arriagx			160									
Cyclone		352	352									
Cyclone2		353	352									
Cyclone3		352	352									
Stratix		352	352									
Stratix2		160	160									
Stratix3			160									
StratixGX		353	352								2	
Stratix2GX			192									
Max2		352	352									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		294	292									
Spartan2E		294	292									
Spartan3		294	292									
Spartan3A		288	288									
Spartan3ADSP		288	288									
Spartan3AN		288	288									
Spartan3E		294	292									
Spartan3L		294	293									
Virtex		294	292									
Virtex2		294	292									
Virtex2p		294	292									
VirtexE		294	292									
Virtex4		294	292									
Virtex5												
CoolRunner2												
CoolRunnerXpla3												
Xc9500												
Xc9500XL												
Xc9500XV												
ProAsicPlus		712										
ProAsic3		516										
ProAsic3E		516										
Fusion												
EC		320	288									
ECP		320	288									
ECP2		320	288									
ECP2M		320	288									
SC		320	288									
MACHXO		320	288									
XP		320	288									
XP2		320	288									

Tools Utilized

The following vendor device tools were used to determine the resource usage statistics:

Actel

Actel Designer Software Version 6.2

Altera

Quartus II 5.0

Lattice

ispLEVER 5.0

Xilinx

Xilinx ISE 6.3

For Virtex4, Spartan3, Spartan3E the Xilinx ISE 7.1 was used.

Revision History

Date	Version No.	Revision
6-Dec-2004	1.0	Service pack 2 release
12-Apr-2005	1.01	Added Virtex4 and Stratix2 resource usage
16-May-2005	1.02	Regenerated resource usage statistics to reflect the corrections to De-Multiplexers
6-Jun-2005	1.03	Added MAX2 resource usage
15-Sep-2005	1.04	Added EC, ECP, Spartan3E, Cyclone2 and StratixGX resource usage
13-Oct-2005	1.05	Added ProAsic3 and ProAsic3E resource usage
20-Apr-2006	1.06	Tools Utilized section added
16-Jun-2006	1.07	XP resource usage added
28-Jul-2006	1.08	MACHXO resource usage added
10-Apr-2007	1.09	Cyclone3, ECP2, ECP2M, Spartan3A, Spartan3E, Spartan3L and Virtex5 resource usage added
17-Jul-2008	1.10	Altium Designer Summer 08 SP1
19-Dec-2008	1.11	Altium Designer Winter 09 SP1

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